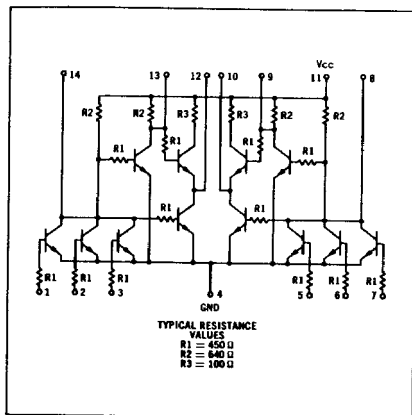


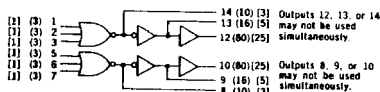
DUAL 3-INPUT BUFFERS NON-INVERTING

PLASTIC mW MRTL MC700P/800P series

MC788P • MC888P



Two 3-input positive logic NOR gates, each followed by an inverting and non-inverting high fan-out amplifier, are provided in a single package. For each section, the output from each stage is available. If more than one output is used, the full loading factors cannot be employed since each output provides the drive for the succeeding stage.

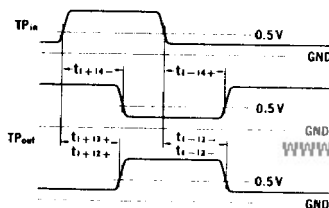
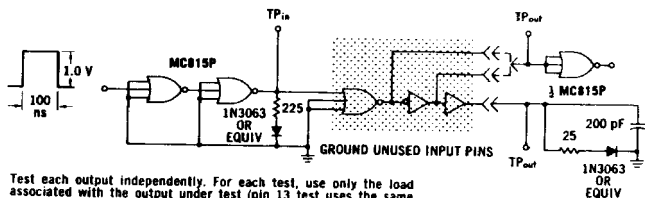


$$14 = 1 + 2 + 3 \quad 13 = 1 + 2 + 3 \quad 12 = 1 + 2 + 3$$

NUMBER IN PARENTHESES INDICATES LOADING FACTOR FOR mW MRTL
 NUMBER IN BRACKETS INDICATES LOADING FACTOR FOR MRTL

$t_{pd} = 24 \text{ ns}$
 $P_d = 145 \text{ mW (Input Low)}$
 $56 \text{ mW (Inputs Low)}$

SWITCHING TIMES TEST CIRCUIT AND WAVEFORMS



The other buffer is tested in the same manner.

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Ground input pins of buffer not under test. Other pins not listed are left open. *Resistor value to V_{CC} .