

MC1699

BEING DISCONTINUED (LIFETIME BUY UNTIL JUNE 14, 1989)

DIVIDE-BY-FOUR GIGAHERTZ COUNTER

The MC1699 is a divide-by-four gigahertz counter. The clock input requires an ac coupled driving signal of 800 mV amplitude (typical). The clock toggles two divide-by-two stages, and the complementary outputs (50% duty cycle) are taken from the second stage.

The MC1699 includes clock enable and reset. The reset is compatible with MECL III voltage levels. The enable input requires a VIL of -2.0 V max. Reset operates only when either the clock or the enable is high.

Pin 11 (13) is available for connection of a decoupling capacitor www.Data heet to ground. This capacitor stabilizes the reference point which is internally coupled to the clock input.

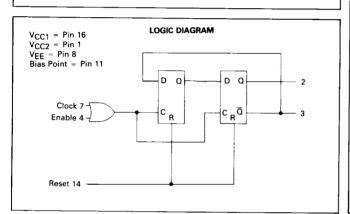
DIVIDE-BY-FOUR **GIGAHERTZ COUNTER**

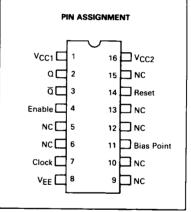


L SUFFIX CERAMIC PACKAGE **CASE 620**

PLASTIC PACKAGE **CASE 648**







ELECTRICAL CHARACTERISTICS

	l i	- 3	o°C	+2	25°C	+ 8	5°C			
Characteristic	Symbol	Min	Max	Min	Max	Min	Max	Unit	Conditions	
Power Supply Drain Current	ΙE	-	_	_	57	_	_	mAdc	All inputs and outputs open except Clock = V _{IHC} ≅ −4.0 Vde	
Input Current Reset Enable	linH	-	- -	_	500 265	_	_	μAdc	VIHmax to Reset, VIL to Enable, VEE to Clock. VILmin to Reset, VIHmax to Enable, VEE to Clock.	
Logic "1" Output Voltage	Voн	~ 1.085	- 0.875	- 1.0	~0.81	- 0.93	- 0.7	Vdc	See Note 2. Or, apply P1 to Rese	
Logic "0" Output Voltage	VOL	_	1.63	_	- 1.6	_	- 1.555	Vdc	and V _{IHmax} to Enable (See Test Conditions below).	
Toggle Frequency (high frequency operation)	fTog	1.0	_	1.0	_	1.0	_	GHz	V _{IL} ¹ to Enable.	
Toggle Frequency (low frequency sine wave input)	fTog	_	_	_	100	_	_	MHz	See Test Circuit and Applicati Information.	

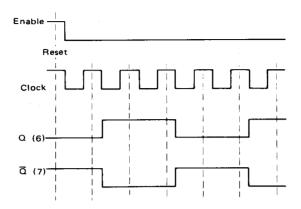
V_{IHmax} VILmin VIHmax

Test Conditions: $V_{CC1} = V_{CC2} = 0$, $V_{EE} = 7.0 \text{ Vdc}$

Enable input requires $V_{IL} = -2.0$ V max. Reset counter by applying pulse P1 to pin 14, then toggle outputs by applying pulse P2 to pin 4 for 2 cycles. Hold power during pulse sequence. Hold clock input α VEE.

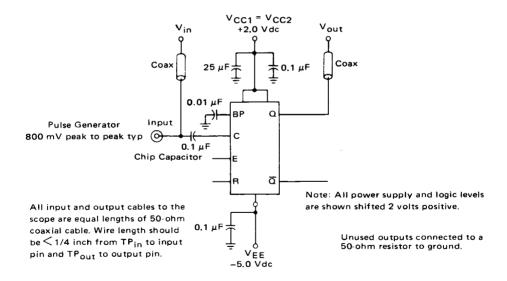
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TIMING DIAGRAM



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TOGGLE FREQUENCY TEST CIRCUIT



APPLICATION INFORMATION

The MC1699 is a very high speed divide-by-four counter intended for prescaler applications. The reset provides increased flexibility for counter and time measuring requirements.

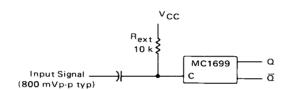
The clock input is designed to accept a capacitorcoupled sine wave signal for frequencies above 100 MHz. Below 100 MHz waveshaping is recommended to obtain good MECL III or MECL 10,000 edge speeds.

With a continuous input signal the clock can be capac-

itor-coupled with no problems. However, if the clock is interrupted and the clock input floats to the bias point reference voltage, the counter may oscillate. To prevent this oscillation, an external resistor can be added as shown in Figure 1. This resistor is recommended only when the clock is interrupted and serves no useful function with a continuous signal. Also, this external resistor is not required when the enable input is used to gate the clock signal.

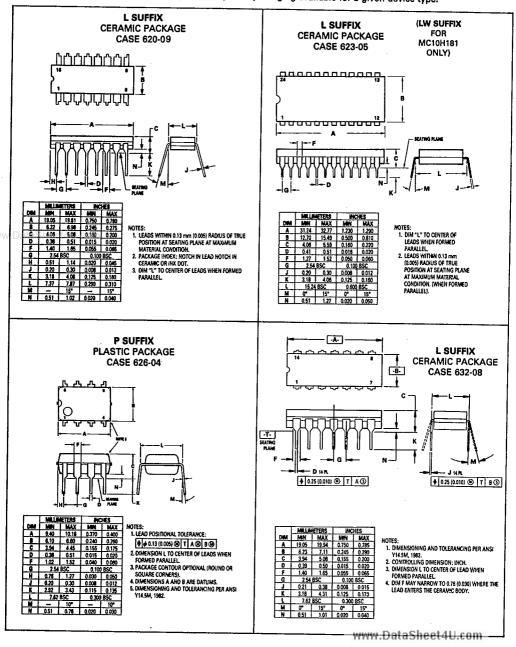
FIGURE 1

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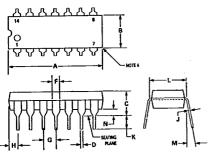


PACKAGE OUTLINE DIMENSIONS

A letter suffix to the MECL logic function part number is used to specify the package style (see drawings below). See appropriate selector guide for specific packaging available for a given device type.







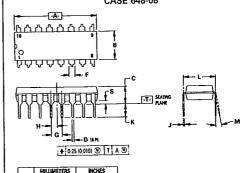
	MILLIA	ETERS	INC	IES
DMA	MIN	HAX	MIN	MAX
A	18.16	1956	0.715	0.770
	6.10	660	0.240	0 250
C	3.69	4.69	0.145	0.185
0	0.38	0.53	0.015	0.021
F	1.02	1.78_	0.040	0.079
G	2.54	BSC	0.100 BSC	
H	132	241	0.052	0.095
J	0.20	0.38	0.008	0.015
K.	2.92	3.43	0.115	0.135
Яb	€ 7.62 85C . C		0.300	
м	G ₃	10°	0°	10°
N	0.39	1.01	0 015	0.039

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NOTES:

- 1. LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM
- MATERIAL CONDITION. 2 DIMENSION "L" TO CENTER OF LEADS WHEN FOR WED PARALLEL.
- 3. DIMENSION "B" DOES NOT INCLUDE MOLD FLASH.
- 4. ROUNDED CORNERS OPTIONAL.

P SUFFIX PLASTIC PACKAGE CASE 648-08



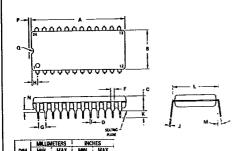
			RICIRCO		
DIM:	MIN	MAX	¥	MAX	
A	18.80	19.55	0.740	0.770	
B	6.35	6.85	0.250	0.270	
C	3.69	4.44	0.145	0.175	
D	0.39	0.53	0.015	0.021	
F	1.02	1.77	0.040	0.070	
G	2.54	BSC	0.100 BSC		
H	1.27	BSC	0.050	BSC	
7	0.21	0.38	0.008	0.015	
K	2.80	3.30	0.110	0.130	
L	7.50	7.74	0.295	0.305	
M	0°	10°	0°3	t0°	
S	0.51	1.01	0.020	0.040	

NOTES.

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: LNCH. 3. DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL
- 4. OHMENSION "B" DOES NOT INCLUDE MOLD FLASH
- 5. ROUNDED CORNERS OPTIONAL

P SUFFIX PLASTIC PACKAGE CASE 649-03

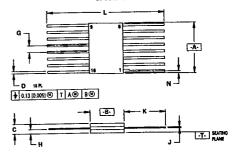
(PW SUFFIX **FOR MC10H181** ONLY)



UMM	MAKE 1	MAA.	1000	1000	
٨	31 50	32.13	1 240	1 265	
В	13.21	13.72	0.520	0.540	
C	4.70	5.25	0.185	0 205	
D	0.38	0.51	0.015	0 0 2 0	
F	1.02	1 52	0.040	0.060	
G	2.54	BSC	0.100 BSC		
H	1 65	2.16	0.065	0 085	
J	0.20	0.30	0.008	0.012	
K	292	3.43	0.115	0.135	
L	14 99	15.49	0.590	0.610	
M	_	10°	1	10°	
N	0.51	1.02	0 029	0.040	
	0.13	0.38	0 005	0.015	
a	0.51	0.76	0.020	0 030	

- 1. LEADS WITHIN 0.13 mm (0.005) RADIUS OF TRUE POSITION AT SEATING PLANE AT MAXIMUM MATERIAL CONDITION
- 2 DIMENSION 'L' TO CENTER OF LEADS WHEN FORWED PARALLEL.

F SUFFIX CERAMIC PACKAGE CASE 650-05

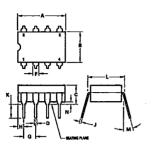


i I		MILLIN	ETERS]	INC	NO.	
	DM	MIN	HAX	MAN	MAX	1.
į	A	9.40	9.90_	0.370	0.390	
	8	6.23	6.60	0.245	0.260	2
	C	1,53	2.15	0.060	0.085	3
	0	0.36	0.48	0.014	0.019	
	G	1.27	BSC	0.050	BSC	4
	Н	0.64	0.01	0.025	0.040	ĺ
	1	0.11	0.17	0.004	0.007	5
	K	6.35	9.39	0.250	0.370	1
	1	18.93	-	0.745		6
	N	_	0.50	- 10	110.0201	D2

NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1582.
- 2. CONTROLLING DIMENSION: INCH. 3. DEMENSION "A" AND "B" ALLOW FOR LID
- MISALIGNMENT, AND GLASS MENISCUS.
- 4. DIMENSION "H" SHALL BE MEASURED AT THE POINT OF EXIT OF THE LEAD FROM THE BODY. 5. LEAD NUMBER 1 IDENTIFIED BY TAB ON LEAD OR DOT ON COVER.
- 6. DIMENSION "J" LYCLUDES SOLDER LEAD FINISH. atasheese wayyiron serenence only.

L SUFFIX CERAMIC PACKAGE CASE 693-02



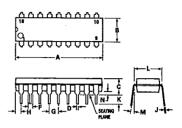
1	IMPLUMICIENS		munes .		
DIM	MM	MAX	MIN	MAX	
A	9.91	10.92	0.390	0.430	
1	6.22	5.99_	0.245	0.275	
C	4.32	5.08	0.170	0.200	
D	0.41	0.51	0.016	0.020	
F	1.40	1.65	0.055	0.065	
G	2.54	8SC	0.100 BSC		
H	1.14	1.65	0.045	0.065	
J	0.20	0.30	0.006	0.012	
K	3.18	4.06	0.125	0.160	
	7.37	7.97	0.290	0.310	
DING		150	<u> </u>	15"	
	0.51	1.02	0.020	0.040	

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- 1. LEADS WITHIN 0.13 mm (0.005) RAD OF TRUE POSITION AT SEATING PLANE AT MAXIMUM
- MATERIAL CONDITION.

 2. DIMENSION "L" TO CENTER OF LEADS WHEN FORMED PARALLEL.

P SUFFIX PLASTIC PACKAGE CASE 707-02

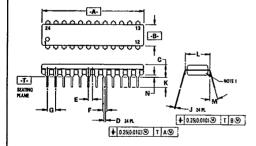


		KIKNA	MUNES		
DIM	MIN	MAX	MIN	MAX	
A.	22.22	23.24	0.875	0.915	
В	6.10	6.60	0.240	0.260	
C	3.56	4.57	0.140	0.180	
D	0.36	0.56	0.014	0.022	
F	1.27	1,78	0.050	0.070	
G	2.54	BSC	0.100 BSC		
	1.02	1.52	0.040	0.060	
J	0.20	0.30	0.008	0.012	
K	2.92	3.43	0.115	0.135	
	_ 7.62 8SC		0.300	BSC	
M	ઢ	15°	8	15°	
N	0.51	1.02	0.020	0.040	

NOTES:

- 1. POSITIONAL TOLERANCE OF LEADS (D), SHALL BE WITHEN 0.25mm(0.010) AT MAXIMUM MATERIAL CONDITION, IN RELATION TO SEATING PLANE AND EACH OTHER.
- 2. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL
 - 3. DIMENSION 8 DOES NOT INCLUDE MOLD FLASH.

P SUFFIX PLASTIC PACKAGE **CASE 724-03**



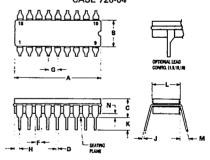
	MALLMETERS		MURES	
DIFFE	MAN	MAX.	MIH	MAX
4	31.25	32.13	1.230	1.265
8	6.35	6.85	0.250	0.270
C	3.69	4.44	0.145	0.175
0	0.38	0.51	0.015	0.020
E	1.27	8SC	005	BSC
F	1.02	1.52	0.040	0.060
G	2.54	BSC	0.100 BSC	
J	0.18	0.30	0.007	0.012
ĸ	2.80	3.55	0.110	0,140
L	7.62 8SC		0.300	BSC .
M	9	15°	c	15°
×	0.51	1.01	0.020	.0.040

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NOTES:

- 1. CHAMFERRED CONTOUR OPTIONAL
- 2. DIM "L" TO CENTER OF LEADS WHEN FORMED PARALLEL
- 3. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M, 1982.
- 4. CONTROLLING DIMENSION: INCH.

L SUFFIX CERAMIC PACKAGE CASE 726-04



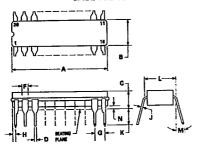
$\overline{}$	MILLIM	ETERS	INC	HES
DHA	MAN	MAX	MIN	MAX
A	22.35	23.11	0.880	0.910
В	6.10	7.49	0.240	0.295
c		5.08	ļ	0.200
0	0.38	0.53	0.015	_0.021
E	1.40	1.78	0.055	0.070
G	2,54	BSC	0.100	BSC
H	0.51	1.14	0.020	0.045
1	0.20	0.30	0.008	0.012
. X	3.18	4.32	0.125	0.170
_	7.62 BSC		0.300	BSC
M	Ş	15°	6°	15°
N	0.51	1.02	0.020 .	0.040.

NOTES:

- 1. LEADS, TRUE POSITIONED WITHEN 0.25 mm (0.010) DIA. AT SEATING PLANE, AT MAXIMUM MATERIAL CONDITION.
- 2. DIM "L" TO CENTER OF LEADS WHEN FORMED
- PARALLEL.
- 3. DIM "A" & "B" INCLUDES MENISCUS.
- 4. "F" DIMENSION IS FOR FULL LEADS. "HALF" LEADS ARE OPTIONAL AT LEAD POSITIONS 1. 9. 10, AND 18.

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L SUFFIX **CERAMIC PACKAGE CASE 732-03**

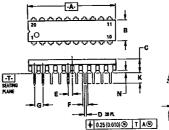


		MILLER	ETERS	INC	HES
	DIM	HAN	MAX	MH	HAX
1	A	23.88	25.15	0.940	0.990
1	1	6.60	7.49	0.260	0.295
1	C	3.81	5.08	0.150_	0.200
	Б	0.38	0.56	0.015	0.022
	F	1.40	1.65	0.055	0.065
į.	G	2.54	BSC	0.100	BSC
1	H	0.51	1.27	0.020	0.050
1	1	0.20	0.30	0.008	0.012
	K	3.18	4.06	0.125	0.160
	Ti.	7.62	BSC	0.300	BSC
_ i	M	0"	15°	O°	15°
www.Data	5 NO	0.25	1.02	0.013	0.040

NOTES:

- 1. LEADS WITHIN 0.25 mm (0.010) DIA_TRUE POSITION AT SEATING PLANE, AT MAXIMUM MATERIAL CONDITION.
- 2. DAY L TO CENTER OF LEADS WHEN FORMED PARALLEL
- 3. DIM A AND B INCLUDES MENISCUS.

P SUFFIX PLASTIC PACKAGE CASE 738-03



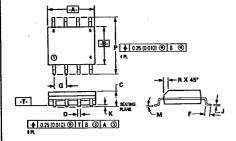
	1 1
	Jan
]	♦ 0.25 (0.010) ® T B®

	MILLIM	ETERS	INC	HES			
DM	MIN	MAX	MIN	MAX			
A	25.66	27.17	1.010	1.070			
В	6.10	6.60	0.240	0.260			
Ç	3.81	4.57	0.150	0.180			
В	0.39	0.55	0.015	0.022			
E	1.27	BSC	0.050 BSC				
F	1.27	1,77	0.050	0.070			
G	2.54	BSC	0.100 BSC				
7	0.21	0.38	0.008	0.015			
K	2.80	3.55	0.110	0.140			
ī	7.62 BSC		0.300	BSC			
М	0°	15°.	O'	15°			
N	0.51	1.01	0.020	0.040			

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- 3. DIMENSION "L" TO CENTER OF LEAD WHEN FORMED PARALLEL.
- 4. DIMENSION "B" DOES NOT INCLUDE MOLD FLASH,

D SUFFIX PLASTIC SOIC PACKAGE CASE 751-03



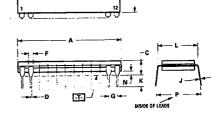
	MILIM	ETERS	INCHES						
DM4	MAN	MAX	W	MAX					
A	4.80	5.00	0.189	0,196					
8	3.80	4.00	0,150	0.157					
C	1.35	1.75	0.054	0.068					
D	0.35	0.49	0.014	0.019					
F	0,40	1.25	0.016	0.049					
G.	1.27	BSC	0.050 BSC						
1	0.18	0.25_	0.007	0.009					
K	0.10_	0.25	0.004	0.009					
M	8	r.	0-						
P	5.80	6.20	0.229	0.244					
R	0.25	0.50	0.610	0.019					

- 1. DIMENSIONS "A" AND "B" ARE DATUMS AND "T" IS A DATUM SURFACE.
- 2. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 - 3. CONTROLLING D.M: MILLIMETER.
 - 4. DIMENSION "A" AND "B" DO NOT INCLUDE MOLD PROTRUSION
 - 5. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.

L SUFFIX



R

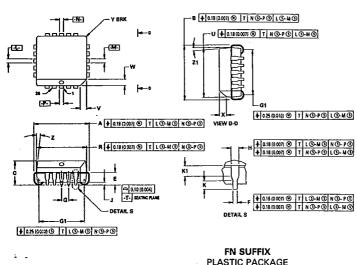


DIM	MIN	MAX	MIN	MAX					
٨	31.50	32.64	1.240	1.285					
8	7.24	7.75	0.285	0.305					
C	3.68	4.44	0.145_	0.175					
0	0.38	0.53	0 015	0.021					
F	1.14	1.57	0.045	0.062					
G	2.54	BSC	0.100	asc					
J	0.20	0.33	0.008	0.013					
K	2.54	4.19	0.100	0.165					
ı	7.62	7.87	0.300	0.310_					
N	0.51	1.27	0.020	0.650					

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INCHES

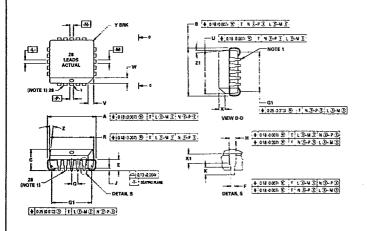
- 1. DIMENSION A IS DATUM.
- 2. POSITIONAL TOLERANCE FOR LEADS: 24 PLACES
- ♦ 0.25 (0.010) T A 3
- 3. IT IS SEATING PLANE.
- 4. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL
- 5. DIMENSIONING AND TOLERANCING PER ANSI Y14.5, 1973.
- 9 9.14 10.16 0.360 WWW.DataSheet4U.com



NOTES:

- 1. OATUMS -L-, -M-, -N-, AND -P- DETERMINED WHERE TOP OF LEAD SHOULDER EXIT PLASTIC BODY AT MOLD PARTING LINE.
- DIM GI, TRUE POSTION TO BE MEASURED AT DATUM •T•, SEATING PLANE.
- DIM R AND U DO NOT INCLUDE MOLD PROTRUSION. ALLOWABLE MOLD PROTRUSION IS 0.25 (0.010) PER SIDE.
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 5. CONTROLLING DIMENSION: INCH.

	MILLIN	ETERS	INC	HES				
DIM	MIN	MAX	MIN	MAX				
A	9.78	10.03	_0.385	0.395				
8	9.78	10.03	0.385	0.395				
С	4.20	4.57	0.165	0.180				
E	2.29	2.79	0.090	0.110				
F	0.33	0.48	0.013	0.019				
G	1.27	BSC	0.050					
Ξ	0.66	0.81	0.026	0.032				
J	0.51		0.020	_				
K	0.64	_	0.025	_				
R	8.89	9.04	0.350	0.356				
U	8.89	9.04	0.350	0.356				
٧	1.07	1.21	0.042	0.048				
M	1.07	1.21	0.042	0.048				
X	1.07	1.42	0.042	0,056				
Y.	_	0.50		0.020				
2	2°	10°	2°	10°				
G1	7.88	8.38	0.310	0.330				
K1	1.02		0.040	_				
Ž1	2°	10°_	_2°	10°				



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FN SUFFIX PLASTIC PACKAGE CASE 776-02

CASE 775-02

OTES:

- 1. DUE TO SPACE LIMITATION, CASE 776-02 SHALL BE REPRESENTED BY A GENERAL (SMALLER) CASE OUTLINE DRAWING RATHER THAN SHOWING ALL 28 LEADS.
- 2. DATUMS -L. -M.- N.- AND -P. DETERMINED WHERE TOP OF LEAD SHOULDER EXIT PLASTIC BODY AT MOLD PARTING LINE.
- BODY AT MOLD PARTING LINE.

 3. DIM G1, TRUE POSITION TO BE MEASURED AT DATUM -T- SEATING PLANE.
- 4. DIM R AND U DO NOT INCLUDE MOLD PROTRUSION. ALLOWABLE MOLD PROTRUSION IS 0.25 (0.019) PER SIDE.
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

6. CONTROLLING DIMENSION: INCH.

	MOLLUM	HETERS _	INCHES ,						
OM	MIN	MAX	MIN	MAX					
A	12.32	12.57	0.485	0 435					
8	12.32	12 57	0.485	0.435					
Ç	4.20	4.57	0.165	0.180					
E	2.29	2.79	2.79 0 090						
F	033	0.48	0.013	0.019					
G	1.27	BSC	0.050	BSC					
Н	0.66	0.81	0.026	0.032					
-	0.51	-	0.020	-					
K	0.64	-	0 025	- 1					
R	11.43	11.58	0.450	0.456					
U	11.43	11.58	0.450	0.456					
V	1.07	1.21	0.042	0.048					
W	1.07	1.21	0.042	0.048					
X	1.07	1.42	0.042	0.056					
Y	1	0.50	_	0.020					
Z	r	102	2°	10"					
G1_	10.42	10.92	0.410	0.430					
Kī	1.02	-	0.040	-					
Zî	-2°	102	2°	10°					

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MECL Logic Surface Mount

WHY SURFACE MOUNT?

Surface Mount Technology is now being utilized to offer answers to many problems that have been created in the use of insertion technology.

Limitations have been reached with insertion packages and PC board technology. Surface Mount Technology offers the opportunity to continue to advance the State-of-the-Art designs that cannot be accomplished with Insertion Technology.

Surface Mount Packages allow more optimum device performance with the smaller Surface Mount configuration. Internal lead lengths, parasitic capacitance and inductance that placed limitations on chip performance have been reduced.

The lower profile of Surface Mount Packages allows more boards to be utilized in a given amount of space. They are stacked closer together and utilize less total volume than insertion populated PC boards.

Printed circuit costs are lowered with the reduction of the number of board layers required. The elimination or reduction of the number of plated through holes in the board, contribute significantly to lower PC board prices.

Surface Mount assembly does not require the preparation of components that are common on insertion technology lines. Surface Mount components are sent directly to the assembly line, eliminating an intermediate step.

Automatic placement equipment is available that can place Surface Mount components at the rate of a few thousand per hour to hundreds of thousands of components per hour.

Surface Mount Technology is cost effective, allowing the manufacturer the opportunity to produce smaller units and offer increased functions with the same size product.

MECL AVAILABILITY IN SURFACE MOUNT

Motorola is now offering MECL 10K and MECL 10KH in the PLCC (Plastic Leaded Chip Carrier) packages.

MECL in PLCC may be ordered in conventional plastic rails or on Tape and Reel. Refer to the Tape and Reel section for ordering details.

TAPE AND REEL

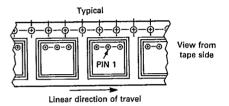
Motorola has now added the convenience of Tape and Reel packaging for our growing family of standard Integrated Circuit products. The packaging fully conforms to the latest EIA RS-481A specification. The antistatic embossed tape provides a secure cavity sealed with a peel-back cover tape.

GENERAL INFORMATION

Reel Size 13 inch (330 mm) Suffix: R2

Tape Width 16 mmUnits/Reel 1000

MECHANICAL POLARIZATION



ORDERING INFORMATION

- Minimum Lot Size/Device Type = 3000 Pieces.
- No Partial Reel Counts Available.
- To order devices which are to be delivered in Tape and Reel, add the appropriate suffix to the device number being ordered.

EXAMPLE:

ORDERING CODE

MC10100FN MC10100FNR2 MC10H100FN MC10H100FNR2

MC10H100FNI MC12015D MC12015DR2

SHIPMENT METHOD

Magazines (Rails)
13 inch Tape and Reel
Magazines (Rails)
13 inch Tape and Reel
Magazines (Rails)
13 inch Tape and Reel

DUAL-IN-LINE PACKAGE TO PLCC PIN CONVERSION DATA

The following tables give the equivalent I/O pinouts of Dual-In-Line (DIL) packages and Plastic Leaded Chip Carrier (PLCC) packages.

Conversion Tables

16 PIN 20 PIN	DIL	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16										
20 PIN	PLCC	2	3	4	5	7	8	9	10	12	13	14	15	17	18	19	20										
20 DIN	DII	1	ا و ا	2	ا <u>۱</u>	5	اءا	۱,	l a l	9	10	111	12	113	114	115	16	117	18	19 2	o						
20 PIN 20 PIN	PLCC	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19 2	0						
		•	•	•			-															l	1	.			
24 PIN	DIL	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	1494	Ŷ,	il'a	5	lé:	t41	U.C	om
24 PIN 28 PIN	PLCC	2	3	4	5	6	7	9	10	11	12	13	14	1 16	5 17	18	19	20	21	23 2	4 2	5 26	27	128			