Zibo Seno Electronic Engineering Co., Ltd.



MBR2040CP-MBR20200CP





20.0 A SCHOTTKY BARRIER DIODE

Features

- Schottky Barrier Chip
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V-O

Mechanical Data

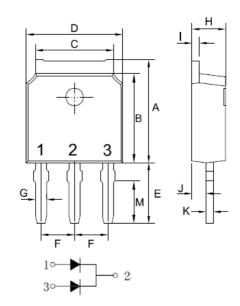
Case: TO-251/IPAK, Molded Plastic

 Terminals: Plated Leads Solderable per MIL-STD-202, Method 208

Polarity: See Diagram
Mounting Position: Any

• Lead Free: For RoHS / Lead Free Version

TO-251/IPAK



TO-251(IPAK)									
Unit:mm									
DIM	MIN	MAX							
Α	6.85	7.25							
В	5.90	6.30							
O	5.13	5.53							
D	6.40	6.80							
E	3.95	4.35							
F	2.19	2.39							
G	0.45	0.85							
Н	2.20	2.40							
_	0.41	0.61							
J	0.71	1.31							
K	0.41	0.61							
М	2.96	3.16							

Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	MBR 2040CP	MBR 2045CP	MBR 2050CP	MBR 2060CP	MBR 2080CP	MBR 20100CP		MBR 20200CP	Units
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	40	45	50	60	80	100	150	200	V
RMS Reverse Voltage	VR(RMS)	28	31	35	42	56	70	105	140	V
Average Rectified Output Current @T _L = 100°C (Note 1)	lo	20.0								Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	IFSM	100							А	
Forward Voltage @I _F = 10A	V FM	0.70		0.8	.80 0		0.85 0.9		12	V
	lкм	0.1 20								mA
Typical Junction Capacitance (Note 2)	Cj	350 280 200				pF				
Typical Thermal Resistance (Note 1)	$R_{ heta}JA$	15							°C/W	
Operating and Storage Temperature Range	Тj, Tsтg	-55 to +150 -55 to +175						°C		

Note: 1. Valid provided that leads are kept at ambient temperature at a distance of 9.5mm from the case.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

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RATING AND CHARACTERISTIC CURVES

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

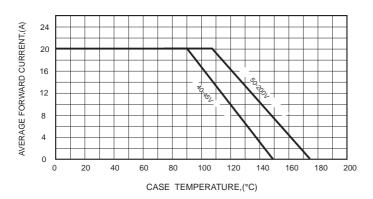


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

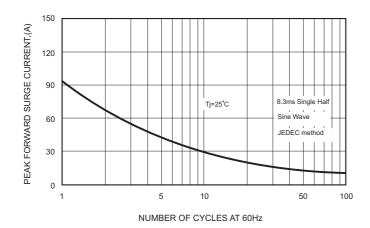


FIG.2-TYPICAL FORWARD

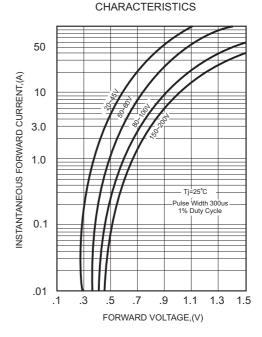


FIG.4 - TYPICAL REVERSE

