MOTOROLA SEMICONDUCTOR TECHNICAL DATA

Advance Information

Surface Mount **Schottky Power Rectifier** SOD–123 Power Surface Mount Package

The Schottky Power Rectifier employs the Schottky Barrier principle with a barrier metal that produces optimal forward voltage drop-reverse current tradeoff. Ideally suited for low voltage, high frequency rectification, or as a free wheeling and polarity protection diodes in surface mount applications where compact size and weight are critical to the system. This package provides an alternative to the leadless 34 MELF style package. These state-of-the-art devices have the following features:

- Guardring for Stress Protection
- Very Low Forward Voltage
- Epoxy Meets UL94, VO at 1/8"
- Package Designed for Optimal Automated Board Assembly

Mechanical Characteristics:

- Reel Options: 3,000 per 7 inch reel / 8 mm tape
- Reel Options: 10,000 per 13 inch reel / 8 mm tape
- Device Marking: B4
- · Polarity Designator: Cathode Band
- Weight: 11.7 mg (approximately)
- Case: Epoxy Molded
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C max. for 10 Seconds

MAXIMUM RATINGS

Rating DataSheet4U.com	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vr	40	V
Average Rectified Forward Current (At Rated V_R , T_C = 115°C)	IO	0.5	A
Peak Repetitive Forward Current (At Rated V _R , Square Wave, 20 kHz, T _C = 115°C)	IFRM	1.0	A
Non–Repetitive Peak Surge Current (Surge applied at rated load conditions, halfwave, single phase, 60 Hz)	^I FSM	5.5	A
Storage / Operating Case Temperature	T _{stg} , T _C	-55 to 150	°C
Operating Junction Temperature	ТJ	-55 to 150	°C
Voltage Rate of Change (Rated V_R , $T_J = 25^{\circ}C$)	dv/dt	1,000	V/μs

118 Thermal Resistance - Junction-to-Lead (2) R_{til} Thermal Resistance – Junction-to-Ambient (3) 206 R_{tja}

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (1)	٧ _F	Tj = 25°C	Tj = 100°C	V
$(I_{F} = 0.5 A)$ $(I_{F} = 1 A)$		0.51 0.62	0.46 0.61	
Maximum Instantaneous Reverse Current	۱ _R	Tj = 25°C	Tj = 100°C	μA
(V _R = 40 V) (V _R = 20 V)		20 10	5,000 13,000	

This document contains information on a new product. Specifications and information herein are subject to change without notice.

(1) Pulse Test: Pulse Width \leq 250 µs, Duty Cycle \leq 2.0%.

(2) Mounted with minimum recommended pad size, PC Board FR4.

(3) 1 inch square pad size (1 X 0.5 inch for each lead) on FR4 board.

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SCHOTTKY BARRIER RECTIFIER 0.5 AMPERES

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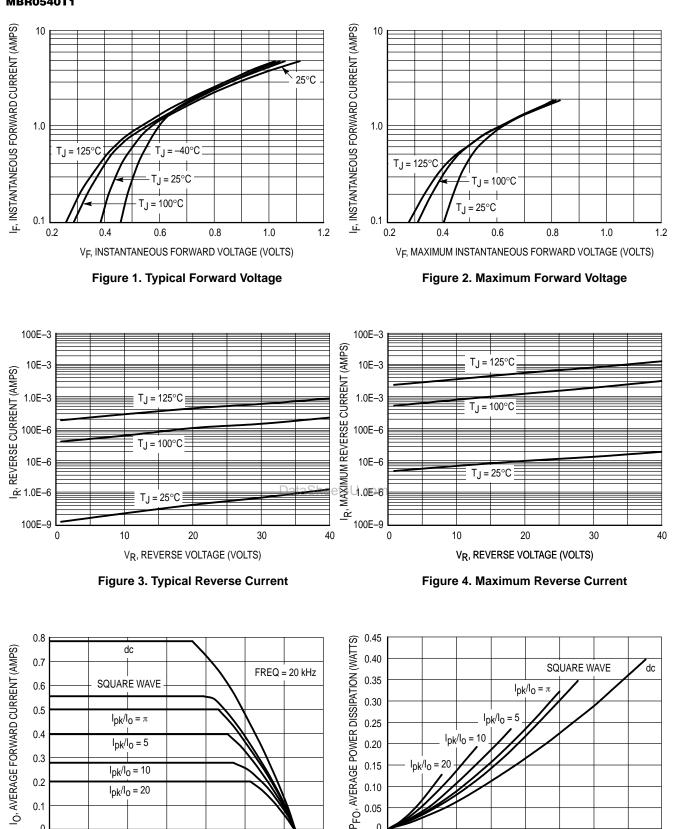
40 VOLTS



SOD-123



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0

0

0.1

0.2

0.3

TL, LEAD TEMPERATURE (°C) **Figure 5. Current Derating**

80

100

120

140

60

40

Figure 6. Forward Power Dissipation

0.4

IO, AVERAGE FORWARD CURRENT (AMPS)

0.5

0.6

0.7

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0.8

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0

0

20

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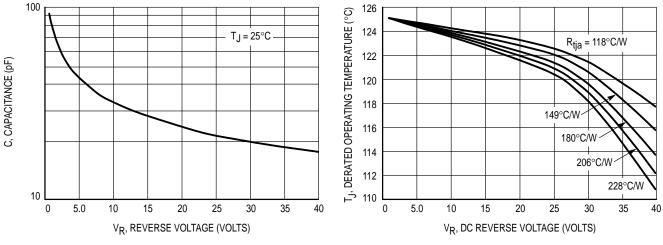


Figure 7. Capacitance

Figure 8. Typical Operating Temperature Derating*

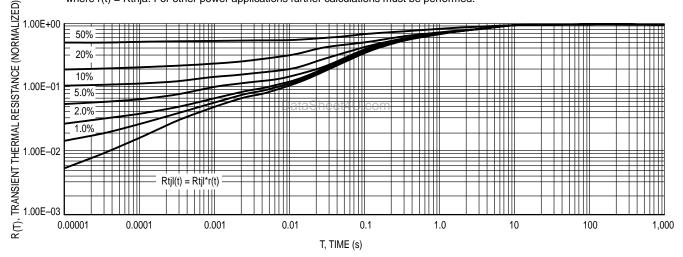
* Reverse power dissipation and the possibility of thermal runaway must be considered when operating this device under any reverse voltage conditions. Calculations of T_J therefore must include forward and reverse power effects. The allowable operating T_J may be calculated from the equation: $T_J = T_{Jmax} - r(t)(Pf + Pr)$ where

r(t) = thermal impedance under given conditions,

Pf = forward power dissipation, and

Pr = reverse power dissipation

This graph displays the derated allowable T_J due to reverse bias under DC conditions only and is calculated as $T_J = T_{Jmax} - r(t)Pr$, where r(t) = Rthja. For other power applications further calculations must be performed.





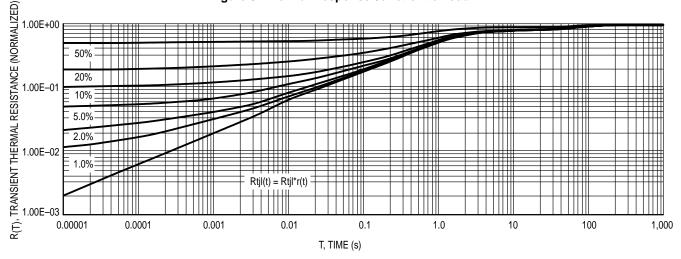


Figure 10. Thermal Response Junction to Ambient

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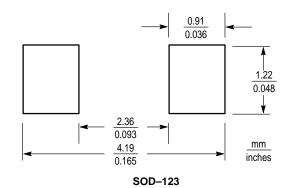
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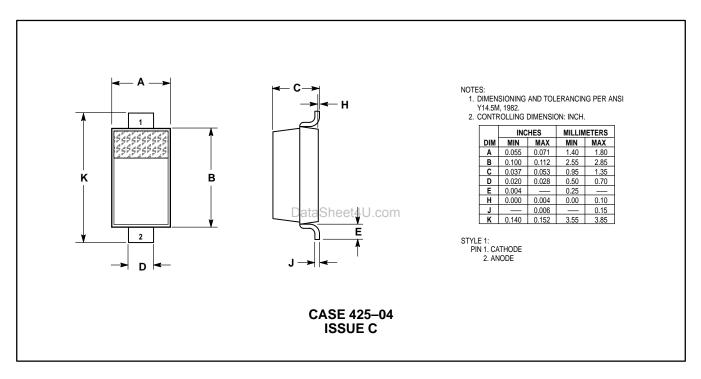
Rectifier Device Data

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RECOMMENDED FOOTPRINT FOR SOD-123



PACKAGE DIMENSIONS



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