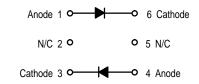
Dual Schottky Barrier Diodes

These Schottky barrier diodes are designed for high speed switching applications, circuit protection, and voltage clamping. Extremely low forward voltage reduces conduction loss. Miniature surface mount package is excellent for hand held and portable applications where space is limited.

- · Extremely Fast Switching Speed
- Low Forward Voltage 0.35 V @ IF = 10 mAdc



MBD54DWT1

Motorola Preferred Device

30 VOLTS
DUAL HOT-CARRIER
DETECTOR AND SWITCHING
DIODES



CASE 419B-01, STYLE 6 SOT-363

MAXIMUM RATINGS (T_J = 125°C unless otherwise noted)

Rating	Symbol	Value	Unit
Reverse Voltage	٧ _R	30	Volts
Forward Power Dissipation @ T _A = 25°C Derate above 25°C	PF	150 1.2	mW mW/°C
Forward Current (DC)	lF	200 Max	mA
Junction Temperature	TJ	125 Max	°C
Storage Temperature Range	T _{stg}	−55 to +150	°C

DEVICE MARKING

MBD54DWT1 = BL

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) (EACH DIODE)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Breakdown Voltage (I _R = 10 μA)	V _{(BR)R}	30	_	_	Volts
Total Capacitance (V _R = 1.0 V, f = 1.0 MHz)	C _T	_	7.6	10	pF
Reverse Leakage (V _R = 25 V)	I _R	_	0.5	2.0	μAdc
Forward Voltage (I _F = 0.1 mAdc)	V _F	_	0.22	0.24	Vdc
Forward Voltage (I _F = 30 mAdc)	VF	_	0.41	0.5	Vdc
Forward Voltage (I _F = 100 mAdc)	VF	_	0.52	1.0	Vdc
Reverse Recovery Time $(I_F = I_R = 10 \text{ mAdc}, I_{R(REC)} = 1.0 \text{ mAdc})$ Figure 1	t _{rr}	_	_	5.0	ns
Forward Voltage (I _F = 1.0 mAdc)	V _F	_	0.29	0.32	Vdc
Forward Voltage (I _F = 10 mAdc)	VF	_	0.35	0.40	Vdc
Forward Current (DC)	lF	_	_	200	mAdc
Repetitive Peak Forward Current	IFRM	_	_	300	mAdc
Non–Repetitive Peak Forward Current (t < 1.0 s)	IFSM	_	_	600	mAdc

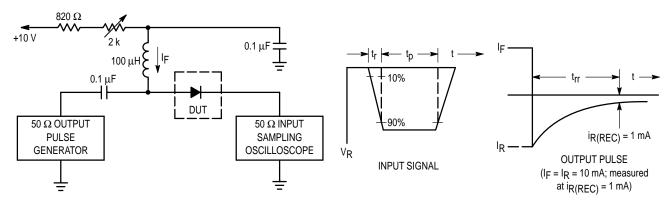
Preferred devices are Motorola recommended choices for future use and best overall value.

Thermal Clad is a registered trademark of the Bergquist Company.

REV 3



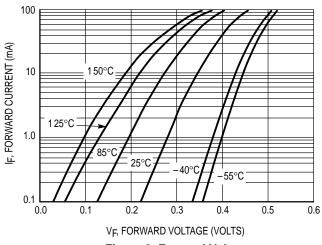
MBD54DWT1



Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (IF) of 10 mA.

- 2. Input pulse is adjusted so IR(peak) is equal to 10 mA.
- 3. $t_p * t_{rr}$

Figure 1. Recovery Time Equivalent Test Circuit



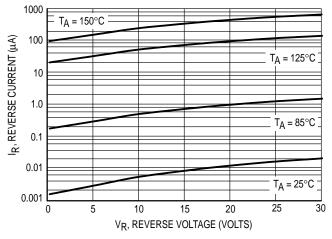


Figure 2. Forward Voltage

Figure 3. Leakage Current

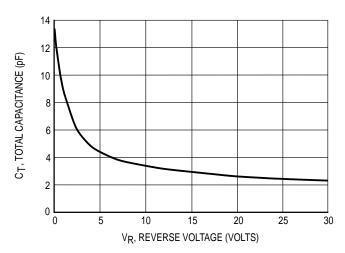


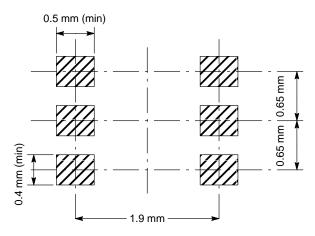
Figure 4. Total Capacitance

INFORMATION FOR USING THE SOT-363 SURFACE MOUNT PACKAGE

MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor packages must be the correct size to insure proper solder connection interface between the board and the package. With the correct pad geometry, the packages will self align when subjected to a solder reflow process.

SOT-363



SOT-363 POWER DISSIPATION

The power dissipation of the SOT–363 is a function of the pad size. This can vary from the minimum pad size for soldering to a pad size given for maximum power dissipation. Power dissipation for a surface mount device is determined by $T_{J(max)}$, the maximum rated junction temperature of the die, $R_{\theta JA}$, the thermal resistance from the device junction to ambient, and the operating temperature, T_A . Using the values provided on the data sheet for the SOT–363 package, P_D can be calculated as follows:

$$P_D = \frac{T_{J(max)} - T_{A}}{R_{\theta,JA}}$$

The values for the equation are found in the maximum ratings table on the data sheet. Substituting these values into the equation for an ambient temperature T_A of $25^{\circ}C$, one can calculate the power dissipation of the device which in this case is 150 milliwatts.

$$P_D = \frac{150^{\circ}C - 25^{\circ}C}{833^{\circ}C/W} = 150 \text{ milliwatts}$$

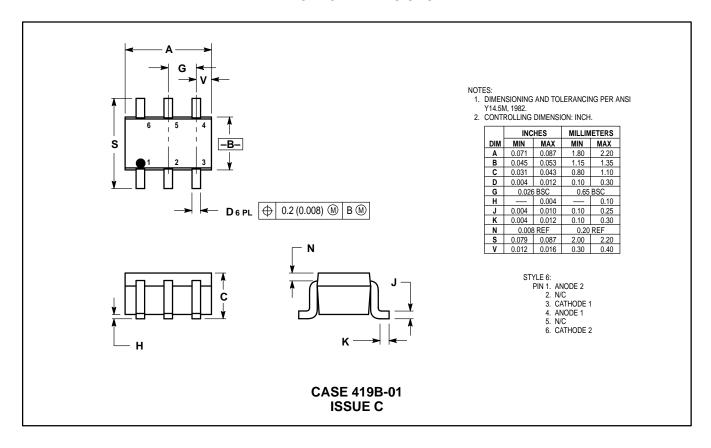
The 833°C/W for the SOT–363 package assumes the use of the recommended footprint on a glass epoxy printed circuit board to achieve a power dissipation of 150 milliwatts. There are other alternatives to achieving higher power dissipation from the SOT–363 package. Another alternative would be to use a ceramic substrate or an aluminum core board such as Thermal Clad™. Using a board material such as Thermal Clad, an aluminum core board, the power dissipation can be doubled using the same footprint.

SOLDERING PRECAUTIONS

The melting temperature of solder is higher than the rated temperature of the device. When the entire device is heated to a high temperature, failure to complete soldering within a short time could result in device failure. Therefore, the following items should always be observed in order to minimize the thermal stress to which the devices are subjected.

- Always preheat the device.
- The delta temperature between the preheat and soldering should be 100°C or less.*
- When preheating and soldering, the temperature of the leads and the case must not exceed the maximum temperature ratings as shown on the data sheet. When using infrared heating with the reflow soldering method, the difference shall be a maximum of 10°C.
- The soldering temperature and time shall not exceed 260°C for more than 10 seconds.
- When shifting from preheating to soldering, the maximum temperature gradient shall be 5°C or less.
- After soldering has been completed, the device should be allowed to cool naturally for at least three minutes.
 Gradual cooling should be used as the use of forced cooling will increase the temperature gradient and result in latent failure due to mechanical stress.
- Mechanical stress or shock should not be applied during cooling.
- * Soldering a device without preheating can cause excessive thermal shock and stress which can result in damage to the device.

PACKAGE DIMENSIONS



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