FUJITSU
MICROELECTRONICS

Sec- 8/67

MB8167A-45
MB8167A-55

NMOS 16,384 BIT STATIC RANDOM ACCESS MEMORY

ADVANCE INFORMATION

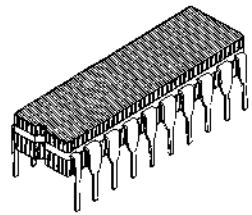
DESCRIPTION

The Fujitsu MB8167A is a 16,384 words by 1-bit static random access memory fabrication using N-channel silicon gate MOS technology. Separate Input/output pins are provided. All devices are fully compatible with TTL logic families in all respects: inputs, output and the use of a single +5V DC supply.

For ease of use, chip enable ($\overline{CE}$) permits the selection of an individual package when outputs are OR-tied, and automatically powers down the MB8167A. This device offers the advantages of low power dissipation, low cost, and high performance.

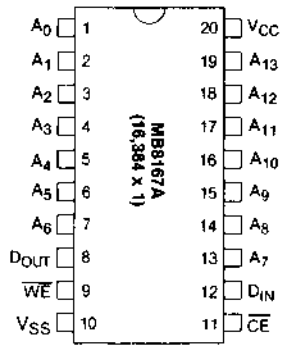
FEATURES

- **Organized as 16,384 words x 1 Bit**
- **Static operation: no clocks or refresh required**
- **Fast Access Time:**
  MB8167A-45: 45ns Max.
  MB8167A-55: 55ns Max.
- **Separate data input and output**
- **TTL compatible inputs and output**
- **Single +5V DC supply voltage**
- **Three-state output with OR-tie capability**
- **Chip enable for simplified memory expansion and automatic power down**
- **All inputs and output have protection against static charge**
- **Standard 20-pin DIP package**
- **Pin compatible with Intel 2167**

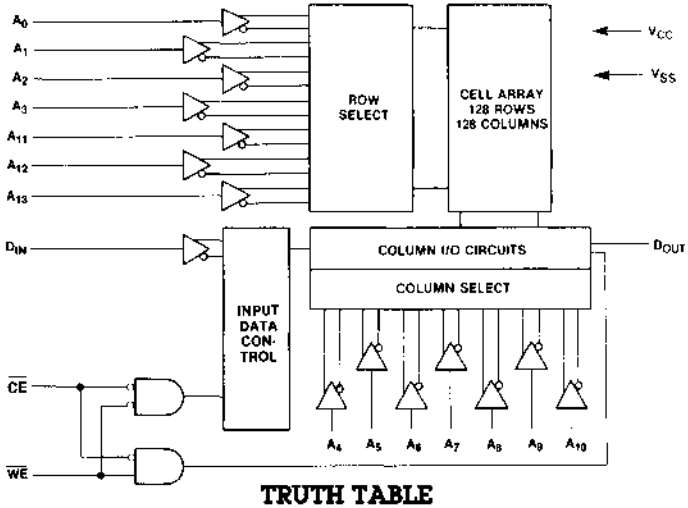


CERDIP PACKAGE
DIP-20C-C03

PIN ASSIGNMENT



MB8167A BLOCK DIAGRAM



TRUTH TABLE

$\overline{CE}$	$\overline{WE}$	MODE	OUTPUT	POWER
H	X	NOT SELECTED	HIGH Z	STANDBY
L	L	WRITE	HIGH Z	ACTIVE
L	H	READ	D_{OUT}	ACTIVE

This device contains circuitry to protect the inputs against damage due to high static voltages or electric fields. However, it is advised that normal precautions be taken to avoid application of any voltage higher than maximum rated voltages to this high impedance circuit.