

# **General Description**

The MAX7384 dual-speed silicon oscillator replaces ceramic resonators, crystals, and crystal-oscillator modules as the primary and secondary clock source for microcontrollers in 3V, 3.3V, and 5V applications. The MAX7384 features a factory-programmed high-speed oscillator, a 32.768kHz oscillator, a clock-selector input, and a µP power-on-reset (POR) supervisor. The clock output can be switched at any time between the high-speed clock and the 32.768kHz clock for low-power operation. Switchover is synchronized internally to provide glitch-free clock switching.

Unlike typical crystal and ceramic-resonator oscillator circuits, the MAX7384 is resistant to vibration and EMI.

The high-output-drive current and absence of highimpedance nodes makes the oscillator less susceptible to dirty or humid operating conditions. With a wide operating temperature range as standard, the MAX7384 is a good choice for demanding home appliance, industrial, and automotive environments.

The MAX7384 is available with factory-programmed frequencies ranging from 10MHz to 16MHz. See Table 1 for standard frequencies and contact the factory for custom frequencies. Refer to the MAX7378 data sheet for frequencies less than 10MHz.

The MAX7384 is available in an 8-pin  $\mu$ MAX<sup>®</sup> package. The MAX7384 standard operating temperature range is -40°C to +125°C. See the *Applications Information* section for extended operating temperature range.

## **Applications**

- White Goods
- Automotive
- Consumer Products
- Appliances and Controls
- Handheld Products
- Portable Equipment
- Microcontroller Systems

## \_\_\_\_ Features

- ♦ 2.7V to 5.5V Operation
- High-Speed Oscillator from 10MHz to 16MHz Low-Speed 32kHz Oscillator Glitch-Free Clock-Speed Switching
- Integrated POR
- ±10mA Clock-Output Drive Capability
- ♦ 2.5% Initial Accuracy
- ◆ ±50ppm/°C Frequency Drift
- ♦ 50% Duty Cycle
- ♦ 5ns Output Rise and Fall Time
- ♦ Low Jitter: 80psp-p at 16MHz
- ♦ 4mA Operating Current at 16MHz
- ♦ 13µA, 32kHz Operating Current
- ♦ -40°C to +125°C Temperature Range

## **Ordering Information**

		-	
PART*	TEMP RANGE	PIN- PACKAGE	RESET OUTPUT
MAX7384A	-40°C to +125°C	8 µMAX	Active high push-pull
MAX7384B	-40°C to +125°C	8 µMAX	Active low push-pull
MAX7384C	-40°C to +125°C	8 µMAX	Open drain

\*Standard version is shown in bold. The first letter after the part number designates the reset output option. Insert the letter corresponding to the desired reset threshold level from Table 1 in the next position. Insert the two-letter code from Table 2 in the remaining two positions for the desired frequency range. Table 1 and Table 2 are located at the end of the data sheet.

# **Typical Application Circuit**



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Pin Configuration appears at end of data sheet.

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# **MAX7384**

## **ABSOLUTE MAXIMUM RATINGS**

V <sub>CC</sub> to GND	0.3V to +6.0V
All Other Pins to GND	0.3V to (V <sub>CC</sub> + 0.3V)
CLOCK, RST Current	±50mÁ
Input Current (SPEED)	±50mA
Continuous Power Dissipation (TA :	= +70°C)
8-Pin µMAX (derate 4.5mW/°C at	bove +70°C)362mW (U8-1)

Operating Temperature Range	55°C to +135°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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# **ELECTRICAL CHARACTERISTICS**

(*Typical Operating Circuit*,  $V_{CC} = 2.7V$  to 5.5V,  $V_L = V_{CC}$ ,  $T_A = -40^{\circ}C$  to  $+125^{\circ}C$ . Typical values are at  $V_{CC} = 5.0V$ ,  $T_A = +25^{\circ}C$ , unless otherwise noted.) (Note 1)

PARAMETER	SYMBOL	. CONDITIONS MIN TYP MA		MAX	UNITS	
Operating Supply Voltage	V <sub>CC</sub>		2.7		5.5	V
	Icc	$f_{CLOCK} = 16MHz$ , $T_A = -40^{\circ}C$ to $+125^{\circ}C$ , no load		8.7		
		$f_{CLOCK} = 14.7456MHz$ , $T_A = -40^{\circ}C$ to $+125^{\circ}C$ , no load			8.0	
On evention of the output		$f_{CLOCK} = 12MHz$ , $T_A = -40^{\circ}C$ to $+125^{\circ}C$ , no load			6.5	mA
Operating Supply Current		$f_{CLOCK} = 11.0592MHz$ , $T_A = -40^{\circ}C$ to $+125^{\circ}C$ , no load			6.0	
		$f_{CLOCK} = 10MHz$ , $T_A = -40^{\circ}C$ to $+125^{\circ}C$ , no load			5.4	
		$f_{CLOCK} = 32.768$ kHz, $T_A = -40$ °C to +125°C, no load		13	25	μA
LOGIC INPUTS (SPEED)						
Input High Voltage	VIH		$0.7 \times V_{CC}$			V
Input Low Voltage	VIL			(	0.3 x Vcc	
Input Current	lin	$V_{CC} = SPEED = 5.5V$			2	μΑ
CLOCK OUTPUT						
Output High Voltage	VOH	$V_L = 4.5V$ , $I_{SOURCE} = 7.0mA$ $V_L = 2.7V$ , $I_{SOURCE} = 2.0mA$	V <sub>L</sub> - 0.4			V
Output Low Voltage	V <sub>OL</sub>	$V_L = 4.5V$ , $I_{SINK} = 20mA$ $V_L = 2.7V$ , $I_{SINK} = 10mA$	-		0.4	V
Initial Fast Clock Frequency	fclock	$V_{CC} = 5V$ , $T_A = +25^{\circ}C$ , deviation from selected frequency	-2.5 +2.5 -5 +3.5		+2.5	
Accuracy		$V_{CC}$ = 2.7V to 5.5V, $T_A$ = +25°C, deviation from selected frequency			70	
Fast Clock Temperature Coefficient		$T_A = -40^{\circ}C \text{ to } + 125^{\circ}C$		±50	±350	ppm/°C



## **ELECTRICAL CHARACTERISTICS (continued)**

(*Typical Operating Circuit*,  $V_{CC}$  = 2.7V to 5.5V,  $V_L$  =  $V_{CC}$ ,  $T_A$  = -40°C to +125°C. Typical values are at  $V_{CC}$  = 5.0V,  $T_A$  = +25°C, unless otherwise noted.) (Note 1)

	PARAMETER	SYMBOL	CONDITIONS		MIN	ТҮР	MAX	UNITS
		6	$V_{CC} = 5V, T_A = +25^{\circ}C$		32.268	32.768	33.268	
	Slow Clock Frequency	ICLOCK	$V_{CC} = 2.7V$ to 5.5V, $T_{A} =$	= +25°C	31.768	32.768	33.768	KHZ
	Slow Clock Temperature Coefficient		$T_{A} = -40^{\circ}C \text{ to } + 125^{\circ}C$			±50	±325	ppm/°C
	Duty Cycle		(Note 2)		40	50	60	%
www.	DataSheet4U.com Output Jitter		Observation of 16MHz for 20s using a 12GHz oscilloscope			80		psp-p
	Output Rise Time	t <sub>R</sub>	10% to 90%			5		ns
	Output Fall Time	t⊨	90% to 10%			5		ns
			.,	$T_A = +25^{\circ}C$	-2		+2	
	Power-On-Reset Threshold	VTH	from nominal threshold	T <sub>A</sub> = -40°C to +125°C	-5		+5	%
	Power-On-Reset Hysteresis	V <sub>HYST</sub>	Difference between rising and falling thresholds			1		%
	Power-On-Reset Delay	PORdly	$V_{CC}$ rising from 0 to 5V in 1µs at +25°C			122		μs
	RESET OUTPUT							
	Output High Voltage		$V_L = 4.5V$ , I <sub>SOURCE</sub> = 7.0mA		V <sub>CC</sub> - 0.4			
	(Note 3)	VOH	$V_L = 2.7V$ , $I_{SOURCE} = 2.0mA$		V <sub>CC</sub> - 0.4			V
		Mai	$V_{L} = 4.5V, I_{SINK} = 20mA$				0.4	V
	Output Low Voltage	VOL	$V_L = 2.7V$ , $I_{SOURCE} = 10$ mA				0.4	v

Note 1: All parameters tested at  $T_A = +25^{\circ}C$ . Specifications over temperature are guaranteed by design.

**Note 2:** Guaranteed by design. Not production tested.

Note 3: For push-pull output only.



# **Typical Operating Characteristics (continued)**

( $V_{CC} = V_L = 5V$ ,  $T_A = +25^{\circ}C$ , frequency = 10MHz, unless otherwise noted.)

**CLOCK OUTPUT WAVEFORM**  $(C_{L} = 100 pF)$ MAX7384 toc10



20ns/div

**HIGH-SPEED TO LOW-SPEED** TRANSITION



CHANNEL 1 = CLOCK, CHANNEL 2 = SPEED



LOW-SPEED TO HIGH-SPEED TRANSITION



CHANNEL 1 = CLOCK, CHANNEL 2 = SPEED

**SUPPLY CURRENT vs. FREQUENCY** 





**MAX7384** 



10µs/div CHANNEL 1 = CLOCK, CHANNEL 2 = SPEED

> LOW-SPEED TO HIGH-SPEED TRANSITION



CHANNEL 1 = CLOCK, CHANNEL 2 = SPEED

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**Pin Description** 

			Pin Description
2	PIN	NAME	FUNCTION
	1	V <sub>C</sub> C	Positive Supply Voltage. Bypass $V_{CC}$ to GND with a 0.1µF capacitor.
	2	VL	Output Supply Voltage. Bypass $V_L$ to GND with a 0.1 $\mu F$ capacitor. $V_L$ must be connected to $V_{CC}.$
	3	SPEED	Clock-Speed Select Input. Drive SPEED low to select the 32kHz fixed frequency. Drive SPEED high to select factory-trimmed frequency.
ĺ	4	RST	Reset Output. Open-drain or push-pull output. See the Ordering Information.
	5	CLOCK	Push-Pull Clock Output
w.İ	DataSheet4U.c	om GND	Ground
	7	E.C.	Externally Connected. Connect to Vcc.
	8	N.C.	No Connection. Not internally connected.

## **Detailed Description**

The MAX7384 is a dual-speed clock generator with integrated reset for microcontrollers (µCs) and UARTs in 3V, 3.3V, and 5V applications. The MAX7384 is a replacement for crystal-oscillator modules, crystals, or ceramic resonators and a system reset IC. The high-speed clock frequency and reset threshold voltage are factory trimmed to specific values (see Table 1). A variety of popular standard frequencies are available. The lowspeed clock frequency is fixed at 32.768kHz; see Table 2. No external components are required for setting or adjusting the frequency.

The push-pull clock output drives a load to within 400mV of either supply rail. The clock output remains stable over the full operating voltage range and does not generate short output cycles during either power-on or changing of the frequency. A typical oscillating startup is shown in the Typical Operating Characteristics.



Figure 1. Functional Diagram

#### Oscillator

#### **Clock-Speed Selection** Drive SPEED low to select slow clock speed (nominally 32.768kHz) or high to select full clock speed. SPEED can be connected to V<sub>CC</sub> or to GND to select fast or slow clock speed, or connected to a logic output (such as a µP GPIO port) used to change clock speed on the fly. If SPEED is connected to a µP GPIO port, connect a pullup or pulldown resistor to set the clock to the preferred speed on power-up. SPEED input bias current is 2µA (max), so a resistor value as high as $500k\Omega$ can be used.

## **Applications Information**

#### Interfacing to a Microcontroller Clock Input

The MAX7384 clock output is a push-pull, CMOS, logic output that directly drives a uP or uC clock input. There are no impedance-matching issues when using the MAX7384. Refer to the microcontroller data sheet for clock input compatibility with external clock signals. The MAX7384 requires no biasing components or load capacitance. When using the MAX7384 to retrofit a crystal oscillator, remove all biasing components from the oscillator input.

#### **Reset Output**

The MAX7384 is available with three reset output stage options: push-pull active low, push-pull active high, and open-drain active low. RST is asserted when the monitored input (V<sub>CC</sub>) drops below the internal V<sub>TH-</sub> threshold and remains asserted for 120µs after the monitored input exceeds the internal VTH+ threshold. The open-drain RST output requires an external pullup resistor.

#### **Output Jitter**

The MAX7384's jitter performance is given in the Electrical Characteristics table as a peak-to-peak value obtained by observing the output of the MAX7384 for



20s with a 12GHz oscilloscope. Jitter measurements are approximately proportional to the output period of the device. Thus, a 10MHz device has approximately twice the jitter value of a 20MHz device. The jitter performance of all clock sources degrades in the presence of mechanical and electrical interference. The MAX7384 is relatively immune to vibration, shock, and EMI influences and thus provides a considerably more robust clock source than crystal or ceramic resonatorbased oscillator circuits.

#### www.DataSheet4U.conInitial Power-Up and Operation

An internal power-up reset asserts RST until the supply voltage has risen above the power-on-reset threshold (VTH+).

RST holds the microcontroller in a reset condition until 120µs after V<sub>CC</sub> has risen above V<sub>TH+</sub>. This reset delay ensures that the clock output and the microcontroller's internal clock circuits have stabilized before the system is allowed to start. Typical microcontroller reset delay ranges from 1ms to 250ms to allow a slow crystal oscillator circuit to start up. The MAX7384 has a fast startup, eliminating the need for such a long reset delay.

#### **Extended Temperature Operation**

The MAX7384 was tested to +135°C during product characterization and shown to function normally at this temperature (see the *Typical Operating Characteristics*). However, production test and qualification is only performed from -40°C to +125°C at this time. Contact the factory if operation outside this range is required.

#### **Power-Supply Considerations**

The MAX7384 operates with a 2.7V and 5.5V powersupply voltage. V<sub>CC</sub> provides power to the the internal circuitry and V<sub>L</sub> supplies power to the clock and reset outputs. Good power-supply decoupling is needed to maintain the power-supply rejection performance of the MAX7384. Bypass both V<sub>CC</sub> and V<sub>L</sub> to GND with a 0.1µF surface-mount ceramic capacitor. Mount the bypassing capacitors as close to the device as possible. Use a larger value bypass capacitor if the MAX7384 is to operate with a large capacitive load. Use a bypass capacitor value of at least 1000 times that of the output load capacitance.

# Table 1. Standard Reset ThresholdLevels

SUFFIX	RESET THRESHOLD (V)			
X	2.50			
V	2.71			
R	2.89	Standard value		
Q	3.13			
Р	3.34			
N	3.44			
J	3.96			
М	4.38	Standard value		

Contact factory for nonstandard reset threshold options.

#### **Table 2. Standard Frequencies**

SUFFIX	STANDARD FREQUENCY (MHz)	
UK	10	
UT	11.0592	
VB	12	
VT	14.7456	
WB	16	

For all other frequency options, contact factory.

#### **Table 3. Standard Part Numbers**

PART	RESET OUTPUT TYPE	RESET THRESHOLD (V)	FREQUENCY (MHz)
MAX7384CRUK	Open drain	2.89	10
MAX7384CRUT	Open drain	2.89	11.0592
MAX7384CRVB	Open drain	2.89	12
MAX7384CRVT	Open drain	2.89	14.7456
MAX7384CRWB	Open drain	2.89	16
MAX7384CMUK	Open drain	4.38	10
MAX7384CMUT	Open drain	4.38	11.0592
MAX7384CMVB	Open drain	4.38	12
MAX7384CMVT	Open drain	4.38	14.7456
MAX7384CMWB	Open drain	4.38	16

**MAX7384** 

# Chip Information

\_Pin Configuration





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PROCESS: BICMOS



## **Package Information**

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



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