

General Description

The MAX5035 easy-to-use, high-efficiency, high-voltage, step-down DC-DC converter operates from an input voltage up to 76V and consumes only 270µA quiescent current at no load. This pulse-width modulated (PWM) converter operates at a fixed 125kHz switching frequency at heavy loads, and automatically switches to pulse-skipping mode to provide low quiescent current and high efficiency at light loads. The MAX5035 includes internal frequency compensation simplifying circuit implementation. The device uses an internal lowon-resistance, high-voltage, DMOS transistor to obtain high efficiency and reduce overall system cost. This device includes undervoltage lockout, cycle-by-cycle current limit, hiccup mode output short-circuit protection, and thermal shutdown.

The MAX5035 delivers up to 1A output current. The output current may be limited by the maximum power dissipation capability of the package. External shutdown is included, featuring 10µA (typ) shutdown current. The MAX5035A/B/C versions have fixed output voltages of 3.3V, 5V, and 12V, respectively, while the MAX5035D/E versions have an adjustable output voltage from 1.25V to 13.2V.

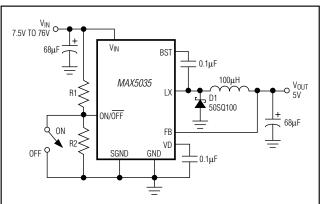
The MAX5035 is available in space-saving 8-pin SO and 8-pin plastic DIP packages and operates over the automotive (-40°C to +125°C) temperature range.

Automotive

Consumer Electronics

Industrial

Distributed Power



Typical Operating Circuit

Features

- Wide 7.5V to 76V Input Voltage Range
- Fixed (3.3V, 5V, 12V) and Adjustable (1.25V to 13.2V) Versions
- 1A Output Current
- ♦ Efficiency Up to 94%
- Internal 0.4Ω High-Side DMOS FET
- ♦ 270µA Quiescent Current at No Load, 10µA Shutdown Current
- Internal Frequency Compensation
- Fixed 125kHz Switching Frequency
- Thermal Shutdown and Short-Circuit Current Limit
- 8-Pin SO and PDIP Packages

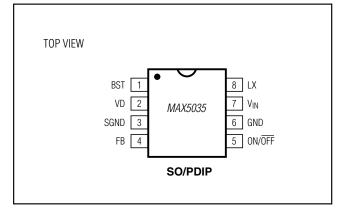
TEMP RANGE	PIN- PACKAGE	OUTPUT VOLTAGE (V)
0°C to +85°C	8 SO	
0°C to +85°C	8 PDIP	3.3
-40°C to +125°C	8 SO	3.3
-40°C to +125°C	8 SO	
0°C to +85°C	8 SO	
0°C to +85°C	8 PDIP	5.0
-40°C to +125°C	8 SO	5.0
-40°C to +125°C	8 SO	
	0°C to +85°C 0°C to +85°C -40°C to +125°C -40°C to +125°C 0°C to +85°C 0°C to +85°C -40°C to +125°C -40°C to +125°C	TEMP RANGE PACKAGE 0°C to +85°C 8 SO 0°C to +85°C 8 PDIP -40°C to +125°C 8 SO -40°C to +125°C 8 SO 0°C to +85°C 8 SO

Ordering Information

N denotes an automotive qualified part.

+Denotes a lead(Pb)-free/RoHS-compliant package. Ordering Information continued at end of data sheet.

Pin Configuration



For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maximintegrated.com.

Applications

ABSOLUTE MAXIMUM RATINGS

(Voltages referenced to GND, unless oth	erwise specified.)	VD Short-Circui
V _{IN}	0.3V to +80V	Continuous Pov
SGND	0.3V to +0.3V	8-Pin PDIP (c
LX	0.8V to (V _{IN} + 0.3V)	8-Pin SO (de
BST	0.3V to (V _{IN} + 10V)	Operating Tem
BST (transient < 100ns)	0.3V to (V _{IN} + 15V)	MAX5035_U
BST to LX		MAX5035_A_
BST to LX (transient < 100ns)	0.3V to +15V	Storage Tempe
ON/OFF	0.3V to +80V	Junction Tempe
VD	0.3V to +12V	Lead Temperat
FB		Soldering Temp
MAX5035A/MAX5035B/MAX5035C	0.3V to +15V	Lead(Pb)-fre
MAX5035D/E	0.3V to +12V	Containing le
V_{OUT} Short-Circuit Duration ($V_{IN} \le 40V$).	Indefinite	0

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS (MAX5035_U_ _)

 $(V_{IN} = +12V, V_{ON/OFF} = +12V, I_{OUT} = 0, T_A = 0^{\circ}C$ to +85°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$. See the *Typical Application Circuit.*)

PARAMETER	SYMBOL		CONDITIONS	MIN	ТҮР	MAX	UNITS
		MAX5035A		7.5		76.0	V
Innut Valtage Denge	Maria	MAX5035B	MAX5035B			76.0	
Input Voltage Range	VIN	MAX5035C		15		76	V
		MAX5035D/E		7.5		76.0	
Undervoltage Lockout	UVLO				5.2		V
		MAX5035A	$V_{IN} = 7.5V$ to 76V, $I_{OUT} = 20$ mA to 1A	3.185	3.3	3.415	
Output Voltage	Vout	MAX5035B	$V_{IN} = 7.5V$ to 76V, $I_{OUT} = 20$ mA to 1A	4.85	5.0	5.15	V
		MAX5035C	$V_{IN} = 15V$ to 76V, $I_{OUT} = 20$ mA to 1A	11.64	12	12.36	
Feedback Voltage	V _{FB}	V _{IN} = 7.5V to 76V, MAX5035D/E		1.192	1.221	1.250	V
Feedback vollage	VEB	$V_{IN} = 7.5V$ to	76V, MAX5035E	1.185	1.221	1.250	v
		$V_{IN} = 12V, I_{LC}$	DAD = 0.5A, MAX5035A		86		
		$V_{IN} = 12V$, $I_{LOAD} = 0.5A$, MAX5035B			90		
Efficiency	η	$V_{IN} = 24V$, I_{LO}	DAD = 0.5A, MAX5035C		94		%
		V _{IN} = 12V, V _C MAX5035D/E	$DUT = 5V, I_{LOAD} = 0.5A,$		90		
		V _{FB} = 3.5V, V	IN = 7.5V to 76V, MAX5035A		270	440	
		V _{FB} = 5.5V, V	IN = 7.5V to 76V, MAX5035B		270	440	μΑ
Quiescent Supply Current	IQ	$V_{FB} = 13V, V_I$	N = 15V to 76V, MAX5035C		270	440	
		V _{FB} = 1.3V, N	1AX5035D		270	440	
		$V_{FB} = 1.3V, N$	1AX5035E		340	460	
Shutdown Current	ISHDN	$V_{ON/OFF} = 0V$	′, V _{IN} = 7.5V to 76V		10	45	μA
Peak Switch Current Limit	ILIM	(Note 1)		1.30	1.9	2.50	А

ELECTRICAL CHARACTERISTICS (continued) (MAX5035_U_ _)

 $(V_{IN} = +12V, V_{ON/OFF} = +12V, I_{OUT} = 0, T_A = 0^{\circ}C$ to +85°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$. See the *Typical Application Circuit.*)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS
Switch Leakage Current	IOL	$V_{IN} = 76V, V_{ON/\overline{OFF}} = 0V, V_{LX} = 0V$		0.01	1	μA
Switch On-Resistance	R _{DS(ON)}	ISWITCH = 1A		0.40	0.80	Ω
PFM Threshold	IPFM	Minimum switch current in any cycle	55	85	130	mA
FB Input Bias Current	Ι _Β	MAX5035D/E	-150	+0.01	+150	nA
ON/OFF CONTROL INPUT						
ON/OFF Input-Voltage Threshold	Vou	Rising trip point for MAX5035A/B/C/D	1.53	1.69	1.85	V
	Von/OFF	Rising trip point for MAX5035E	1.40	1.65	1.90	v
ON/OFF Input-Voltage Hysteresis	V _{HYST}			100		mV
ON/OFF Input Current	ION/OFF	$V_{ON/OFF} = 0V$ to V_{IN}		10	150	nA
ON/OFF Operating Voltage Range	Von/OFF				76	V
OSCILLATOR	1					
Oscillator Frequency	fosc		109	125	135	kHz
Maximum Duty Cycle	D _{MAX}	MAX5035D/E		95		%
VOLTAGE REGULATOR						
Regulator Output Voltage	VD	$V_{IN} = 8.5V$ to 76V, $I_L = 0$	6.9	7.8	8.8	V
Dropout Voltage		$7.5V \le V_{IN} \le 8.5V$, $I_L = 1mA$		2.0		V
Load Regulation	$\Delta VD/\Delta I_{VD}$	0 to 5mA		150		Ω
PACKAGE THERMAL CHARACT	ERISTICS					
Thermal Resistance	0	SO package (JEDEC 51)		170		
(Junction to Ambient)	θJA	DIP package (JEDEC 51)		110		°C/W
THERMAL SHUTDOWN						
Thermal-Shutdown Junction Temperature	T _{SH}			+160		°C
Thermal-Shutdown Hysteresis	T _{HYST}			20		°C

ELECTRICAL CHARACTERISTICS (MAX5035_A__)

 $(V_{IN} = +12V, V_{ON/\overline{OFF}} = +12V, I_{OUT} = 0, T_A = T_J = -40^{\circ}C$ to $+125^{\circ}C$, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$. See the *Typical Application Circuit.*) (Note 2)

PARAMETER	SYMBOL		CONDITIONS	MIN	ТҮР	MAX	UNITS
		MAX5035A		7.5		76.0	
Input Valtage Denge	Maria	MAX5035B		7.5		76.0	v
Input Voltage Range	V _{IN}	MAX5035C		15		76	v
		MAX5035D/E		7.5		76.0	
Undervoltage Lockout	UVLO				5.2		V
Output Voltage	Vout	MAX5035A	$V_{IN} = 7.5V$ to 76V, I _{OUT} = 20mA to 1A	3.185	3.3	3.415	
		MAX5035B	$V_{IN} = 7.5V$ to 76V, $I_{OUT} = 20$ mA to 1A	4.825	5.0	5.175	V
		MAX5035C	$V_{IN} = 15V$ to 76V, $I_{OUT} = 20$ mA to 1A	11.58	12	12.42	

ELECTRICAL CHARACTERISTICS (MAX5035_A_ _) (continued)

 $(V_{IN} = +12V, V_{ON/OFF} = +12V, I_{OUT} = 0, T_A = T_J = -40^{\circ}C$ to $+125^{\circ}C$, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$. See the *Typical Application Circuit.*) (Note 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS	
E 11 1.17.11		V _{IN} = 7.5V to 76V, MAX5035D	1.192	1.221	1.250		
Feedback Voltage	V _{FB}	V _{IN} = 7.5V to 76V, MAX5035E	1.185	1.221	1.250	V	
		V _{IN} = 12V, I _{LOAD} = 0.5A, MAX5035A		86			
		V _{IN} = 12V, I _{LOAD} = 0.5A, MAX5035B		90			
Efficiency	η	V _{IN} = 24V, I _{LOAD} = 0.5A, MAX5035C		94		%	
		V _{IN} = 12V, V _{OUT} = 5V, I _{LOAD} = 0.5A, MAX5035D/E		90			
		$V_{FB} = 3.5V, V_{IN} = 7.5V$ to 76V, MAX5035A		270	440		
		$V_{FB} = 5.5V, V_{IN} = 7.5V$ to 76V, MAX5035B		270	440		
Quiescent Supply Current	lq	V _{FB} = 13V, V _{IN} = 15V to 76V, MAX5035C		270	440	μA	
		V _{FB} = 1.3V, MAX5035D		270	440		
		V _{FB} = 1.3V, MAX5035E		340	460		
Shutdown Current	ISHDN	$V_{ON/OFF} = 0V$, $V_{IN} = 7.5V$ to 76V		10	45	μA	
Peak Switch Current Limit	ILIM	(Note 1)	1.30	1.9	2.50	A	
Switch Leakage Current	I _{OL}	$V_{IN} = 76V, V_{ON}\overline{OFF} = 0V, V_{LX} = 0V$		1			
		$V_{IN} = 76V, V_{ON/\overline{OFF}} = 0V, V_{LX} = 0V,$ MAX5035E			5	μA	
Switch On-Resistance	R _{DS(ON)}	I _{SWITCH} = 1A		0.40	0.80	Ω	
PFM Threshold	IPFM	Minimum switch current in any cycle	55	85	130	mA	
FB Input Bias Current	Ι _Β	MAX5035D/E	-150	+0.01	+150	nA	
ON/OFF CONTROL INPUT			•				
		Rising trip point for MAX5035A/B/C/D	1.50	1.69	1.85	V	
ON/OFF Input-Voltage Threshold	Von/OFF	Rising trip point for MAX5035E	1.40	1.65	1.90		
ON/OFF Input-Voltage Hysteresis	VHYST			100		mV	
ON/OFF Input Current	ION/OFF	$V_{ON/OFF} = 0V$ to V_{IN}		10	150	nA	
ON/OFF Operating Voltage Range	V _{ON/OFF}				76	V	
OSCILLATOR	1		1				
Oscillator Frequency	fosc		105	125	137	kHz	
Maximum Duty Cycle	DMAX	MAX5035D/E		95		%	
VOLTAGE REGULATOR		•	•				
Regulator Output Voltage	VD	$V_{IN} = 8.5V$ to 76V, $I_L = 0$	6.5	7.8	9.0	V	
Dropout Voltage		7.5V ≤ V _{IN} ≤ 8.5V, I _L = 1mA	1	2.0		V	
Load Regulation	$\Delta VD/\Delta I_{VD}$	0 to 5mA		150		Ω	

ELECTRICAL CHARACTERISTICS (MAX5035_A_ _) (continued)

 $(V_{IN} = +12V, V_{ON/OFF} = +12V, I_{OUT} = 0, T_A = T_J = -40^{\circ}C$ to $+125^{\circ}C$, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$. See the *Typical Application Circuit.*) (Note 2)

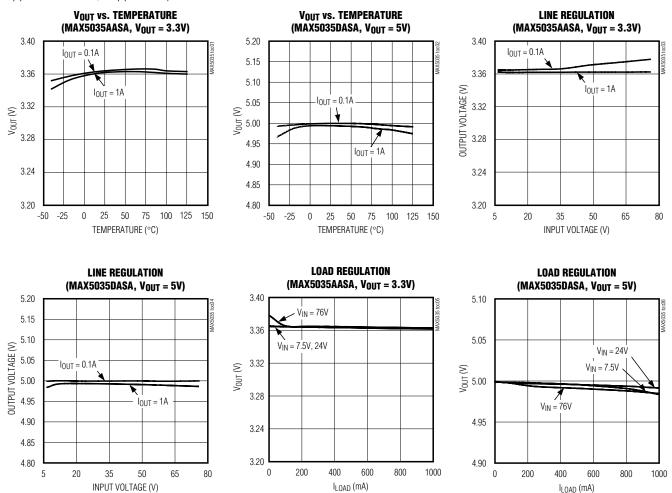
PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS	
PACKAGE THERMAL CHARACTERISTICS							
Thermal Resistance	0	SO package (JEDEC 51)		170		°C/W	
(Junction to Ambient)	θJA	DIP package (JEDEC 51)	110		°C/w		
THERMAL SHUTDOWN							
Thermal-Shutdown Junction Temperature	T _{SH}			+160		°C	
Thermal-Shutdown Hysteresis	THYST			20		°C	

Note 1: Switch current at which current limit is activated.

Note 2: All limits at -40°C are guaranteed by design, not production tested.

Typical Operating Characteristics

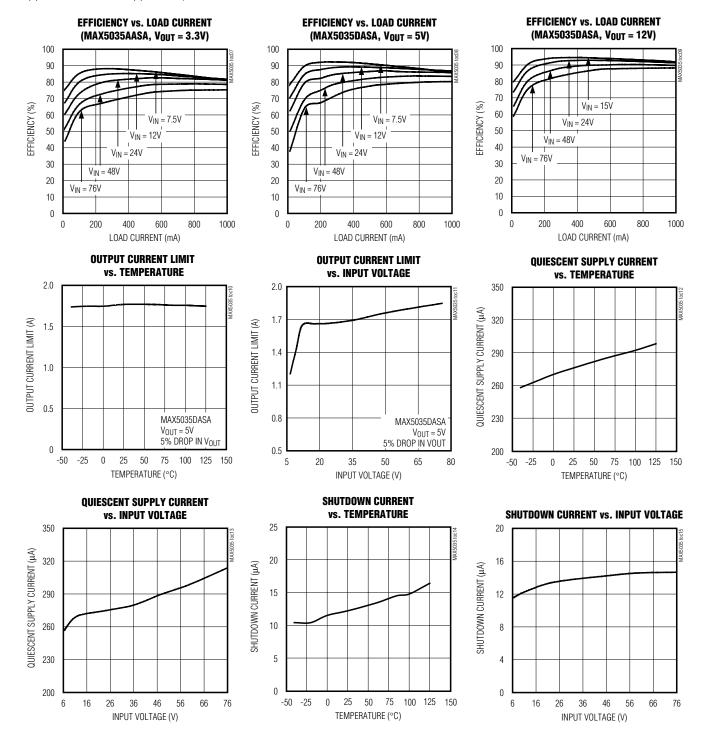
 $(V_{IN} = 12V, V_{ON/\overline{OFF}} = 12V, T_A = -40^{\circ}C$ to +125°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$. See the *Typical Application Circuit*, if applicable.)



Maxim Integrated

Typical Operating Characteristics (continued)

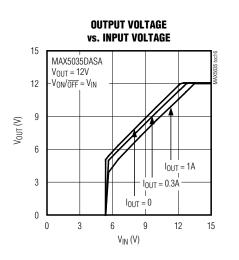
 $(V_{IN} = 12V, V_{ON/OFF} = 12V, T_A = -40^{\circ}C$ to +125°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$. See the *Typical Application Circuit*, if applicable.)

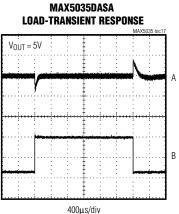


Maxim Integrated

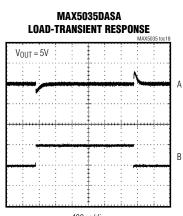
Typical Operating Characteristics (continued)

(VIN = 12V, VON/OFF = 12V, TA = -40°C to +125°C, unless otherwise noted. Typical values are at TA = +25°C. See the Typical Application Circuit, if applicable.)





A: V_{OUT}, 200mV/div, AC-COUPLED B: I_{OUT}, 500mA/div, 0.1A TO 1A



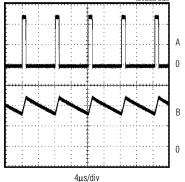
400µs/div A: V_{OUT}, 200mV/div, AC-COUPLED B: IOUT, 500mA/div, 0.5A TO 1A

MAX5035DASA LOAD-TRANSIENT RESPONSE $V_{OUT} = 5V$ В

A

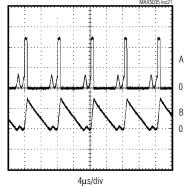
400µs/div A: V_{OUT}, 200mV/div, AC-COUPLED B: IOUT, 500mA/div, 0.1A TO 0.5A

MAX5035DASA LX WAVEFORMS



A: SWITCH VOLTAGE (LX PIN), 20V/div (VIN = 48V) B: INDUCTOR CURRENT, 500mA/div (I_{OUT} = 1A)

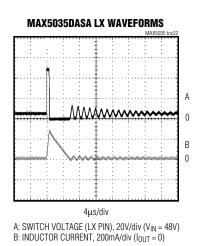
MAX5035DASA LX WAVEFORMS



A: SWITCH VOLTAGE (LX PIN), 20V/div (VIN = 48V) B: INDUCTOR CURRENT, 200mA/div (I_{OUT} = 100mA)

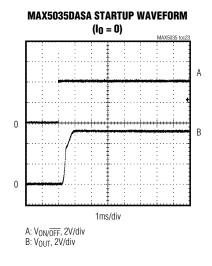
Typical Operating Characteristics (continued)

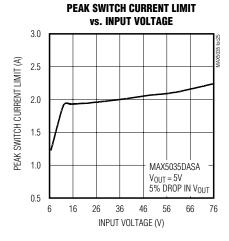
 $(V_{IN} = 12V, V_{ON/\overline{OFF}} = 12V, T_A = -40^{\circ}C$ to +125°C, unless otherwise noted. Typical values are at T_A = +25°C. See the *Typical Application Circuit*, if applicable.)



MAX5035DASA STARTUP WAVEFORM (lo = 1A) MAX5035 tor24 A 0 0 0 1 1 ms/div

A: V_{ON/OFF}, 2V/div B: V_{OUT}, 2V/div

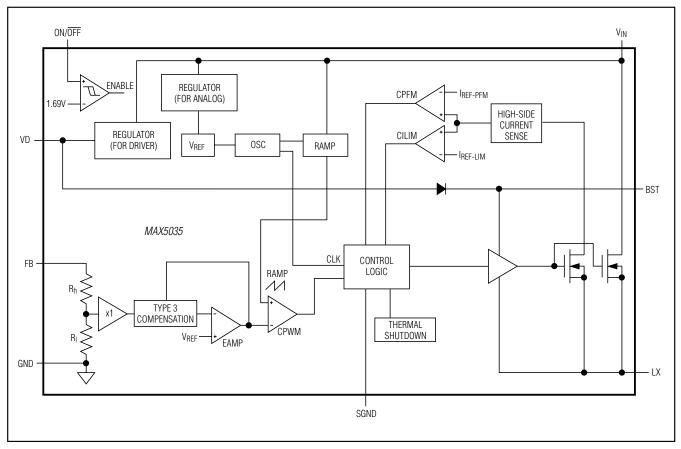




Pin Description

PIN	NAME	FUNCTION			
1	BST	Boost Capacitor Connection. Connect a 0.1µF ceramic capacitor from BST to LX.			
2	VD	Internal Regulator Output. Bypass VD to GND with a 0.1µF ceramic capacitor.			
3	SGND Internal Connection. SGND must be connected to GND.				
		Output Sense Feedback Connection. For fixed output voltage (MAX5035A, MAX5035B, MAX5035C), connect FB to V_{OUT} . For adjustable output voltage (MAX5035D, MAX5035E), use an external resistive voltage-divider to set V_{OUT} . V _{FB} regulating set point is 1.22V.			
5	5 ON/OFF Shutdown Control Input. Pull ON/OFF low to put the device in shutdown mode. Driv normal operation.				
6	6 GND Ground				
7	VIN	V _{IN} Input Voltage. Bypass V _{IN} to GND with a low ESR capacitor as close to the device as possible.			
8	LX	Source Connection of Internal High-Side Switch			

Block Diagram



Detailed Description

The MAX5035 step-down DC-DC converter operates from a 7.5V to 76V input voltage range. A unique voltage-mode control scheme with voltage feed-forward and an internal switching DMOS FET provides high efficiency over a wide input voltage range. This pulsewidth modulated converter operates at a fixed 125kHz switching frequency. The device also features automatic pulse-skipping mode to provide low quiescent current and high efficiency at light loads. Under no load, the MAX5035 consumes only 270 μ A, and in shutdown mode, consumes only 10 μ A. The MAX5035 also features undervoltage lockout, hiccup mode output short-circuit protection, and thermal shutdown.

Shutdown Mode

Drive ON/OFF to ground to shut down the MAX5035. Shutdown forces the internal power MOSFET off, turns off all internal circuitry, and reduces the V_{IN} supply current to 10 μ A (typ). The ON/OFF rising threshold is 1.69V (typ). Before any operation begins, the voltage at ON/OFF must exceed 1.69V (typ). The ON/OFF input has 100mV hysteresis.

Undervoltage Lockout (UVLO)

Use the ON/ $\overline{\text{OFF}}$ function to program the UVLO threshold at the input. Connect a resistive voltage-divider from V_{IN} to GND with the center node to ON/ $\overline{\text{OFF}}$ as shown in Figure 1. Calculate the threshold value by using the following formula:

$$V_{\text{UVLO(TH)}} = \left(1 + \frac{\text{R1}}{\text{R2}}\right) \times 1.85\text{V}$$

The minimum recommended VUVLO(TH) is 6.5V, 7.5V, and 13V for the output voltages of 3.3V, 5V, and 12V, respectively. The recommended value for R2 is less than $1M\Omega$.

If the external UVLO threshold-setting divider is not used, an internal undervoltage-lockout feature monitors the supply voltage at V_{IN} and allows operation to start when V_{IN} rises above 5.2V (typ). This feature can be used only when V_{IN} rise time is faster than 2ms. For slower V_{IN} rise time, use the resistive-divider at ON/OFF.

Boost High-Side Gate Drive (BST)

Connect a flying bootstrap capacitor between LX and BST to provide the gate-drive voltage to the high-side N-channel DMOS switch. The capacitor is alternately charged from the internally regulated output voltage VD and placed across the high-side DMOS driver. Use a 0.1μ F, 16V ceramic capacitor located as close to the device as possible.

On startup, an internal low-side switch connects LX to ground and charges the BST capacitor to VD. Once the BST capacitor is charged, the internal low-side switch is turned off and the BST capacitor voltage provides the necessary enhancement voltage to turn on the high-side switch.

Thermal-Overload Protection

The MAX5035 features integrated thermal overload protection. Thermal overload protection limits total power dissipation in the device, and protects the device in the event of a fault condition. When the die temperature exceeds $+160^{\circ}$ C, an internal thermal sensor signals the shutdown logic, turning off the internal power MOSFET and allowing the IC to cool. The thermal sensor turns the internal power MOSFET back on after the IC's die temperature cools down to $+140^{\circ}$ C, resulting in a pulsed output under continuous thermal overload conditions.

Applications Information

Setting the Output Voltage

The MAX5035A/B/C have preset output voltages of 3.3V, 5.0V, and 12V, respectively. Connect FB to the preset output voltage (see the *Typical Operating Circuit*).

The MAX5035D/E versions offer an adjustable output voltage. Set the output voltage with a resistive voltagedivider connected from the circuit's output to ground (Figure 1). Connect the center node of the divider to FB. Choose R4 less than $15k\Omega$, then calculate R3 as follows:

$$R3 = \frac{(V_{OUT} - 1.22)}{1.22} \times R4$$

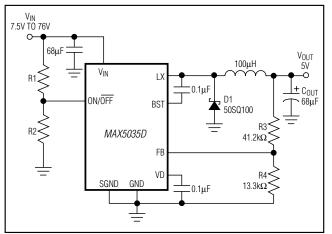


Figure 1. Adjustable Output Voltage

The MAX5035 features internal compensation for optimum closed-loop bandwidth and phase margin. With the preset compensation, it is strongly advised to sense the output immediately after the primary LC.

Inductor Selection

The choice of an inductor is guided by the voltage difference between V_{IN} and V_{OUT} , the required output current, and the operating frequency of the circuit. Use an inductor with a minimum value given by:

$$L = \frac{(V_{IN} - V_{OUT}) \times D}{0.3 \times I_{OUTMAX} \times f_{SW}}$$

where:

$$D = \frac{V_{OUT}}{V_{IN}}$$

 I_{OUTMAX} is the maximum output current required, and fsw is the operating frequency of 125kHz. Use an inductor with a maximum saturation current rating equal to at least the peak switch current limit (I_{LIM}). Use inductors with low DC resistance for higher efficiency.

Selecting a Rectifier

The MAX5035 requires an external Schottky rectifier as a freewheeling diode. Connect this rectifier close to the device using short leads and short PC board traces. Choose a rectifier with a continuous current rating greater than the highest expected output current. Use a rectifier with a voltage rating greater than the maximum expected input voltage, VIN. Use a low forward-voltage Schottky rectifier for proper operation and high efficiency. Avoid higher than necessary reverse-voltage Schottky rectifiers that have higher forward-voltage drops. Use a Schottky rectifier with forward-voltage

V _{IN} (V)	DIODE PART NUMBER	MANUFACTURER
	15MQ040N	IR
7.5 to 00	B240A	Diodes, Inc.
7.5 to 36	B240	Central Semiconductor
	MBRS240, MBRS1540	ON Semiconductor
	30BQ060	IR
7.5 to 56	B360A	Diodes, Inc.
7.5 10 56	CMSH3-60	Central Semiconductor
	MBRD360, MBR3060	ON Semiconductor
7 5 to 70	50SQ100, 50SQ80	IR
7.5 to 76	MBRM5100	Diodes, Inc.

Table 1. Diode Selection

drop (V_{FB}) less than 0.45V at $+25^{\circ}$ C and maximum load current to avoid forward biasing of the internal body diode (LX to ground). Internal body diode conduction may cause excessive junction temperature rise and thermal shutdown. Use Table 1 to choose the proper rectifier at different input voltages and output current.

Input Bypass Capacitor

The discontinuous input-current waveform of the buck converter causes large ripple currents in the input capacitor. The switching frequency, peak inductor current, and the allowable peak-to-peak voltage ripple that reflects back to the source dictate the capacitance requirement. The MAX5035 high switching frequency allows the use of smaller-value input capacitors.

The input ripple is comprised of ΔV_Q (caused by the capacitor discharge) and ΔV_{ESR} (caused by the ESR of the capacitor). Use low-ESR aluminum electrolytic capacitors with high ripple-current capability at the input. Assuming that the contribution from the ESR and capacitor discharge is equal to 90% and 10%, respectively, calculate the input capacitance and the ESR required for a specified ripple using the following equations:

$$ESR_{IN} = \frac{\Delta V_{ESR}}{\left(I_{OUT} + \frac{\Delta I_{L}}{2}\right)}$$
$$C_{IN} = \frac{I_{OUT} \times D (1-D)}{\Delta V_{Q} \times f_{SW}}$$

where:

$$\Delta I_{L} = \frac{(V_{IN} - V_{OUT}) \times V_{OUT}}{V_{IN} \times f_{SW} \times L}$$

$$D = \frac{V_{OUT}}{V_{IN}}$$

 I_{OUT} is the maximum output current of the converter and f_{SW} is the oscillator switching frequency (125kHz). For example, at V_{IN} = 48V, V_{OUT} = 3.3V, the ESR and input capacitance are calculated for the input peak-topeak ripple of 100mV or less yielding an ESR and capacitance value of 80m Ω and 51µF, respectively.

Low-ESR, ceramic, multilayer chip capacitors are recommended for size-optimized application. For ceramic capacitors, assume the contribution from ESR and capacitor discharge is equal to 10% and 90%, respectively.

The input capacitor must handle the RMS ripple current without significant rise in temperature. The maximum capacitor RMS current occurs at about 50% duty cycle.

2

Ensure that the ripple specification of the input capacitor exceeds the worst-case capacitor RMS ripple current. Use the following equations to calculate the input capacitor RMS current:

$$I_{CRMS} = \sqrt{I_{PRMS}^2 - I_{AVGIN}}$$

where:

$$I_{PRMS} = \sqrt{\left(I_{PK}^{2} + I_{DC}^{2} + \left(I_{PK} \times I_{DC}\right)\right) \times \frac{D}{3}}$$
$$I_{AVGIN} = \frac{V_{OUT} \times I_{OUT}}{V_{IN} \times \eta}$$
$$I_{PK} = I_{OUT} + \frac{\Delta I_{L}}{2}, I_{DC} = I_{OUT} - \frac{\Delta I_{L}}{2}$$
$$and D = \frac{V_{OUT}}{V_{IN}}$$

 $\mathsf{I}_{\mathsf{PRMS}}$ is the input switch RMS current, $\mathsf{I}_{\mathsf{AVGIN}}$ is the input average current, and η is the converter efficiency.

The ESR of aluminum electrolytic capacitors increases significantly at cold temperatures. Use a 1μ F or greater value ceramic capacitor in parallel with the aluminum electrolytic input capacitor, especially for input voltages below 8V.

Output Filter Capacitor

The worst-case peak-to-peak and RMS capacitor ripple current, allowable peak-to-peak output ripple voltage, and the maximum deviation of the output voltage during load steps determine the capacitance and the ESR requirements for the output capacitors.

The output capacitance and its ESR form a zero, which improves the closed-loop stability of the buck regulator. Choose the output capacitor so the ESR zero frequency (fz) occurs between 20kHz to 40kHz. Use the following equation to verify the value of fz. Capacitors with 100m Ω to 250m Ω ESR are recommended to ensure the closed-loop stability, while keeping the output ripple low.

$$f_{Z} = \frac{1}{2 \times \pi \times C_{OUT} \times ESR_{OUT}}$$

The output ripple is comprised of ΔV_{OQ} (caused by the capacitor discharge) and ΔV_{OESR} (caused by the ESR of the capacitor). Use low-ESR tantalum or aluminum electrolytic capacitors at the output. Assuming that the contributions from the ESR and capacitor discharge equal 80% and 20% respectively, calculate the output

capacitance and the ESR required for a specified ripple using the following equations:

$$\mathsf{ESR}_{\mathsf{OUT}} = \frac{\Delta \mathsf{V}_{\mathsf{OESR}}}{\Delta \mathsf{I}_{\mathsf{L}}}$$

$$C_{OUT} \approx \frac{\Delta I_L}{2.2 \times \Delta V_{OQ} \times f_{SW}}$$

The MAX5035 has an internal soft-start time (tss) of 400 μ s. It is important to keep the output rise time at startup below tss to avoid output overshoot. The output rise time is directly proportional to the output capacitor. Use 68 μ F or lower capacitance at the output to control the overshoot below 5%.

In a dynamic load application, the allowable deviation of the output voltage during the fast-transient load dictates the output capacitance value and the ESR. The output capacitors supply the step load current until the controller responds with a greater duty cycle. The response time (tRESPONSE) depends on the closedloop bandwidth of the converter. The resistive drop across the capacitor ESR and capacitor discharge cause a voltage droop during a step load. Use a combination of low-ESR tantalum and ceramic capacitors for better transient load and ripple/noise performance. Keep the maximum output-voltage deviation above the tolerable limits of the electronics being powered. Assuming a 50% contribution each from the output capacitance discharge and the ESR drop, use the following equations to calculate the required ESR and capacitance value:

$$\text{ESR}_{\text{OUT}} = \frac{\Delta \text{V}_{\text{OESR}}}{\text{I}_{\text{STEP}}}$$

$$C_{OUT} = \frac{I_{STEP} \times t_{RESPONSE}}{\Delta V_{OQ}}$$

where ISTEP is the load step and tRESPONSE is the response time of the controller. Controller response time is approximately one-third of the reciprocal of the closed-loop unity-gain bandwidth, 20kHz typically.

PCB Layout Considerations

Proper PCB layout is essential. Minimize ground noise by connecting the anode of the Schottky rectifier, the input bypass capacitor ground lead, and the output filter capacitor ground lead to a single point ("star"

ground configuration). A ground plane is required. Minimize lead lengths to reduce stray capacitance, trace resistance, and radiated noise. In particular, place the Schottky rectifier diode right next to the device. Also, place BST and VD bypass capacitors very close to the device. Use the PC board copper plane connecting to $V_{\rm IN}$ and LX for heatsinking.

Application Circuits

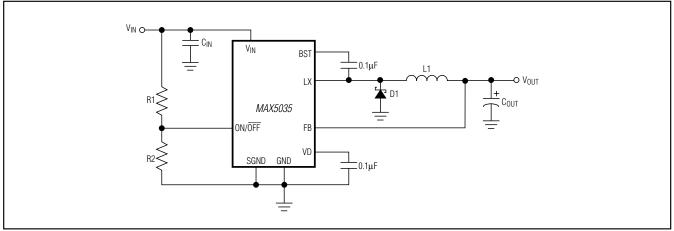


Figure 2. Fixed Output Voltages

V _{IN} (V)	V _{OUT} (V)	IOUT (A)	EXTERNAL COMPONENTS			
7.5 to 76	3.3	0.5	$C_{IN} = 68\mu$ F, Panasonic, EEVFK2A680Q $C_{OUT} = 68\mu$ F, Vishay Sprague, 594D686X_010C2T $C_{BST} = 0.1\mu$ F, 0805			
7.5 to 76	3.3	1	R1 = $1M\Omega \pm 1\%$, 0805 R2 = $384k\Omega \pm 1\%$, 0805 D1 = 50 SQ100, IR L1 = 100μ H, Coilcraft Inc., DO5022P-104			
7.5 to 76	5	0.5	$C_{IN} = 68\mu$ F, Panasonic, EEVFK2A680Q $C_{OUT} = 68\mu$ F, Vishay Sprague, 594D68X_010C2T $C_{BST} = 0.1\mu$ F, 0805			
7.5 to 76	5	1	R1 = $1M\Omega \pm 1\%$, 0805 R2 = $384k\Omega \pm 1\%$, 0805 D1 = $50SQ100$, IR L1 = 100μ H, Coilcraft Inc., DO5022P-104			
15 to 76	12	1	$ \begin{array}{l} C_{IN} = 68 \mu F, Panasonic, EEVFK2A680Q\\ C_{OUT} = 15 \mu F, Vishay Sprague, 594D156X0025C2T\\ C_{BST} = 0.1 \mu F, 0805\\ R1 = 1M\Omega \pm 1\%, 0805\\ R2 = 139 k\Omega \pm 1\%, 0805\\ D1 = 50 SQ100, IR\\ L1 = 220 \mu H, Coilcraft Inc., DO5022P-224 \end{array} $			

Table 2. Typical External Components Selection (Circuit of Figure 2)

Table 2. Typical External Components Selection (Circuit of Figure 2) (continued)

V _{IN} (V)	V _{OUT} (V)	IOUT (A)	EXTERNAL COMPONENTS
045.14	3.3	1	$ \begin{array}{l} C_{IN} = 220 \mu F, Panasonic, EEVFK1E221P \\ C_{OUT} = 68 \mu F, Vishay Sprague, 594D686X_010C2T \\ C_{BST} = 0.1 \mu F, 0805 \\ R1 = 1M \Omega \pm 1\%, 0805 \\ R2 = 274 k \Omega \pm 1\%, 0805 \\ D1 = B220, Diodes Inc. \\ L1 = 100 \mu H, Coilcraft Inc., DO5022P-104 \\ \end{array} $
9 to 14	9 to 14	1	$\begin{split} C_{IN} &= 220 \mu F, Panasonic, EEVFK1E221P\\ C_{OUT} &= 68 \mu F, Vishay Sprague, 594D686X_010C2T\\ C_{BST} &= 0.1 \mu F, 0805\\ R1 &= 1M\Omega \pm 1\%, 0805\\ R2 &= 274 k\Omega \pm 1\%, 0805\\ D1 &= B220, Diodes Inc.\\ L1 &= 100 \mu H, Coilcraft Inc., DO5022P-104 \end{split}$
	3.3	1	$ \begin{array}{l} C_{IN} = 220 \mu F, \mbox{Panasonic}, \mbox{EVFK1H221P} \\ C_{OUT} = 68 \mu F, \mbox{Vishay Sprague}, \mbox{594D686X_010C2T} \\ C_{BST} = 0.1 \mu F, \mbox{0805} \\ R1 = 1 M \Omega \pm 1\%, \mbox{0805} \\ R2 = 130 k \Omega \pm 1\%, \mbox{0805} \\ D1 = M B R S 2040, \mbox{ON Semiconductor} \\ L1 = 100 \mu H, \mbox{Coilcraft Inc.}, \mbox{DO5022P-104} \end{array} $
18 to 36	5	1	$\begin{split} C_{IN} &= 220 \mu F, Panasonic, EEVFK1H221P\\ C_{OUT} &= 68 \mu F, Vishay Sprague, 594D686X_010C2T\\ C_{BST} &= 0.1 \mu F, 0805\\ R1 &= 1M\Omega \pm 1\%, 0805\\ R2 &= 130 k\Omega \pm 1\%, 0805\\ D1 &= MBRS2040, ON Semiconductor\\ L1 &= 100 \mu H, Coilcraft Inc., DO5022P-104 \end{split}$
	12	1	$\begin{split} C_{IN} &= 220 \mu F, Panasonic, EEVFK1H221P\\ C_{OUT} &= 15 \mu F, Vishay Sprague, 594D156X_0025C2T\\ C_{BST} &= 0.1 \mu F, 0805\\ R1 &= 1M\Omega \pm 1\%, 0805\\ R2 &= 130 k\Omega \pm 1\%, 0805\\ D1 &= MBRS2040, ON Semiconductor\\ L1 &= 220 \mu H, Coilcraft Inc., DO5022P-224 \end{split}$

Table 3. Component Suppliers

SUPPLIER	PHONE	FAX	WEBSITE	
AVX Corporation	843-946-0238	843-626-3123	www.avxcorp.com	
Coilcraft, Inc.	847-639-6400	847-639-1469	www.coilcraft.com	
Diodes Incorporated	805-446-4800	805-446-4850	www.diodes.com	
Panasonic Corp.	800-344-2112	714-737-7323	www.panasonic.com	
SANYO Electric Co., Ltd.	619-661-6835	619-661-1055	www.sanyo.com	
TDK Corp.	847-803-6100	847-390-4405	www.component.tdk.com	
Vishay	402-563-6866	402-563-6296	www.vishay.com	

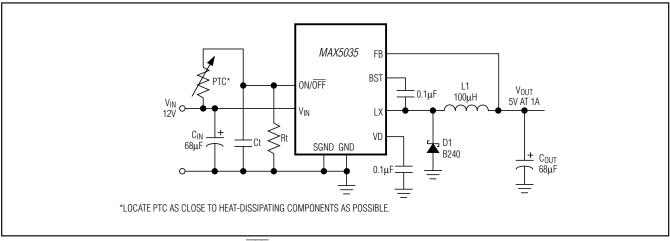


Figure 3. Load Temperature Monitoring with ON/OFF (Requires Accurate VIN)

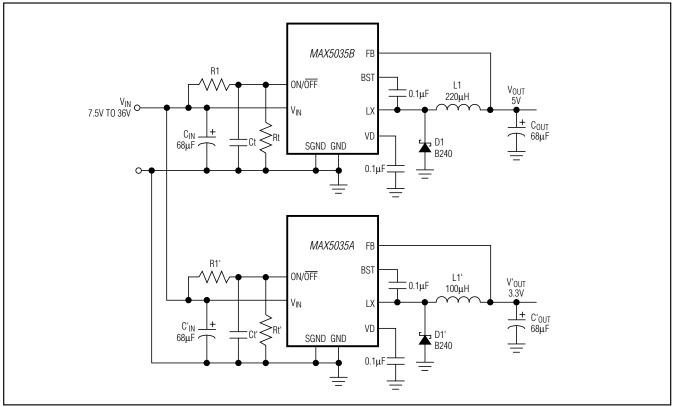


Figure 4. Dual-Sequenced DC-DC Converters (Startup Delay Determined by R1/R1', Ct/Ct' and Rt/Rt')

Ordering Information (continued)

PART	TEMP RANGE	PIN- PACKAGE	OUTPUT VOLTAGE (V)
MAX5035CUSA	0°C to +85°C	8 SO	
MAX5035CUPA	0°C to +85°C	8 PDIP	10
MAX5035CASA	-40°C to +125°C	8 SO	12
MAX5035CASA/V+	-40°C to +125°C	8 SO	
MAX5035DUSA	0°C to +85°C	8 SO	
MAX5035DUPA	0°C to +85°C	8 PDIP	
MAX5035DASA	-40°C to +125°C	8 SO	ADJ
MAX5035DASA/V+	-40°C to +125°C	8 SO	
MAX5035EUSA	0°C to +85°C	8 SO	
MAX5035EASA	-40°C to +125°C	8 SO	ADJ
MAX5035EASA/V+	-40°C to +125°C	8 SO	

/V denotes an automotive qualified part.

+Denotes a lead(Pb)-free/RoHS-compliant package.

_Chip Information

PROCESS: BiCMOS

Package Information

For the latest package outline information and land patterns (footprints), go to <u>www.maxim-ic.com/packages</u>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
8 SO	S8+2	<u>21-0041</u>	<u>90-0096</u>
8 PDIP	P8+1	<u>21-0043</u>	—

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	
0	9/03	Initial release	—
1	6/04	Removed future-product asterisks and made specification changes	1, 2, 3
2	1/07	Modified Absolute Maximum Ratings section, updated Ordering Information, style edits	2, 3
3	5/09	Modified Absolute Maximum Ratings section	1, 2, 16, 18
4	4/10	Updated Electrical Characteristics table specifications	2, 3, 4, 16, 17
5	5/11	Added new variant (MAX5035E)	1–4, 9, 10, 16



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