## Voltage Controlled Oscillator 8.8 - 9.8 GHz

#### Features

- Low Phase Noise
- Wide Tuning Range
- Divide-by-Two Output
- Integrated Buffer Amplifier
- Excellent Temperature Stability
- +5V Bias
- Lead-Free 5 mm 32-Lead PQFN Package
- Halogen-Free "Green" Mold Compound
- RoHS\* Compliant and 260°C Reflow Compatible

#### Description

The MAOC-009264 is an InGaP HBT-based voltage controlled oscillator for frequency generation. No external matching components are required. This VCO is easily integrated into a phase lock loop using the divide-by-two output. The extremely low phase noise makes this part ideal for many radio applications including high capacity digital radios.

The MAOC-009264 primary applications are Point-to-Point Radio, Point-to-Multipoint Radio, Communications Systems, and Low Phase Noise applications.

The 5 mm PQFN package has a lead-free finish that is RoHS compliant and compatible with a 260°C reflow temperature. The package also features low lead inductance and an excellent thermal path.

### Ordering Information<sup>1</sup>

1

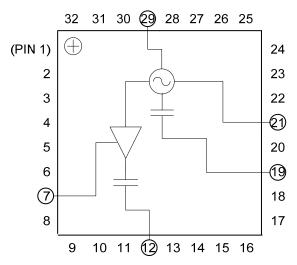
Part Number	Package
MAOC-009264-TR0500	500 piece reel
MAOC-009264-TR1000	1000 piece reel
MAOC-009264-SMB003	Sample Board

1. Reference Application Note M513 for reel size information.



Rev. V2

#### **Block Diagram**



## Pin Designations<sup>2</sup>

Pin	Function	Pin	Function
1	N/C	17	N/C
2	N/C	18	N/C
3	N/C	19	RF
4	N/C	20	N/C
5	N/C	21	V <sub>cc</sub>
6	N/C	22	N/C
7	VBUFFER	23	N/C
8	N/C	24	N/C
9	N/C	25	N/C
10	N/C	26	N/C
11	N/C	27	N/C
12	RF/2	28	N/C
13	N/C	29	V <sub>TUNE</sub>
14	N/C	30	N/C
15	N/C	31	N/C
16	N/C	32	N/C

 The exposed pad centered on the package bottom must be connected to RF and DC ground. Connecting all N/C pins to RF/DC Ground in the layout is also recommended.

\* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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## Electrical Specifications: $T_A$ =+25°C, $V_{CC} = V_{BUFFER} = 5.0 V^3$ , $Z_0 = 50 \Omega$

Parameter **Test Conditions** Units Min. Тур. Max. RF Port. 8.8 - 9.8 GHz 5 9 **Output Power** dBm RF/2 Port. 4.4 - 4.9 GHz -1 3 SSB Phase Noise RF Port, 10KHZ Offset -88 dBc/Hz V<sub>CC</sub>=V<sub>BUFFER</sub>=V<sub>TUNE</sub>=5V RF Port, 100KHZ Offset -115 Harmonics/Subharmonics RF Port, <sup>1</sup>/<sub>2</sub> F<sub>o</sub> -24 dBc V<sub>CC</sub>=V<sub>BUFFER</sub>=V<sub>TUNE</sub>=5V RF Port, 2 Fo -25 Pulling (Sensitivity to Match) RF Port, VSWR = 1.95:1 to 2.25:1 10.3 MHz pk-pk V<sub>CC</sub>=V<sub>BUFFER</sub>=V<sub>TUNE</sub>=5V Pushina RF Port,  $V_{TUNE} = 5 V$ 8 MHz/V RF/2 Port, V<sub>TUNE</sub> = 5 V (Sensitivity to Supply Voltage) 4 Frequency Drift Rate RF Port. 8.8 - 9.8 GHz 0.75 MHz/°C (Sensitivity to Temperature) RF/2 Port. 4.4 - 4.9 GHz 0.3 RF Port, 8.8 - 9.8 GHz 6 **Output Return Loss** dB RF/2 Port, 4.4 - 4.9 GHz 7 Tuning Sensitivity @ RF Port GHz/V 0.14  $V_{TUNE} = 5 V$ 185 205 I TOTAL (ICC + IBUFFER) Supply Current 165 175 Icc mΑ 20 30 **I**BUFFER **Tune Voltage** V 13 **V**<sub>TUNE</sub> 1 V<sub>TUNE</sub> = 13 V **Tuning Current Leakage** μA 5 10

3. VCO can operate over the 4.75 V to 5.25 V supply voltage range.

### Absolute Maximum Ratings <sup>4,5,6</sup>

Parameter	Absolute Maximum	
Supply Voltage (V <sub>CC</sub> & V <sub>BUFFER</sub> )	+5.5 Vdc	
V <sub>TUNE</sub>	0 to +15 Vdc	
Storage Temperature	-55°C to +150°C	
Operating Temperature	-40°C to +85°C	
Case Temperature (T <sub>C</sub> ) (measured @ exposed pad)	+100°C	
Junction Temperature <sup>7</sup>	+135°C	

- 4. Exceeding any one or combination of these limits may cause permanent damage to this device.
- 5. M/A-COM Technology Solutions does not recommend sustained operation near these survivability limits.
- Operating at nominal conditions with T<sub>J</sub> ≤ +135°C will ensure MTBF > 2.5 x 10<sup>6</sup> hours.
- Junction Temperature (T<sub>J</sub>) = T<sub>C</sub> + Θjc \* (V \* I) Typical thermal resistance (Θjc) = 35° C/W.
  a) For T<sub>C</sub> = 25°C, T<sub>J</sub> = 54°C @ 5 V, 165 mA
  - b) For T<sub>c</sub> = 85°C, T<sub>J</sub> = 115°C @ 5 V, 170 mA

#### 2

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### **Handling Procedures**

Please observe the following precautions to avoid damage:

#### **Static Sensitivity**

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.



ESD Rating: Class 1A

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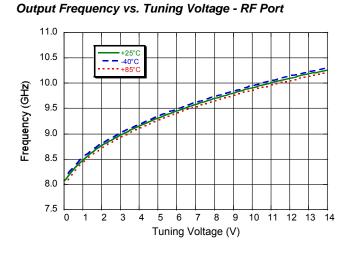
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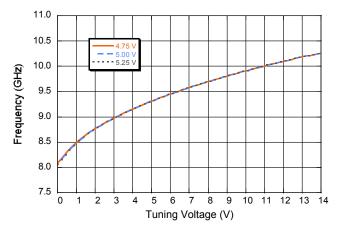
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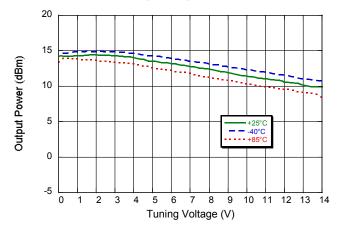
## Typical Performance Curves: $V_{CC} = V_{BUFFER} = 5V$ , $T_A = +25^{\circ}C$ (unless otherwise indicated)



**Output Frequency vs. Tuning / Supply Voltage - RF Port** 





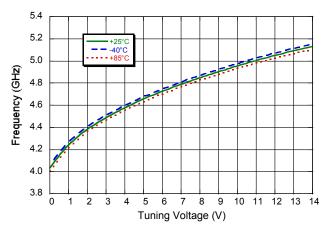


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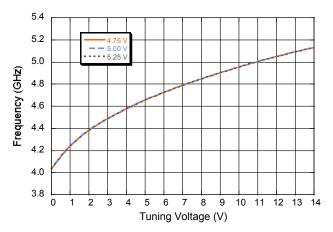
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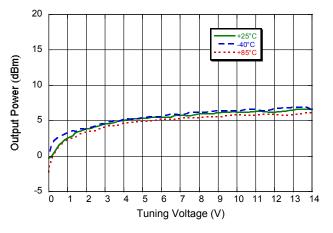
Output Frequency vs. Tuning Voltage - RF/2 Port



Output Frequency vs. Tuning / Supply Voltage - RF/2 Port



Output Power vs. Tuning Voltage - RF/2 Port



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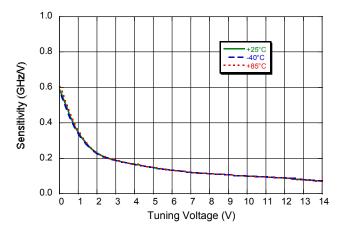


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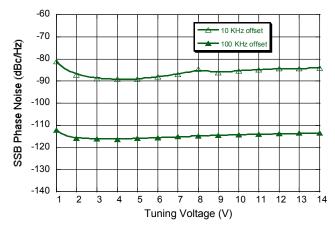
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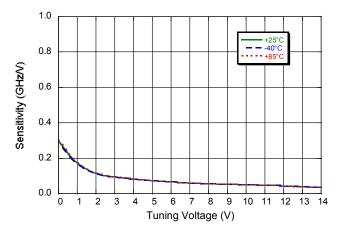
Frequency Sensitivity vs. Tuning Voltage - RF Port



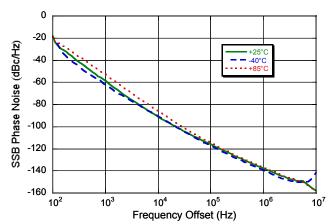
Single Side Band Phase Noise vs. Tuning Voltage RF Port



Frequency Sensitivity vs. Tuning Voltage - RF/2 Port



Single Side Band Phase Noise vs. Frequency Offset RF Port ( $V_{TUNE} = 5V$ )



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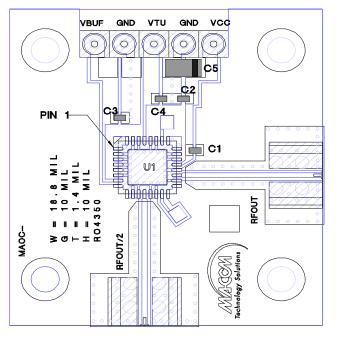
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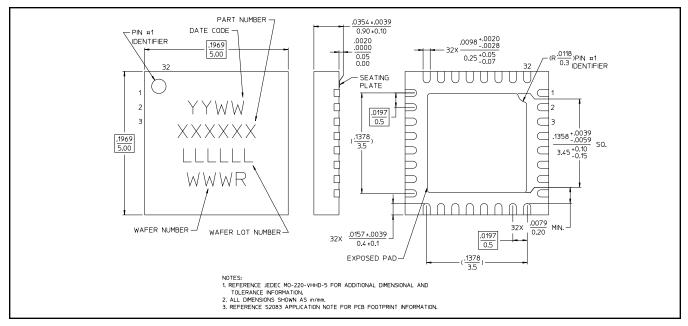
#### Sample Board



#### Parts List

Component	Value	Case Size
C1	100 pF	0402
C2, C3, C4	0.1 µF	0402
C5	10 µF Tantalum	1206

### Lead-Free 5 mm 32-Lead PQFN<sup>†</sup>



<sup>†</sup> Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin over copper.

#### 5

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