# **Triacs** Silicon Bidirectional Triode Thyristors

... designed primarily for industrial and consumer applications for full wave control of ac loads such as appliance controls, heater controls, motor controls, and other power switching applications.

- All Diffused and Glass-Passivated Junctions for Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal resistance and High Heat Dissipation
- Center Gate Geometry for Uniform Current Spreading
- Gate Triggering Guaranteed in Three Modes (MAC229 Series) or Four Modes (MAC229A Series)









### **MAXIMUM RATINGS** ( $T_J = 25^{\circ}C$ unless otherwise noted.)

Rating	Symbol	Value	Unit
Peak Repetitive Off-State Voltage <sup>(1)</sup> (T <sub>J</sub> = $-40$ to $110^{\circ}$ C	VDRM		Volts
1/2 Sine ave 50 to 60 Hz, Gate Open) MAC229-4, MAC229A4 MAC229-6, MAC229A6 MAC229-8, MAC229A8 MAC229-10, MAC229A10		200 400 600 800	
On-State RMS Current (T <sub>C</sub> = 80°C) Full Cycle Sine Wave 50 to 60 Hz	IT(RMS)	8	Amps
Peak Non–repetitive Surge Current (One Full Cycle 60 Hz, TJ = 110°C)	ITSM	80	Amps
Circuit Fusing (t = 8.3 ms)	l <sup>2</sup> t	26	A <sup>2</sup> s
Peak Gate Current (t $\leq$ 2 µs)	I <sub>GM</sub>	±2	Amps
Peak Gate Voltage (t $\leq$ 2 µs)	VGM	±10	Volts
Peak Gate Power (t $\leq 2 \mu$ s)	PGM	20	Watts
Average Gate Power (T <sub>C</sub> = 80°C, t $\leq$ 8.3 ms)	PG(AV)	0.5	Watts
Operating Junction Temperature Range	ТJ	-40 to 110	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to 150	°C
Mounting Torque		8	in. lb.

1. V<sub>DRM</sub> for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source (cont.) such that the voltage ratings of the devices are exceeded.



## **MAC229 Series MAC229A Series**

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	2.2	°C/W
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	60	°C/W

**ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$  and either polarity of MT2 to MT1 voltage unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak Blocking Current <sup>(1)</sup> (V <sub>D</sub> = Rated V <sub>DRM</sub> , Gate Open) $T_J = 25^{\circ}C$ $T_J = 110^{\circ}C$	IDRM	_		10 2	μA mA
Peak On-State Voltage (ITM = 11 A Peak, Pulse Width $\leqslant$ 2 ms, Duty Cycle $\leqslant$ 2%)	VTM	-	—	1.8	Volts
Gate Trigger Current (Continuous dc) $(V_D = 12 V, R_L = 100 \Omega)$ MT2(+), G(+); MT2(+), G(-); MT2(-), G(-) MT2(-), G(+) "A" SUFFIX ONLY	lgt	_		10 15	mA
	VGT	  0.2 0.2	 	2 2.5 —	Volts
Holding Current (V <sub>D</sub> = 12 Vdc, I <sub>TM</sub> = 200 mA, Gate Open)	Ч	-	—	15	mA
Gate-Controlled Turn-On Time (V <sub>D</sub> = Rated V <sub>DRM</sub> , I <sub>TM</sub> = 16 A Peak, I <sub>G</sub> = 30 mA)	<sup>t</sup> gt	-	1.5	—	μs
Critical Rate of Rise of Off-State Voltage (V <sub>D</sub> = Rated V <sub>DRM</sub> , Exponential Waveform, T <sub>C</sub> = 110°C)	dv/dt	-	25	_	V/µs
Critical Rate of Rise of Commutation Voltage ( $V_D$ = Rated $V_{DRM}$ , $I_{TM}$ = 11.3 A, Commutating di/dt = 4.1 A/ms, Gate Unenergized, $T_C$ = 80°C)	dv/dt(c)	_	5	_	V/µs

1. Ratings apply for open gate conditions. Devices shall not be tested with a constant current source for blocking voltage such that the voltage applied exceeds the rated blocking voltage.





## PACKAGE DIMENSIONS



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