Precision Sub-Miniature 5.0x3.2mm TCXO / VCTCXO Designed for Telecom Applications

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Description:

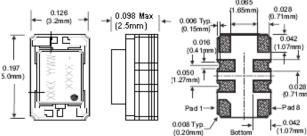
The Connor-Winfield 5.0x3.2mm Temperature Compensated Crystal Oscillators and Voltage Controlled Temperature Compensated Crystal Oscillators are designed for use in applications

requiring tight frequency stability in a small

package. Through the use of Analog Temperature Compensation, this device is capable of holding sub 1-ppm stabilities over wide temperature ranges.

0.028 (0.71mm) 0.126 0.083 Max 0.006 Typ (2.1mm) (3.2mm) (0.15m 0.042 0.016 8MHZ 9 (1.07mm) (0.4108 08 0 ŧ 0.197 0.050 (1.27mm) (5.0mm M602 N 0.028 1 (0.71m Pad 1 Pad 8 .0.042 (1.07mm) 0.008 Tvp Top View Dimensional Tolerance +/-0.005 (+/-0.127mm) (0.20m Bottom

Alternate Package Layout for Select Frequencies



Features:

- 3.3V Operation
- LVCMOS or clipped Sinewave Output Logic
- Sub-Miniature 5.0x3.2mm SMT Package
- Frequency Stabilities Available: ✓_{STRATUM 3} ±0.28 ppm with Stratum 3 Holdover ±0.50 ppm or ±1.00 ppm
- Temperature Ranges Available:
 - 0 to 70°C; 0 to 85°C; -20 to 70°C; -40 to 85°C
- Low Power <10mA
- Low Jitter <1pS RMS
- Low Phase Noise
- Tape and Reel Packaging
- RoHS Compliant / Lead Free VROHS
- Recommended for new designs

Applications:

- STRATUM 3 Applications
- GPS Receivers
- Instrumentation
- Femtocells
- FTTH, FTTC

Pad Connections Pad Connection 1: Volltage Control or N/C 2: Do Not Connect 3: Do Not Connect 4: Ground 5: Output Do Not Connect 6: 7: Do Not Connect 8: Supply, Vcc

Ordering Information

M	6		2	012.8M
Type: Precision TCXO VCTCXO 3.2x5.0mm	Temperature Range 3 = 0 to 85°C 5 = 0 to 70°C 6 = -40 to 85°C 7 = -20 to 70°C	1 = ±0.50 ppm	Features 2 = TCXO, LVCMOS, 3.3 Vdc 3 = TCXO, Clipped Sinewave, 3.3 Vdc 4 = VCTCXO, LVCMOS, 3.3 Vdc 5 = VCTCXO, Clipped Sinewave, 3.3 Vdc	Output Frequency Frequency Format -xxx.XM Min* -xxx.XM Max* *Amount of numbers after the decimal point. M = MHz

* See page 3 for frequency range information on each part number.

Example: M602-012.8M = 3.2x5mm, TCXO, LVCMOS, 3.3Vdc, -40° to 85°C, ±0.28ppm, Output Frequency 12.8MHz To order an M602 with an output frequency of: 6.4MHz = M602-006.4M Consult the factory for available frequencies



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1-ppm stabilities over

0.083 Max. → 1.2.1mm) → 0.005 Typ-Jik



Supply Voltage (Vcc)

Supply Current (Icc)

Integrated Phase Jitter (BW=12kHz to 20MHz)

SSB Phase Noise at 10Hz offset

SSB Phase Noise at 1KHz offset

SSB Phase Noise at 100Hz offset

SSB Phase Noise at >10KHz offset

SSB Phase Noise at >100KHz offset

Period Jitter

Start Up Time

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Vdc

mΑ

ps rms

ps rms

dBc/Hz

dBc/Hz

dBc/Hz

dBc/Hz

dBc/Hz

ms

Absolute Maximum Ratings					
Parameter	Minimum	Nominal	Maximum	Units	Notes
Storage Temperature	-55	-	85	°C	
Supply Voltage (Vcc)	-0.5	-	6.0	Vdc	
Input Voltage (Vc)	-0.5	-	Vcc + 0.5	Vdc	
	Operating Sp	ecifications			
Parameter	Minimum	Nominal	Maximum	Units	Notes
TCXO Frequency Calibration @ 25°C	-1.00	-	1.00	ppm	1
Supply Voltage Variation. (Vcc±5%)	-0.2	-	0.2	ppm	
Load Coefficient, ±5%	-0.2	-	0.2	ppm	
Static Temperature Hysteresis	-0.4	-	0.4	ppm	2
Aging First Year	-1.00	-	1.00	ppm	
Total Frequency Tolerance (20 Years)	-4.60	-	4.60	ppm	3

3.3

6

3

0.3

-90

-115

-135

-152

-154

-

3.465

10

5

1.0

-70

-100

-130

-145

-150

10

3.135

-

-

-

-

-

-

Input Characteristics for Voltage Control (Pad 1	Input	Characteristics	for Voltage	Control ((Pad 1)
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Parameter	Minimum	Nominal	Maximum	Units	Notes
Control Voltage Range ($Vcc = 3.3V$) (Vc)	0.3	1.65	3.0	Vdc	
Frequency Tuning measured @ 25°C	±10	-	-	ppm	5
Linearity	±5	-	-	%	
Slope	Positive				
Input Impedance	100K	-	-	Ohms	
Modulation Bandwidth (3dB)	10	-	-	KHz	

LVCMOS Output Characteristics					
Parameter	Minimum	Nominal	Maximum	Units	Notes
LOAD	-	15	-	pF	6
Voltage (High) (Voh)	90% Vcc	-	-	Vdc	
(Low) (Vol)	-	-	10% Vcc	Vdc	
Current (High) (loh)	-	-	-4	mA	
(Low) (IoI)	4	-	-	mA	
Duty Cycle at 50% of Vcc	45	50	55	%	
Rise / Fall Time 10% to 90%	-	-	8	ns	

Clipped Sinewave Output Characteristics

Parameter	Minimum	Nominal	Maximum	Units	Notes
LOAD	-	-	-		7
Output Load Resistance	-	10K	-	Ohms	6
Output Load Capacitance	-	10	-	pF	6
Output Voltage	1.00	-	-	V pk-pk	
Output Impedance	-	200	-	Ohms	

Notes:

1) TCXO: Initial calibration @ 25°C. Specifications at time of shipment after 48 hours of operation.

2) Frequency change after reciprocal temperature ramped over the operating range. Frequency measured before and after at 25°C.

3) Inclusive of calibration @ 25°C, frequency vs. change in temperature, change in supply voltage (±5%), load change (±5%), reflow soldering process and 20 years aging.

4) For best in application performance, careful selection of an external power source is critical. Select an external regulator that meets or exceeds to the following specifications regarding voltage regulation tolerance, initial accuracy, temperature coefficient, voltage noise, and low voltage noise density. Factory Test Conditions: Initial Accuracy ±2mv, Noise (0.1Hz to 10KHz) 15uV p-p, Voltage Noise Density = 50nV/ (Square root Hz), Temperature Coefficient <5ppm°C.

5) Additional pull ranges are available; please contact the factory for additional information.

6) Attention: To achieve optimal frequency stability, and in some cases to meet the specification stated on this datasheet, it is required that the circuit connected to this TCXO output must have the equivalent input capacitance that is specified by the nominal load capacitance. Deviations from the nominal load capacitance will have a graduated effect on the stability of approximately 20ppb per pF load difference.

7) Output is DC coupled.

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M705

Clipped Sinewave

Notes

Model Specifications

Model Number	M502	M503	M504	M505	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sinewa	ave
TCX0/VCTCX0	TCXO	TCX0	VCTCXO	VCTCXO	
Frequency Range		6.4 to 49.	152 MHz		
Frequency Stability		±0.28	opm		1
Supply Voltage		3.3V0	dc		
Temperature Range		0 to 70)°C		
Holdover Stability		±0.32	opm		2
Model Number	M302	M303	M304	M305	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sinewa	ave
TCXO/VCTCXO	TCXO	TCXO	VCTCXO	VCTCXO	
Frequency Range		6.4 to 49.	152 MHz		
Frequency Stability		±0.28p			1
Supply Voltage		3.3Vc	lc		
Temperature Range		0 to 85	5°C		
Holdover Stability		±0.32p	pm		2
Model Number	M512	M513	M514	M515	Notes
Output Type	LVCMOS	Clipped Sinewave		Clipped Sinewa	
TCXO/VCTCXO	TCXO	TCXO	VCTCXO	VCTCXO	
Frequency Range		6.4 to 49.	152 MHz		
Frequency Stability		±0.50p	opm		1
Supply Voltage		3.3V			
Temperature Range		0 to 70)°C		
Model Number	M312	M313	M314	M315	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sinewa	ave
TCXO/VCTCXO	TCXO	TCXO	VCTCXO	VCTCXO	
Frequency Range		6.4 to 49.	152 MHz		
Frequency Stability		±0.50p	pm		1
Supply Voltage		3.3Vc	lc		
Temperature Range		0 to 85	5°C		
Model Number	M522	M523	M524	M525	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sinewa	
TCX0/VCTCX0	TCXO	TCXO	VCTCXO	VCTCXO	
Frequency Range		6.4 to 52	MHz		
Frequency Stability		±1.00p	opm		1
Supply Voltage		3.3V	dc		
Temperature Range		0 to 70)°C		
Model Number	M322	M323	M324	M325	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sinewa	ave
TCXO/VCTCXO	TCXO	TCXO	VCTCXO	VCTCXO	
Frequency Range		6.4 to 52	MHz		
Frequency Stability		±1.00p	pm		1
Supply Voltage		3.3Vc	lc		
Temperature Range		0 to 85	5°C		

Output Type	LVUIVIUS	Clipped Sillewave	LVGIVIUS	Clipped Silleway	/e
TCXO/VCTCXO	TCX0	TCXO	VCTCXO	VCTCXO	
Frequency Range		6.4 to 49	.152 MHz		
Frequency Stability		±0.28p	opm		1
Supply Voltage		3.3V0	dc		
Temperature Range		-20 to 7	′0°C		
Holdover Stability		±0.32p	opm		2
	14000	14000	1400.4	14005	
Model Number	M602	M603	M604	M605	Notes
Output Type	LVCMOS	Clipped Sinewave		Clipped Sineway	/e
	TCXO	TCXO	VCTCXO	VCTCXO	
Frequency Range	6.4 to 49.152			2 6.4 to 49.152	
Frequency Stability			8ppm		1
Supply Voltage			Vdc		
Temperature Range			0 85°C		
Holdover Stability		±0.3	2ppm		2
	N710	N710			Niete -
Model Number	M712	M713	M714	M715	Notes
Output Type	LVCMOS	Clipped Sinewave		Clipped Sineway	/e
	TCXO	TCX0	VCTCX0	VCTCXO	
Frequency Range			.152 MHz		
Frequency Stability		<u>+0.50</u>			1
Supply Voltage		3.3V0			
Temperature Range		-20 to 7			
Model Number	M612	M613	M614	M615	Notes
Output Type	LVCMOS	Clipped Sinewave		Clipped Sineway	/e
TCXO/VCTCXO	TCXO	TCXO	VCTCXO	VCTCXO	
Frequency Range	6.4 to 60	6.4 to 49.152		52 6.4 to 49.152	
Frequency Stability			Oppm		1
Supply Voltage			Vdc		
Temperature Range		-40 to) 85°C		
	14700	1.1=00			
Model Number	M722	M723	M724	M725	Notes
Output Type	LVCMOS	Clipped Sinewave		Clipped Sineway	/e
	TCXO	TCXO	VCTCXO	VCTCXO	
Frequency Range		6.4 to 52			
Frequency Stability		±1.00p			1
Supply Voltage		3.3Vo			
Temperature Range		-20 to 7	′0°C		
Model Number	M622	M623	M624	M625	Notes
Output Type	LVCMOS	Clipped Sinewave	LVCMOS	Clipped Sineway	/e
TCXO/VCTCXO	TCX0	TCXO	VCTCXO	VCTCXO	
Frequency Range		6.4 to 52	2 MHz		
Frequency Stability		±1.0	0ppm		1
Supply Voltage		2.2	Vdc		

3.3Vdc

-40 to 85°C

Model Specifications

M704

M703

LVCMOS Clipped Sinewave LVCMOS

M702

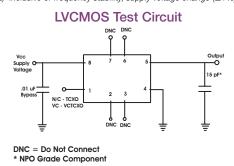
Model Number

Output Type

Notes:

1) Frequency stability vs. change in temperature. [±(Fmax - Fmin)/2.Fo].

2) Inclusive of frequency stability, supply voltage change $(\pm 1\%)$, aging, for 24 hours.



Clipped Sinewave Test Circuit

* NPO Grade Component

Supply Voltage Temperature Range

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Environmental Characteristics

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"0" LEVEL

SYMMETRY

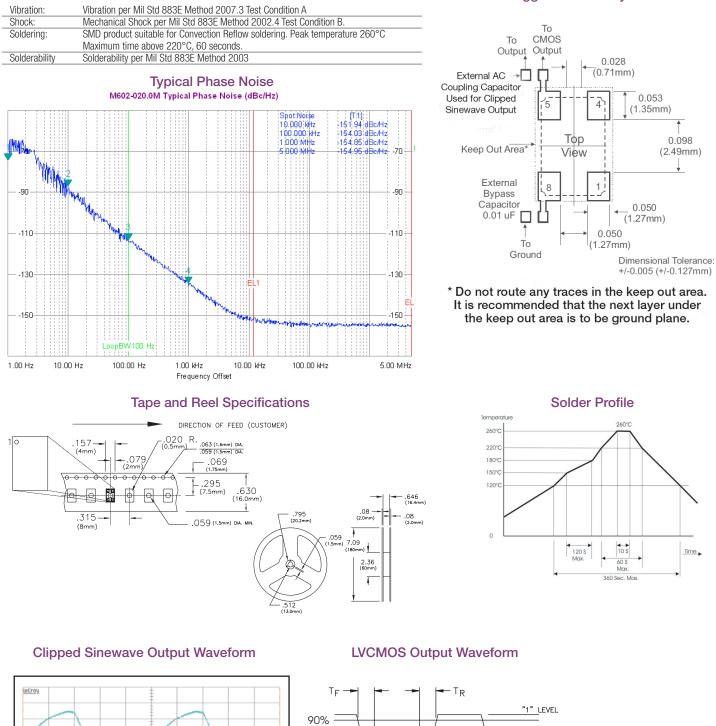
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Suggested Pad Layout



50%

10%

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10ns 200mV