# UNISONIC TECHNOLOGIES CO., LTD

# M54134

# LINEAR INTEGRATED CIRCUIT

# EARTH LEAKAGE CURRENT **DETECTOR**

#### **DESCRIPTION**

The UTC M54134 is a semiconductor integrated circuit developed for use in high-speed earth leakage breakers incorporating functions to protect against voltage surges and inverter noise.

## **FEATURES**

- \* Improvement of ability against unwanted tripping by lightning-surge and lightning impulse.
- Two times counting system adopted.
- \* Improvement of ability against unwanted tripping by inverter-noise.

Built-in operational amplifier (of low current dissipation) for active low-pass filter.

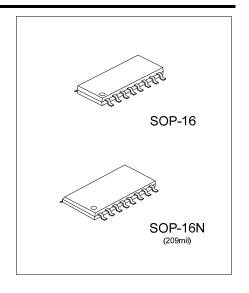
Improved high-frequency, high harmonic superposition performance

- \* Internal time delay function
- \* An external capacitor is used to set the delay time.
- \* High input sensitivity: V<sub>T</sub>=11.5mVrms Typ.
- \* Low-current dissipation (at R<sub>IREF</sub>=180kΩ)

In stand-by condition: I<sub>S</sub>=610µA Typ.

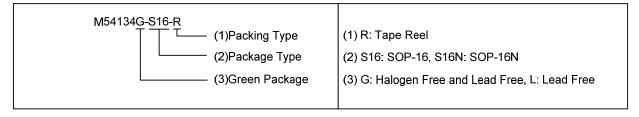
\* High stabilities design

Adopt the circuits that is not affected by fluctuations of supply voltage/ambient temperature.

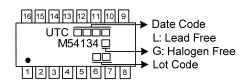


## ORDERING INFORMATION

| Ordering Number |                | Dealeana | Dooking   |  |
|-----------------|----------------|----------|-----------|--|
| Lead Free       | Halogen Free   | Package  | Packing   |  |
| M54134L-S16-R   | M54134G-S16-R  | SOP-16   | Tape Reel |  |
| M54134L-S16N-R  | M54134G-S16N-R | SOP-16N  | Tape Reel |  |

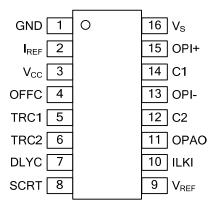


#### **MARKING**



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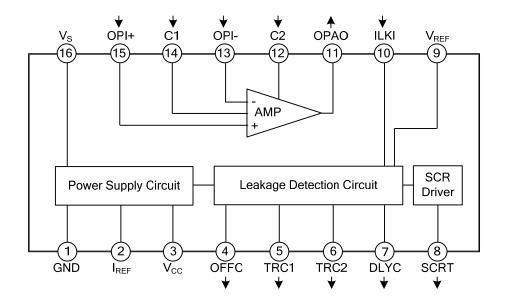
# **■ PIN CONFIGURATION**



# **■ PIN DESCRIPTION**

| PIN NO. | PIN NAME         | DESCRIPTION  |
|---------|------------------|--|
| 1       | GND              | Grounding  |
| 2       | I <sub>REF</sub> | Pin for connecting resistor that sets constant current for internal circuits; approx. 1.3 V.   |
| 3       | V <sub>CC</sub>  | Output pin of the internal constant-voltage circuit. Connect decoupling capacitor.   |
| 4       | OFFC             | Leakage input signal does not continue.  Leakage is detected and SCR turn on.  In these cases, this IC will be restored to the initial condition after a predetermined time. Connect capacitor that determines restore time. |
| 5       | TRC1             | Pin for connecting capacitor that integrates signal output from discriminator of leak-signal input level.  |
| 6       | TRC2             | Pin for connecting capacitor to eliminate noise.   |
| 7       | DLYC             | Pin for connecting capacitor that sets delay time in case of using delay function.   |
| 8       | SCRT             | Output pin for driving a SCR.  |
| 9       | $V_{REF}$        | Pin for providing input reference level of leakage detection. About 2.4V appears.  |
| 10      | ILKI             | Other input pin of leakage detection.  |
| 11      | OPAO             | Output pin of operational amplifier.   |
| 12      | C2               | Pin for connecting capacitor that prevents abnormal oscillations. Connect capacitor across IC at pins 11 and 12.   |
| 13      | OPI-             | Negative input pins of operational amplifier   |
| 14      | C1               | Pin for connecting capacitor that prevents noise from causing malfunction. Connect capacitors across IC at pins 13 and 14 and across IC at pins 15 and 14.   |
| 15      | OPI+             | Positive input pins of operational amplifier   |
| 16      | VS               | Power supply   |

# ■ BLOCK DIAGRAM



# ■ **ABSOLUTE MAXIMUM RATING** (T<sub>A</sub>=25°C, unless otherwise noted)

| PARAMETER                               |  | SYMBOL           | RATINGS         | UNIT |  |
|---|--|------------------|-----------------|------|--|
| Maximum Supply Voltage                  |  | $V_{S(MAX)}$     | 15              | V    |  |
| Differential Input Voltage OPI+ to OPI- |  | $V_{ID}$         | -0.8 ~ +0.8     | V    |  |
| Supply Current                          |  | Is               | 4               | mA   |  |
| Differential Input Current OPI+ to OPI- |  | I <sub>IOP</sub> | -5 ~ <b>+</b> 5 | mA   |  |
| Input Current V <sub>REF</sub> to GND   |  | I <sub>IG</sub>  | 10              | mA   |  |
| Power Dissipation                       |  | $P_{D}$          | 200             | mW   |  |
| Operating Ambient Temperature           |  | $T_{OPR}$        | -20 ~ +85       | °C   |  |
| Storage Temperature                     |  | $T_{STG}$        | -55 ~ +125      | °C   |  |

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

## ■ RECOMMENDED OPERATING CONDITIONS

| PARAMETER            | SYMBOL | RATINGS | UNIT |
|----------------------|--------|---------|------|
| Supply Voltage Range | Vs     | 7 ~ 12  | V    |

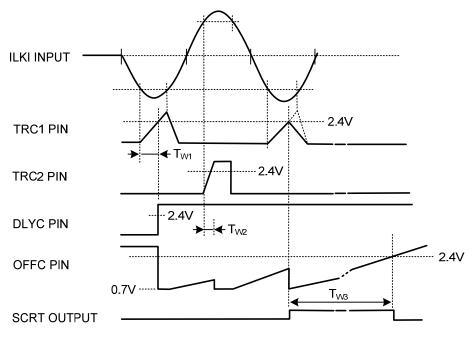
# ■ ELECTRICAL CHARACTERISTICS (T<sub>A</sub>=25°C, V<sub>S</sub>=9V, unless otherwise noted)

| DADAMETED   | CVMDOL              | TECT CONDITIONS                              | NAINI  | TVD  | NAAV | LINIT    |
|---|---------------------|--|--------|------|------|----------|
| PARAMETER   | SYMBOL              | TEST CONDITIONS                              | IVIIIN | TYP  | WAX  | UNIT     |
| POWER SUPPLY CIRCUIT                              | 1                   | 1  | 1      | 10.0 | T    | ·        |
| Maximum Supply Voltage                            | V <sub>S(MAX)</sub> | I <sub>S</sub> =4mA                          | -      | 13.9 | 15   | V        |
| / <sub>CC</sub> -Pin Output Voltage               | V <sub>CC</sub>     | I <sub>OH</sub> =-1mA                        |        | 5.2  |      | V        |
| Supply Current (In Standby)                       | I <sub>S0</sub>     |  | 520    | 610  | 700  | μΑ       |
| Supply Current (While Detecting Leakage)          | I <sub>S1</sub>     |  | 560    | 650  | 740  | μΑ       |
| Supply Current (Immediately after Drive a SCR)    | l <sub>S2</sub>     |  | 480    | 570  | 660  | μΑ       |
| Ambient Temperature Dependence of I <sub>S0</sub> |                     | T <sub>A</sub> =-25~+85°C                    |        | -0.2 |      | %/°C     |
| OPERATIONAL AMPLIFIER                             | _                   |  |        |      | ā    | ā.       |
| Differential Input Clamp Voltage                  | V <sub>IC</sub>     | I <sub>IDC</sub> =±4mA                       |        | ±0.8 |      | V        |
| DPOA-Pin "H" Output Current                       | I <sub>OH</sub>     |  |        | 2.8  |      | mΑ       |
| DPOA-Pin "L" Output Current                       | I <sub>OL</sub>     |  |        | 8.0  |      | mΑ       |
| nput Bias Current                                 | I <sub>IC</sub>     |  |        | 125  |      | nA       |
| /oltage Gain                                      | G <sub>V</sub>      | f=1kHz                                       |        | 40   |      | dB       |
| Frequency Band Width                              | Bw                  | -3dB   |        | 6    |      | kHz      |
| Maximum Output Voltage                            | Vo                  |  |        | 3.5  |      | $V_{PP}$ |
| Output Offset Voltage                             | V <sub>O(OFF)</sub> |  |        | 0    |      | mV       |
| EAK DETECTOR CIRCUIT                              |                     |  |        |      |      |          |
| / <sub>REF</sub> -Pin Output Voltage              | Vo                  | I <sub>OH</sub> =-200μA                      |        | 2.4  |      | V        |
| / <sub>REF</sub> -GND Clamp Voltage               | V <sub>RCL</sub>    | I <sub>RCL</sub> =5mA                        |        | 4.7  |      | V        |
| 20 land 1/-14- and                                |                     | \\/\frac{1}{2} \\                            |        | ±14. |      | mVd      |
| OC Input Voltage of Leakage Detection             | $V_{I(ON)}$         | With respect to V <sub>REF</sub>             |        | 0    |      | С        |
| LKI-Pin Input Bias Current                        | I <sub>IH</sub>     | V <sub>IN</sub> =V <sub>REF</sub>            |        | 220  |      | nA       |
| B-mS CIRCUIT                                      |                     |  |        |      |      |          |
| FRC1 threshold voltage                            | V <sub>TH1</sub>    |  |        | 2.4  |      | V        |
| Accuracy of TRC1-Pin "H" Output Current           | E <sub>IOH1</sub>   | V <sub>O</sub> =0V, I <sub>OH1</sub> =-7.6μA | -10    |      | +10  | %        |
| Accuracy of T <sub>W1</sub> pulse width           | E <sub>TW1</sub>    | C=0.01µf, T <sub>W1</sub> =3ms               | -15    |      | +15  | %        |
| Ambient Temperature Dependence of T <sub>W1</sub> |                     | T <sub>A</sub> =-20~+85°C                    |        | 0    |      | %/°C     |

# ■ ELECTRICAL CHARACTERISTICS (Cont.)

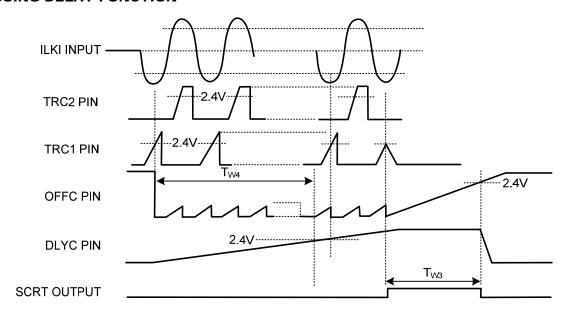
| PARAMETER  | SYMBOL   | TEST CONDITIONS                             |                       | MIN  | TYP  | MAX | UNIT  |
|--|--|---|-----------------------|------|------|-----|-------|
| 1-mS CIRCUIT                                     |  |   |                       |      |      |     |       |
| TRC2 Threshold Voltage                           | $V_{TH2}$  |   |                       |      | 2.4  |     | V     |
| Accuracy of TRC2-pin "H" Output Current          | E <sub>IOH2</sub>  | V <sub>O</sub> =0V, I <sub>OH2</sub>        | =-7.6µA               | -10  |      | +10 | %     |
| Accuracy of TW2 Pulse Width                      | E <sub>TW2</sub>   | C=0.0047µF, T <sub>W2</sub> =1.5ms          |                       | -15  |      | +15 | %     |
| Ambient Temperature Dependence of V <sub>T</sub> |  | T <sub>A</sub> =+25°C~+85°C                 |                       |      | -8.0 |     | %     |
| Ambient Temperature Dependence of V†             |  | T <sub>A</sub> =+25°C~                      | -20°C                 |      | +2.0 |     | %     |
| Ambient Temperature Dependence of Tw2            |  | T <sub>A</sub> =-20~+85                     | 5°C                   |      | 0    |     | %/°C  |
| Total AC Input Voltage of Leakage Detection      | otal AC Input Voltage of Leakage Detection V <sub>T</sub> 60Hz |   |                       |      | 11.5 |     | mVrms |
| Reset Circuit                                    |  |   |                       |      |      |     |       |
| OFFC Threshold Voltage                           | $V_{TH}$   |   |                       |      | 2.4  |     | V     |
| Accuracy of OFFC-pin "H" Output Current          | E <sub>IOH</sub>   | V <sub>O</sub> =0V, I <sub>OH</sub> =-7.6μA |                       | -10  |      | +10 | %     |
| Accuracy of Reset Time Pulse Width               | E <sub>TW3</sub>   | C=0.33µF, T <sub>W3</sub> =75ms             |                       | -30  |      | +30 | %     |
| Delay Circuit                                    |  |   |                       |      |      |     |       |
| DLYC Threshold Voltage                           | V <sub>TH</sub>  |   |                       | 2.4  |      | V   |       |
| Accuracy of DLYC-pin "H" Output Current          | E <sub>IOH</sub>   | V <sub>O</sub> =0V, I <sub>OH</sub> =-7.6μA |                       | -10  |      | +10 | %     |
| Accuracy of Delay Timer Pulse Width              | E <sub>TW4</sub>   | C=1.0µF, T <sub>W4</sub> =300ms             |                       | -30  |      | +30 | %     |
| SCR Driver Circuit                               |  |   |                       |      |      |     |       |
| SCRT-Pin "L" Output Voltage                      | $V_{OL8}$  | I <sub>OL</sub> =200μA                      |                       |      | 0.1  | 0.2 | V     |
| Supply Voltage for I <sub>OH</sub> Hold          | $V_{S(OFF)}$   |   |                       |      | 3.0  | 4.0 | V     |
|  | I <sub>OHC</sub>   |   | T <sub>A</sub> =-20°C | -100 | -160 |     | μA    |
| SCRT-Pin "H" Output Current                      | I <sub>OHN</sub>   | V <sub>O</sub> =8V                          | T <sub>A</sub> =+20°C | -50  | -130 |     | μA    |
|  | I <sub>OHH</sub>   |   | T <sub>A</sub> =+85°C | -33  | -100 |     | μΑ    |

# ■ WITHOUT DELAY FUNCTION



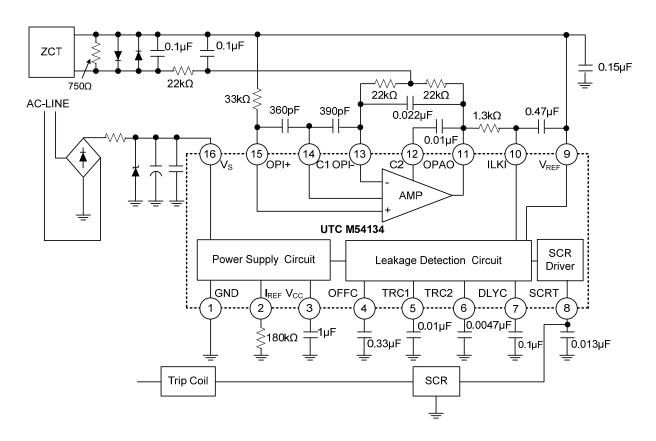
Without Delay Function

# ■ USING DELAY FUNCTION



**Using Delay Function** 

## **■ TYPICAL APPLICATION CIRCUIT**



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