# RENESAS

M51981ML/SL

# Voltage Detecting, System Resetting IC Series

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### Description

M51981ML/SL are semiconductor integrated circuits designed for detecting supply voltage and resetting all types of logic circuits such as CPUs.

They fined extensive applications, including battery checking circuit, level detecting circuit and waveform shaping circuit.

### Features

- Few external parts
- Low threshold operating voltage (Supply voltage to keep low-state at low supply voltage): 0.6 V (Typ) at  $R_L = 22 \text{ k}\Omega$
- Wide supply voltage range: 2 V to 17 V
- Wide application range

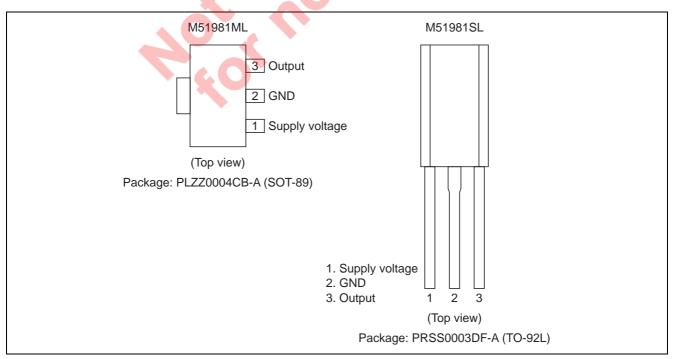
#### Application

• Reset circuit of Pch, Nch, CMOS, microcomputer, CPU and MCU, Reset of logic circuit, Battery check circuit, switching circuit back-up voltage, level detecting circuit, waveform shaping circuit, delay waveform generating circuit, DC/DC converter, over voltage protection circuit

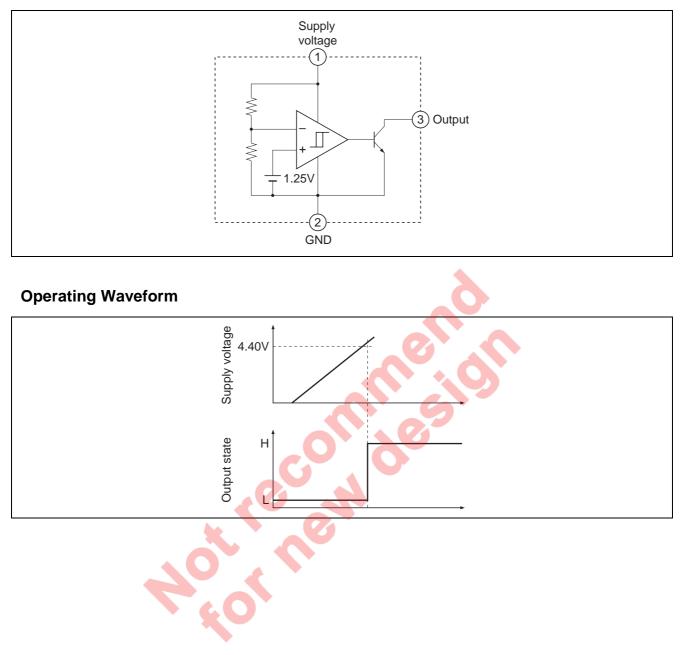
### **Recommended Operating Condition**

• Supply voltage range: 2 V to 17 V

### Pin Arrangement



## **Block Diagram**



### Absolute Maximum Ratings

				$(Ta = 25^{\circ}C, unless)$	otherwise noted)	
Item	Symbol	Ratings	Unit	Conditions		
Supply voltage	V <sub>CC</sub>	18	V			
Output sink current	lsink	6	mA			
Output voltage	Vo	18	V	Output with constant current load		
Power dissipation	Pd	700	mW	SL: TO-92L ML: SOT-89		
		500				
Thermal derating	Κθ	7	mW/°C	Ta ≥ 25°C	SL: TO-92L	
		5			ML: SOT-89	
Operating temperature	Topr	-30 to +85	°C			
Storage temperature	Tstg	-40 to +125	°C			

### **Electrical Characteristics**

 $(Ta = 25^{\circ}C, unless otherwise noted)$ 

Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Detecting voltage	Vs	4.20	4.40	4.60	V		
Hysteresis voltage	$\Delta V_{S}$	30	50	80	mV		
Detecting voltage temperature coefficient	V <sub>S</sub> /∆T	_	0.01	—	%/°C		
Circuit current	Icc		340	510	μA	V <sub>CC</sub> = 5V	
Output saturation voltage	Vsat	_	0.2	0.4	V	V <sub>cc</sub> = 4V, Isink = 4mA	
Threshold operating	V <sub>OPL</sub>		0.67	0.8	v	L reset type minimum supply	$R_{\text{L}} = 2.2 \text{k} \Omega, \text{ Vsat} \leq 0.4 \text{V}$
voltage		-	0.55	0.7		voltage for IC operation	$R_L$ = 100k $\Omega$ , Vsat $\leq 0.4V$
Output leakage current	I <sub>OH</sub>	_	Ō	30	nA		
Propagation delay time	t <sub>PHL</sub>	<	6		μS	Response time when V <sub>CC</sub> changes $H \rightarrow L$ Response time when V <sub>CC</sub> changes $L \rightarrow H$	
	t <sub>PLH</sub>		3	4			

### **Example of Application Circuit**

#### Reset Circuit of M51981

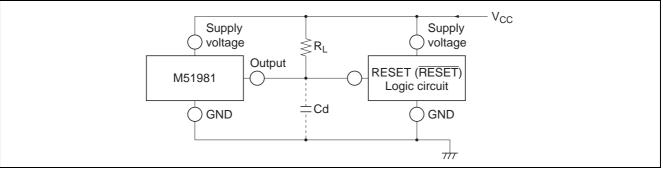
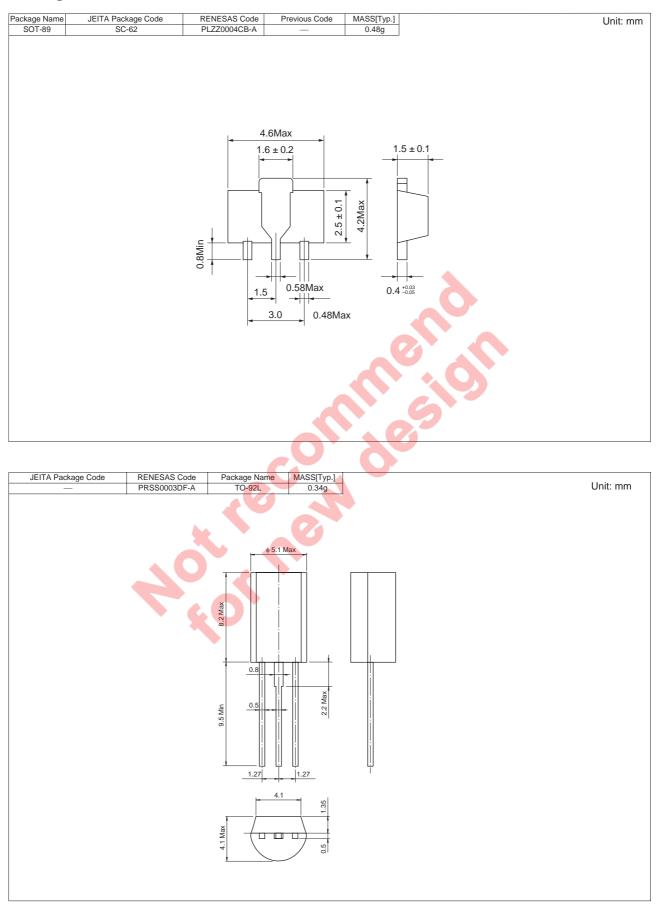


Figure 1 Reset Circuit of M51981

Note: 1. The logic circuit preferably should not have a pull-down resistor, but if one is present, add load resistor  $R_L$  to overcome the pull-down resistor.

### **Package Dimensions**



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