



### LV78D Series 3.3 V LVDS Clock Oscillators

January 2008

#### This device is obsolete, January 2008 This is replaced by the LV98xxDV device For new designs use the LV93xxDV device

- Pletronics' LV78D Series is a quartz crystal controlled precision square wave generator with an LVDS output.
- FR4 base with a mechanical metal cover.
- Solder pad compatible with many 9x14mm plastic J lead packages.
- Has internal bypass capacitor on the Vcc lead
- Tape and Reel or cut tape packaging is available.
- 80 to 250 MHz
- 9.04mm x 8.91mm (S package)
- Enable/Disable Function on pad 2 (see LV76D for E/D on pad 1)
- Disable function includes low standby power mode
- 3<sup>rd</sup> Overtone Crystals used
- Low Jitter
- 5x7 mm LCC ceramic oscillator inside

# Pletronics Inc. certifies this device is in accordance with the RoHS 5/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead, Mercury, PBB's, PBDE's Weight of the Device: 0.4 grams Moisture Sensitivity Level: 1 As defined in J-STD-020C Second Level Interconnect code: e4

#### **Absolute Maximum Ratings:**

Parameter	Unit
V <sub>cc</sub> Supply Voltage	-0.5V to +5.0V
Vi Input Voltage	-0.5V to V <sub>cc</sub> + 0.5V
Vo Output Voltage	-0.5V to V <sub>cc</sub> + 0.5V

#### **Thermal Characteristics**

The maximum die or junction temperature is 155°C The thermal resistance junction to board is 60 to 100°C/Watt depending on the solder pads, ground plane and construction of the PCB.



### LV78D Series 3.3<sup>w</sup>V<sup>taSheet4U.com</sup> LVDS Clock Oscillators

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Part N	lum	ber	:					
LV78	45	D	Ε	V	-125.0M	-XX		Part Marking:
							<b>Packaging code or blank</b> T250 = 250 per Tape and Reel T500 = 500 per Tape and Reel T1K = 1000 per Tape and Reel	PLE LV78D <i>FF.FFF</i> M • <i>YMDXX</i>
							Frequency in MHz	or
							Supply Voltage V <sub>cc</sub> V = $3.3V \pm 10\%$	LV78DX <i>FF.FFF</i> M PLE XX
							<b>Optional Enhanced OTR</b> <b>Blank</b> = Temp. range -10 to +70°C <b>E</b> = Temp. range -40 to +85°C	• YYWWXX
							Series Model	
							Frequency Stability 45 = ± 50 ppm 44 = ± 25 ppm 20 = ± 20 ppm	
							Series Model	

#### Marking Legend:

PLE = Pletronics

FF.FFFM = Frequency in MHz

YYWW or YWW or YMD = Date of Manufacture (year and week, or year-month-day) All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

#### Codes for Date Code YMD

Code	6	7	8	9		0	1		2	
Year	2006	2007	2008	2009		2010	201	1	2012	
									-	
		_		_	_	_	-		Ι.	

Code	~	Ъ	C	U	E	Г	G	п	J	n	L	M
Month J	AN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	Α	В	С
Day	1	2	3	4	5	6	7	8	9	10	11	12
Code	D	E	F	G	Н	J	К	L	М	Ν	Р	R
Day	13	14	15	16	17	18	19	20	21	22	23	24
Code	т	U	V	w	Х	Y	Z					
Day	25	26	27	28	29	30	31					

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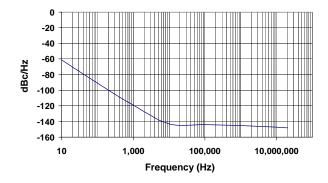
Electrical Specification	for 3.30	)V <u>+</u> 10%	over th	e specified temperature range				
Item	Min	Max	Unit	Condition				
Frequency Range	80	250	MHz					
Frequency Accuracy "45"	-50	+50	ppm	For all supply voltages, load changes, aging for 1 year, shock, vibration and temperatures				
"44"	-25	+25		r year, shock, vibration and temperatures				
<b>"20</b> "	-20	+20						
Output Waveform		LVDS						
Output High Level		1.60	Volts	See load circuit R1 = 50 ohms				
Output Low Level	0.90		Volts	See load circuit R1 = 50 ohms				
Differential Output (V <sub>OD</sub> )	247	454	mVolts	See load circuit R1 = 50 ohms				
Output Offset Voltage (V <sub>os</sub> )	1.125	1.375	Volts	See load circuit R1 = 50 ohms				
Differential Output Error $(dV_{os})$		50	mVolts	See load circuit R1 = 50 ohms				
Output Symmetry	45	55	%	Referenced to 50% of amplitude or crossing point				
Output $T_{RISE}$ and $T_{FALL}$	300	700	pS	Vth is 20% and 80% of waveform				
Jitter	-	0.15	pS RMS	Measured from 12KHz to 20MHz from Fnominal				
	-	2.8		Measured from 10Hz to 1MHz from Fnominal				
Vcc Supply Current	-	66	mA	Includes current of properly terminated device				
Enable/Disable Internal Pull-up	50	-	Kohm	To Vcc (equivalent resistance)				
V disable	-	0.8	Volts	Referenced to Ground				
V enable	2.0	-	Volts	Referenced to Ground				
Output leakage V <sub>OUT</sub> = V <sub>CC</sub>	-10	+10	uA	Pad 1 low, device disabled				
V <sub>OUT</sub> = 0V	-10	+10	uA					
Enable	-	10	nS	Time for output to reach a logic state				
Disable time	-	10	nS	Time for output to reach a high Z state				
Start up time	-	5	mS	Measured from the time Vcc = 3.0V				
Operating Temperature Range	-10	+70	°C	Standard Temperature Range				
	-40	+85	°C	Extended Temperature Range "E" Option				
Storage Temperature Range	-55	+125	°C					
Standby Current I <sub>cc</sub>	-	3	uA	Pad 1 low, device disabled				

Specifications with Pad 1 E/D open circuit

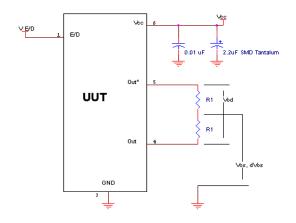
#### **Typical Phase-Noise Response**



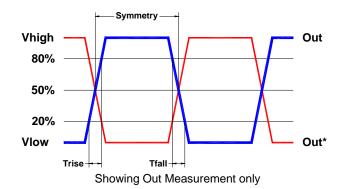
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Load Circuit









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#### Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

#### **ESD** Rating

Model	Minimum Voltage	Conditions		
Human Body Model	1500	MIL-STD-883 Method 3115		
Charged Device Model	1000	JESD 22-C101		

#### Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

P/N:	LV7845DV-	
Custor	mer P/N:	
Qty:	1234	D/C

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

**RoHS** Compliant

2nd LvL Interconnect Category=e4 Max Safe Temp=245C for 10s 2X Max

#### Layout and application information

Recommend connecting Pad 1 and Pad 2 together to permit the design to accept Enable/Disable on both input pads (see LV76D for E/D on pad 1)

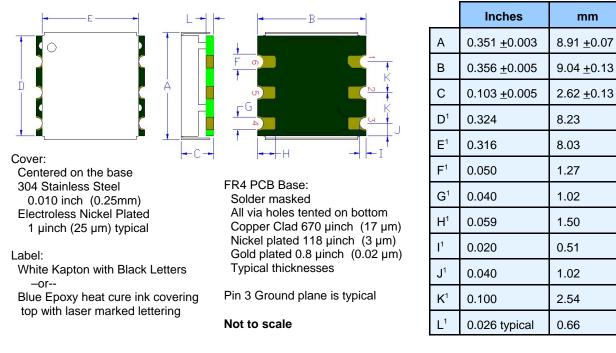
For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



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#### **Mechanical:**



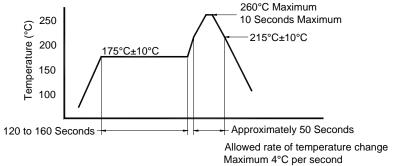
- The package is not hermetically sealed (the crystal unit inside is hermetically sealed).
- The sides are intentionally left open to permit cleaning material to freely flow in the package, thus minimizing the accumulation of contaminants during cleaning processes.
- The internal part of the package must be thoroughly dry before operating.

Pad	Function	Note
1	No connect	There is no internal connection to this pad
2	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is <0.30 volts, the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{CC}$ if the oscillator is to be always on.
3	Ground (GND)	
4	Output	The outputs must be terminated, 100 ohms between the outputs is the ideal
5	Output*	termination.
6	Supply Voltage (V <sub>cc</sub> )	Recommend connecting appropriate power supply bypass capacitors as close as possible.



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#### Reflow Cycle (typical for lead free processing)



The part may be reflowed 2 times without degradation.

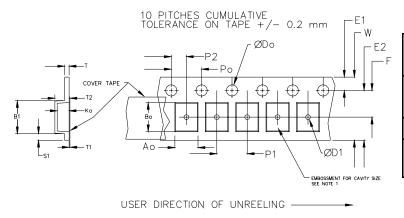
#### Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

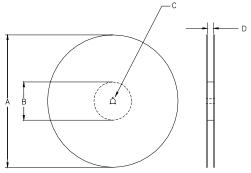
Not to scale

	Constant Dimensions Table 1											
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max				
8mm		1.0			2.0							
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05							
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1				
24mm		1.5			<u>+</u> 0.1							

	Variable Dimensions Table 2											
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko					
24 mm	9.88	22.25	11.5 <u>+</u> 0.1	16.0 <u>+</u> 0.1	3.22	24.3	Note 1					

Note 1: Embossed cavity to conform to EIA-481-B Dimensions in mm





		RE			
А	inches	7.0	10.0	13.0	
	mm	177.8	254.0	330.2	
в	inches	2.50	4.00	3.75	
	mm	63.5	101.6	95.3	Tape Widt
с	mm	13.0 +0.5 / -0.2			h
D	mm			24.4 +2.0 -0.0	24.0

Reel dimensions may vary from the above



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