



Spec No.: DS30-2011-0026 Effective Date: 07/08/2011

Revision: A

LITE-ON DCC

RELEASE

BNS-OD-FC001/A4

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LED DISPLAY

LTP-7357KS DATA SHEET

Rev	Description	Ву
01	RDR Original Spec	Phanomkorn J. March 18, 2009
02	Add height of reflector's wall for protects splash of epoxy Due to adjusting the epoxy ratio for narrow the bin grade	Phanomkorn J. December 23, 2010
-	NPPR Original Spec	Phanomkorn J. February 16, 2011
A	Revise the product width's tolerance on Page 2 of 6 from 12.6mm ±0.1mm to 12.6mm +0.18/-0.25mm	Phanomkorn J. June 02, 2011

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Customer Approval					
Date					

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FEATURES

- *0.678 inch (17.22 mm) MATRIX HEIGHT.
- *LOW POWER REQUIREMENT.
- * SINGLE PLANE, WIDE VIEWING ANGLE.
- * SOLID STATE RELIABILITY.
- *5×7 ARRAY WITH X-Y SELECT.
- *COMPATIBLE WITH USASCII AND EBCDIC CODES.
- *STACKABLE HORIZONTALLY.
- *CATEGORIZED FOR LUMINOUS INTENSITY.
- *LEAD-FREE PACKAGE(ACCORDING TO ROHS)

DESCRIPTION

The LTP-7357KS is a 0.678-inch (17.22 mm) matrix height 5x7 dot matrix display. This device utilizes AlInGaP Yellow LED chips, which are made from AlInGaP on a non-transparent GaAs substrate, and has a gray face and white dot color.

DEVICE

PART NO.	DESCRIPTION		
AlInGaP Yellow	CATHODE COLUMN		
LTP-7357KS	ANODE ROW		

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PACKAGE DIMENSIONS PART NO. DATE CODE BIN CODE 2.54X5=12.7 [0.500] 17.72 [0.698] 2.54 [0.100] 2.54X4=10.16[0.400] 2.52 [0.099] +0.18 12.6[0.496] -0.25 12.19±0.5 [.48±.02] 2.54[0.100] 1.50[.059] full R 0.92±0.05 2.54[0.100] 2.41[0.095]-REF 10.19[0.401] non-symmetric 2.54[0.1 7.65[0.301] Ø0.51±0.08 0.46[.018] [Ø.02±.003] .54[.100] 2.54[.100] non-symmetric

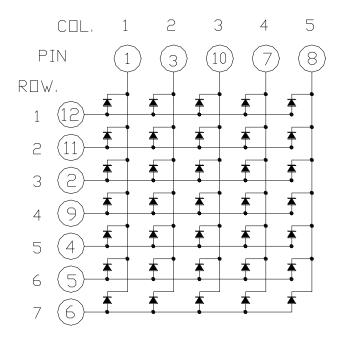
NOTES: 1. All dimensions are in millimeters. Tolerances are \pm 0.25 mm (0.01") unless otherwise noted.

- 2. Pin tip's shift tolerance is \pm 0.4 mm.
- 3. Foreign material on segment ≤ 10 mils
- 4. Ink contamination (surface) ≤ 20 mils
- 5. Bending $\leq 1/100$
- 6. Bubble in segment ≤ 10 mils

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INTERNAL CIRCUIT DIAGRAM



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PIN CONNECTION

No.	CONNECTION				
1	CATHODE COLUMN 1				
2	ANODE ROW 3				
3	CATHODE COLUMN 2				
4	ANODE ROW 5				
5	ANODE ROW 6				
6	ANODE ROW 7				
7	CATHODE COLUMN 4				
8	CATHODE COUUMN 5				
9	ANODE ROW 4				
10	CATHODE COLUMN 3				
11	ANODE ROW 2				
12	ANODE ROW 1				

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ABSOLUTE MAXIMUM RATING AT Ta=25°C

PARAMETER	MAXIMUM RATING	UNIT			
Average Power Dissipation Per Dot	70	mW			
Peak Forward Current Per Dot	60	mA			
Average Forward Current Per Dot	25	mA			
Derating Linear From 25°C Per Dot	0.28	mA/°C			
Reverse Voltage Per Segment	5	V			
Operating Temperature Range	-35°C to +105°C	·			
Storage Temperature Range	-35°C to +105°C				

Soldering Conditions: 1/16 inch below seating plane for 3 seconds at 260^oC

or of temperature unit (during assembly) not over max. temperature rating above.

ELECTRICAL / OPTICAL CHARACTERISTICS AT Ta=25°C

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
Average Luminous Intensity	Iv	630	1650		μcd	I _p =32mA 1/16Duty
Peak Emission Wavelength	λр		588		nm	I _F =20mA
Spectral Line Half-Width	Δλ		15		nm	I _F =20mA
Dominant Wavelength	λd		587		nm	I _F =20mA
Forward Voltage any Dot	VF		205	2.6	V	I _F =20mA
Reverse Current any Dot	Ir			100	μΑ	V _R =5V
Luminous Intensity Matching Ratio (Similar Light Area)	Iv-m			2:1		I _p =32mA 1/16Duty

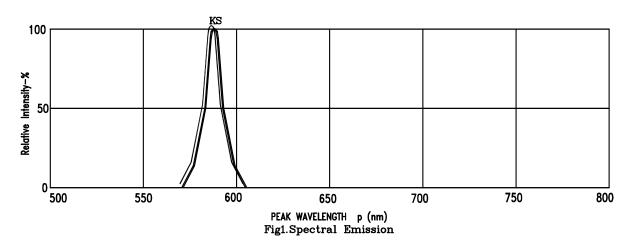
Note: Luminous intensity is measured with a light sensor and filter combination that approximates the CIE (Commision Internationale De L'Eclairage) eye-response curve.

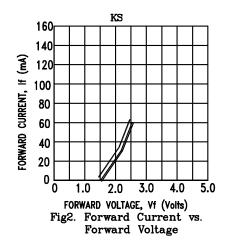
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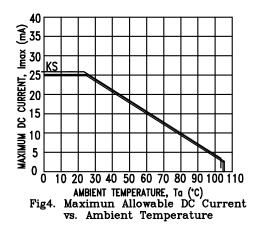
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TYPICAL ELECTRICAL / OPTICAL CHARACTERISTIC CURVES

(25°C Ambient Temperature Unless Otherwise Noted)







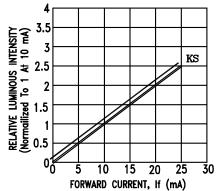
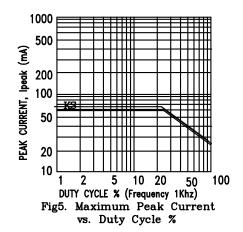


Fig3. Relative Luminous Intensity vs. DC Forward Current



NOTE: KS=AlinGaP YELLOW

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