

**SAMSUNG**

ELECTRONICS

Product Information

Issued Date : Mar 02, 2005

SAMSUNG TFT-LCD**MODEL NO. : LTA460W2-L01**

Note : _____

Any Modification of Spec is not allowed without SEC's permission.

Senior Engineer :

PREPARED BY : AMLCD TECHNICAL CUSTOMER SERVICE TEAM

Samsung Electronics Co . , LTD.

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**Product Information****Revision History**

No	Date	page	Before Change	After Change	Remark
0.0	2004.12.1	All	First issued		
0.1	2005.3.02	4	Haze 44%	Haze 41%	
		7	Response Time Tr 3.5(typ.) 10(max.) Tf 4.5(typ.) 10(max.)	Response Time Tr 8(typ.) 13(max.) Tf 6(typ.) 7(max.)	

Product Information**General Description***** Description**

LTA460W2-L01 is a color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFTs as a switching devices. This model is composed of a TFT LCD panel, a driver circuit and a back-light system. The resolution of a 46.0" contains 1366 x 768 pixels and can display up to 16.7 million colors with wide viewing angle of 85° or higher in all directions.

*** Features**

- High contrast ratio, high aperture structure
- PVA(Patterned Vertical Align) mode
- Wide viewing angle($\pm 170^\circ$)
- High speed response
- WXGA(1,366 x 768 pixels) resolution (16:9)
- Low Power consumption
- Direct Type 24 CCFT(Cold Cathode Fluorescent Tube)
- DE only mode
- 1Channel LVDS(Low-Voltage Differential Signal) interface.(1 pixel/clock)

*** Applications**

Public Display

Home-alone Multimedia TFT-LCD TV

Display terminals for AV application products

High Definition TV Ready(HD TV Ready)

*** General information**

Items	Specification	Unit	Note
Display area	1018.353(H) x 572.544(V)	mm	
Driver element	a-Si TFT Active matrix		
Display colors	16.7M(true)	colors	
Number of pixels	1,366 x 768	pixel	16:9
Pixel arrangement	RGB Vertical Stripe		
Pixel pitch	0.2485(H) × 0.7455(W)	mm	
Display mode	Normally Black		
Surface treatment	Haze 41% , Hard-Coating (3H)		Anti-Glare

Product Information

* Mechanical information (Panel Module Only)

Item		Min.	Typ.	Max.	Note
Module size	Horizontal(H)	1082.0	1083.0	1084.0	mm
	Vertical(V)	626.0	627.0	628.0	mm
	Depth(D)	55.5	56.5	57.5	mm
Weight		12,000	13,500	15,000	g

1. Absolute Maximum Ratings

1.1 Absolute ratings of environment

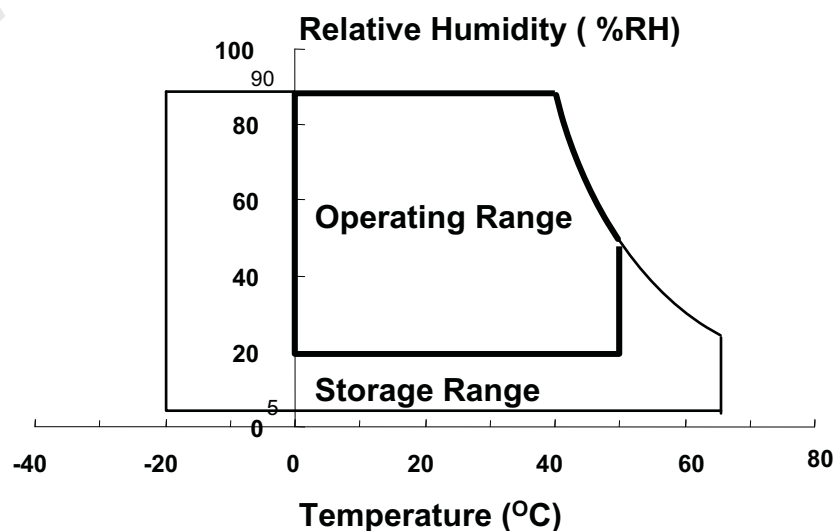
Item	Symbol		Min.	Max.	Unit	Note
Storage temperature	T _{STG}		-20	65	℃	(1)
Operating temperature (Ambient temperature)	T _{OPR}		0	50	℃	(1)
Shock (non - operating)	S _{nop}	x,y axis	-	50	G	(2),(4)
		z axis	-	35	G	
Vibration (Non - operating)	V _{nop}		-	1.5	G	(3),(4)

Note (1) Temperature and relative humidity range are shown in the figure below.

93.8% RH Max. (40 °C ≥ Ta)

Maximum wet-bulb temperature at 39 °C or less. (Ta > 40 °C) No condensation.

- (2) 11ms, sine wave, 1 time for ±X, ±Y, ±Z axis
- (3) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis
- (4) At testing Vibration and Shock, the fixture in holding the Module to be tested have to be hard and rigid enough so that the Module would not be twisted or bent by the fixture.



Product Information**1.2 ELECTRICAL ABSOLUTE RATINGS****(1) TFT LCD Module**(V_{ss} = GND = 0 V)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	VDD	V _{ss} -0.5	5.5	V	(1)

NOTE (1) Within Ta (25 ± 2 °C)

(2) BACK-LIGHT UNIT

(Ta = 25 ± 2°C)

Item	Symbol	Min.	Max.	Unit.	Note
Lamp Current	I _L	(4.0)	(7.0)	mArms	(1),(2)
Lamp Frequency	F _L	(40)	(80)	kHz	(1)

NOTE (1) Permanent damage to the device may occur if maximum values are exceeded.
Functional operation should be restricted to the conditions described under
Normal Operating Conditions.

(2) Specified values are for a single lamp.

Product Information

2. Optical Characteristics

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (1).

◆ Measuring equipment : TOPCON BM-5A , BM-7, PHOTO RESEARCH PR650
EZ-Contrast (Eldim)

* Ta = 25±2°C, VDD=5V, fv=60Hz, fdCLK=80MHz, IL=(6.0)mArms, 1 By 1 Reversal Operation

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R		600	800	-	msec	(3) BM-5A
Response Time	Rising	Tr		-	8	13		(4) BM-7
	Falling	Tf		-	6	7		
Luminance of White (Center of screen)		YL	Normal $\phi = 0$ $\theta = 0$	400	500	-	cd/m2	(5) BM-5A
Color Chromaticity (CIE 1931)	Red	Rx	Viewing Angle	TYP. -0.03	(0.648)	TYP. +0.03		(6) PR650
		Ry			(0.333)			
	Green	Gx			(0.271)			
		Gy			(0.592)			
	Blue	Bx			(0.141)			Color Temp.: 10000°K
		By			(0.066)			
	White	Wx			0.280			
		Wy			0.290			
Lamp Temperature				-	10,000	-	K	
Viewing Angle	Hor.	θ L	C/R $\geq$ 10:1	75	85	-	Degrees	(7) BM-5A
		θ R		75	85	-		
	Ver.	ϕ H		75	85	-		
		ϕ L		75	85	-		
Brightness Uniformity (9 points)		Buni		-	-	25	%	(2),(8) BM-5A

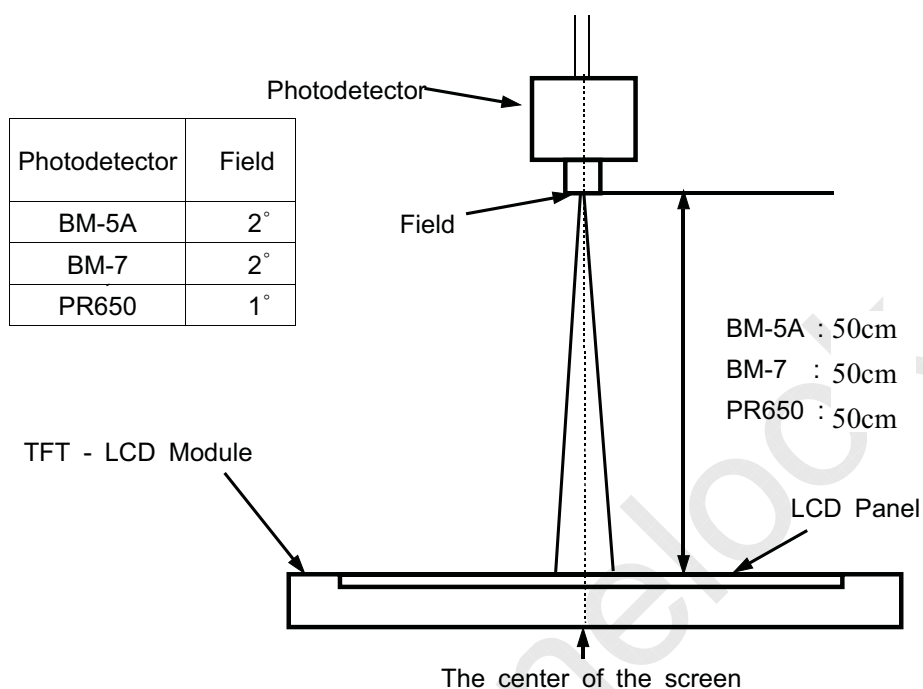
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Note 1) Test Equipment Setup

After stabilizing and leaving the panel alone at a given temperature for 30 min ,the measurement should be executed. Measurement should be executed in a stable, windless, and dark room 30 min after lighting the back-light. This should be measured in the center of screen.

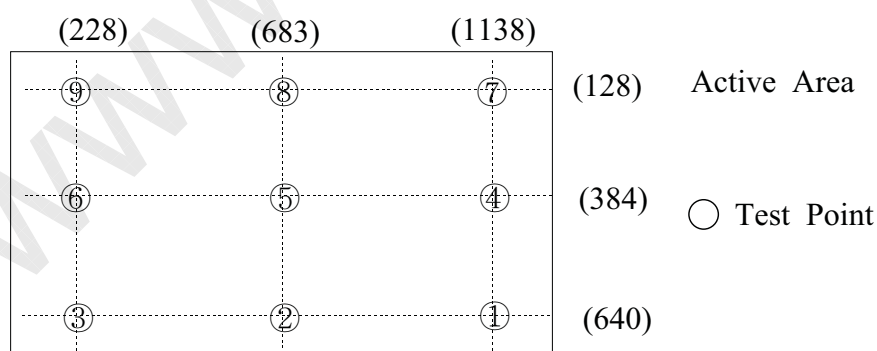
A single lamp current : (6.0) mA

Environment condition : $T_a = 25 \pm 2 \text{ }^{\circ}\text{C}$



Optical Measuring Equipment Setup

Note 2) Definition of test point



Product Information

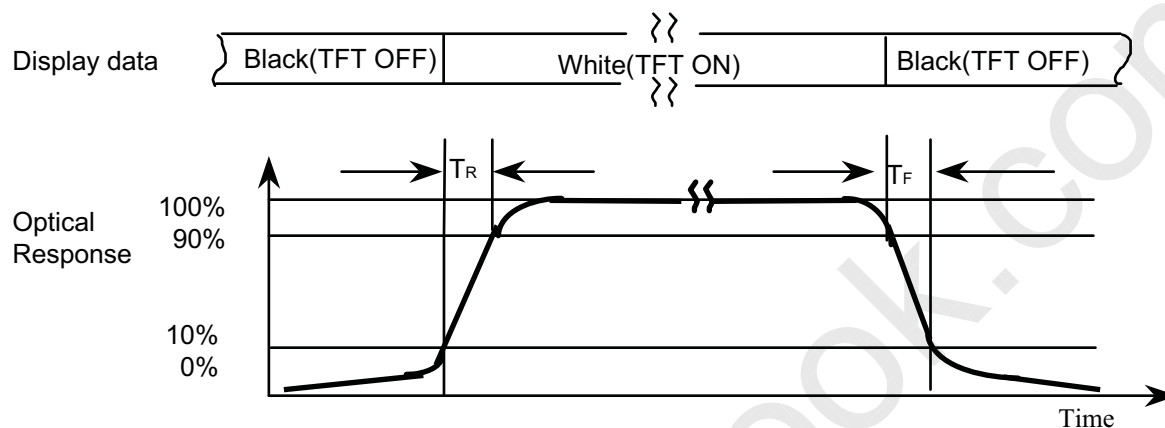
Note 3) Definition of Contrast Ratio (C/R) : Ratio of gray max (Gmax) & gray min (Gmin) at the center point(5) of the panel

$$CR = \frac{G_{\max}}{G_{\min}}$$

Gmax : Luminance with all pixels white

Gmin : Luminance with all pixels black

Note 4) Definition of Response time : Sum of T_R , T_F

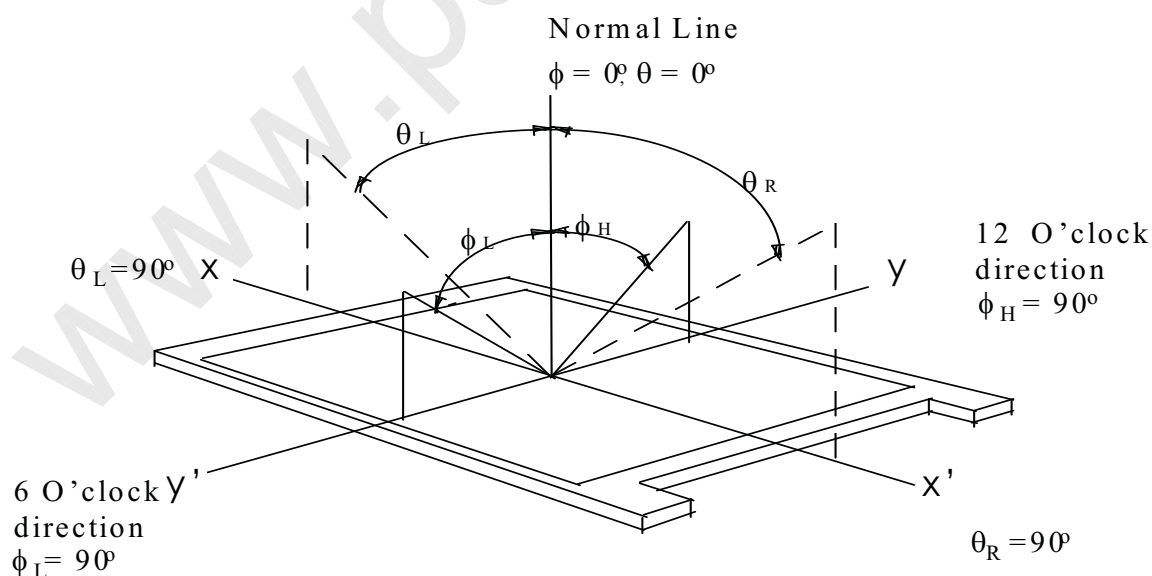


Note 5) Definition of Luminance of White : Luminance of white at center point(5).

Note 6) Definition of Color Chromaticity (CIE 1931)

Color coordinate of Red , Green , Blue & White at center point(5).

Note 7) Definition of Viewing Angle : Viewing angle range ($CR \geq 10$)



Product Information

Note 8) Definition of 5 points brightness uniformity

$$B_{uni} = 100 * \frac{(B_{max} - B_{min})}{B_{max}}$$

Bmax : Maximum brightness

Bmin : Minimum brightness

Product Information

3. Electrical Characteristics

3.1 TFT LCD MODULE

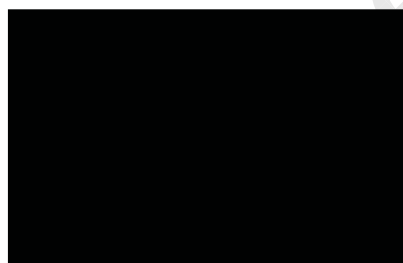
Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		Module V _{DD}	4.5	5.0	5.5	V	(1)
Current of Power Supply	(a)Black	I _{DD}	-	1500	-	mA	(2),(3) (Without Inverter) 1by1 Driving
	(b)Mosaic		-	1700	-	mA	
	(d)N-Pattern		-	2000	2300	mA	
Vsync Frequency		f _V	-	60	-	Hz	
Hsync Frequency		f _H	46.3	50.18	-	kHz	
Main Frequency		f _{DCLK}	65	80	90	MHz	
Rush Current		I _{RUSH}	-	(4.5)	(6)	A	(4)

Note (1) Voltage of Power Supply is the value which is measured at the input connector of panel.

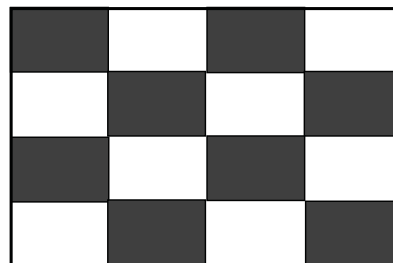
(2) $f_V=60\text{Hz}$, $f_{DCLK}=80\text{MHz}$, $V_{DD}=5.0\text{V}$, DC Current.

(3) Power dissipation check pattern(LCD Module only)

(a) Black Pattern

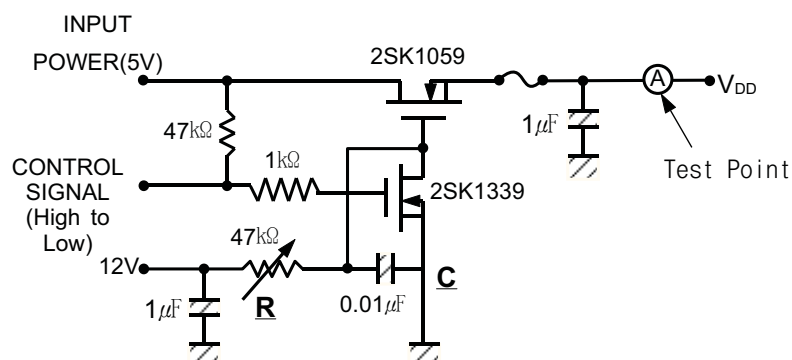


(b) Mosaic Pattern



(c) N-pattern

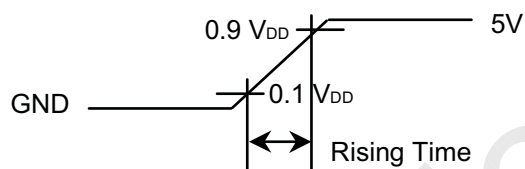


Product Information**(4) Measurement Conditions (5V Operation, Rising time =470 μ s)**

Note : Control Signal : High(+5V) -->Low(Ground)

All Signal lines to panel except for power 5V : Ground

The rising time of supplied voltage is controlled to 470us by R and C value.



Product Information

3.2 BACK-LIGHT UNIT

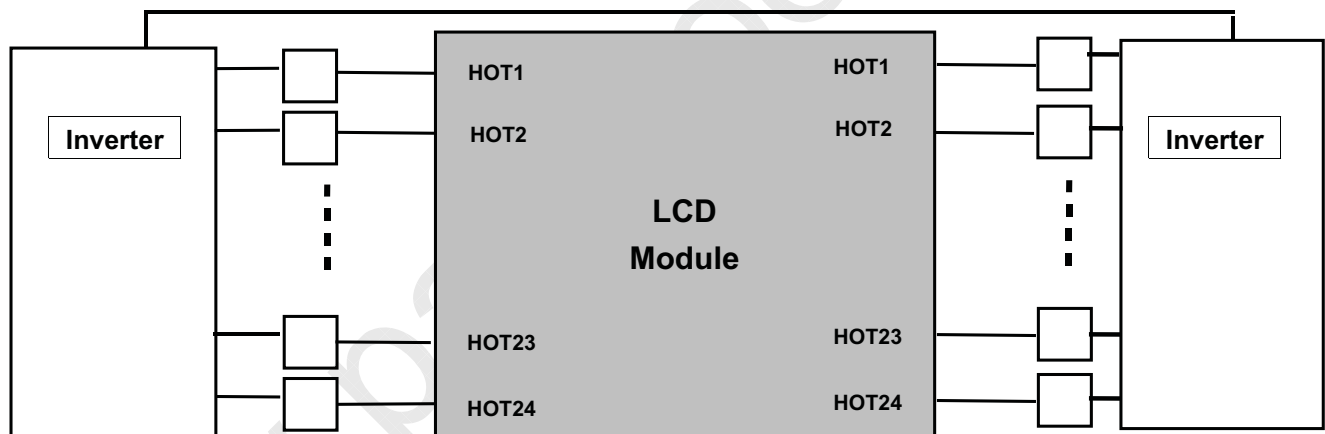
The back-light system is an direct - lighting type with 24 CCFTs (Cold Cathode Fluorescent Tube) The characteristics of 24 direct lamps are shown in the following tables.

$T_a = 25 \pm 2^\circ\text{C}$

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Lamp Current	I_L	(4.0)	(6.0)	(7.0)	mArms	(1)
Lamp Voltage	V_L	(1480)	(1540)	(1600)	Vrms	(1)
Lamp Frequency	f_L	(40)	-	(80)	kHz	(2)
Operating Life Time	Hr	50,000	-	-	Hour	(3)
Start up Voltage	V_s	-	-	0°C:(2500)	Vrms	(4)
				25°C:(1990)		

Note) The waveform of the inverter output voltage must be area symmetric and the design of the inverter must have specifications for the modularized lamp.

Specified values are for a single lamp.



The performance of the back-light, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter.

When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the back-light and the inverter(miss lighting, flicker, etc.) never occur. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

Note (1) lamp current is measured with current meter.

Refer to the block diagram of the back-light unit in the next page for more information.

Lamp Voltage Min : Lamp Current (7.0) mArms

Lamp Voltage Max : Lamp Current (4.0) mArms

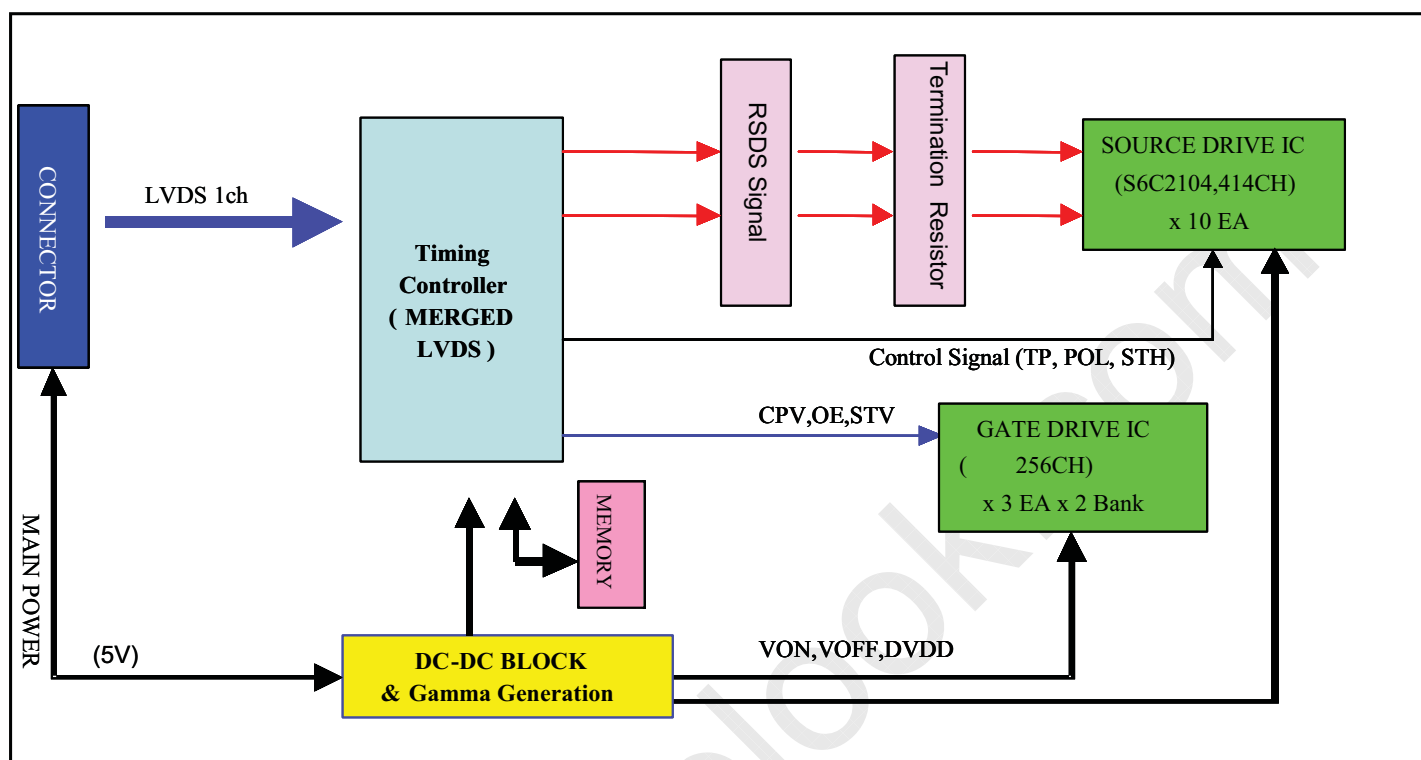
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- (2) Lamp frequency may produce interference with horizontal synchronous frequency and this may cause line flow on the display. Therefore lamp frequency shall be detached from the horizontal synchronous frequency and its harmonics as far as possible in order to avoid interference.
- (3) Life time (Hr) of a lamp is defined as the time in which it continues to operate under the condition of $T_a = 25 \pm 2^\circ\text{C}$ and $IL = 6.0\text{mA rms}$ for a lamp until the brightness becomes 50% or lower than its original value.
- (4) If an inverter has shutdown function it should keep its output for more than 1 second even if the lamp connector open. Otherwise the lamps may not to be turned on.

Product Information

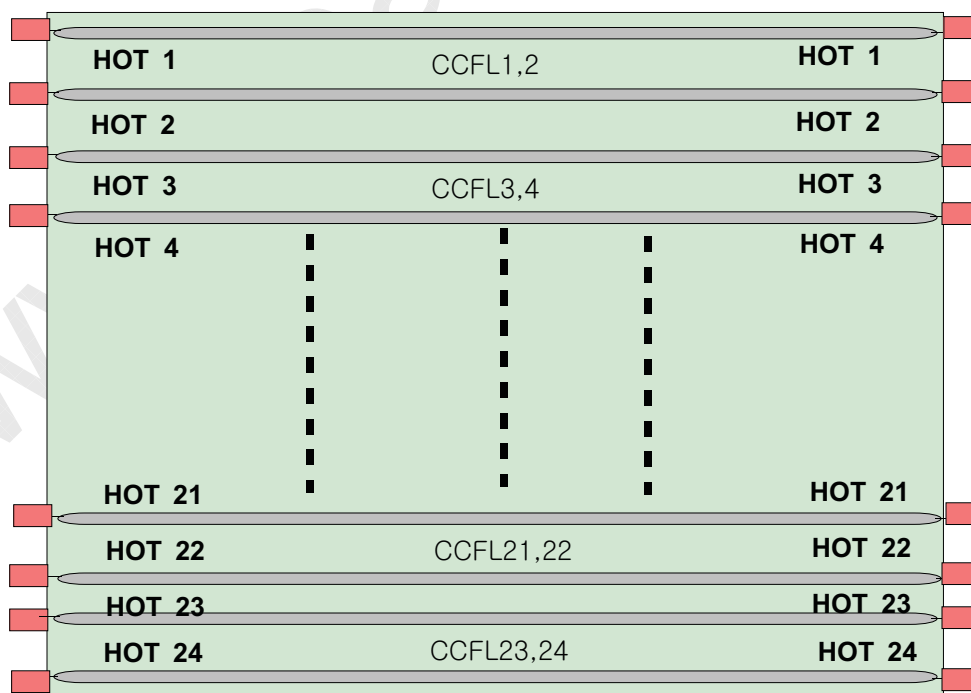
4. Block Diagram

4.1 TFT LCD MODULE



4.2 BACK-LIGHT UNIT

■ HOT 사양 : HIGH VOLTAGE



Product Information

5. Input Terminal Pin Assignment

5.1. Input Signal & Power : Connector 30P, JAE FI-E30S

No	Signal	No	Signal
1	N.C*	16	GND
2	N.C*	17	Rx3-
3	N.C*	18	Rx3+
4	GND	19	GND
5	Rx0-	20	N.C
6	Rx0+	21	LVDS Option
7	GND	22	N.C
8	Rx1-	23	GND
9	Rx1+	24	GND
10	GND	25	GND
11	Rx2-	26	V _{DD} (=+5[V])
12	Rx2+	27	V _{DD} (=+5[V])
13	GND	28	V _{DD} (=+5[V])
14	RxCLK-	29	V _{DD} (=+5[V])
15	RxCLK+	30	V _{DD} (=+5[V])

* NOT CONNECTED : THIS PINS ARE ONLY USED FOR SEC INTERNAL OPERATIONS.

** LVDS OPTION : IF THIS PIN : HIGH (3.3 V) → NORMAL NS LVDS FORMAT

OTHERWISE : LOW (GND) OR OPEN(NC) → JEIDA LVDS FORMAT

Sequence : On = V_{DD}(T1) ≥ LVDS Option ≥ Interface Signal(T2)

OFF = Interface Signal(T3) ≥ LVDS Option ≥ V_{DD}

5.2. Inverter Control Connector

Connector : JST S14B-PH-SM3-TB

No	Signal
1	D 24V
2	D 24V
3	D 24V
4	D 24V
5	D 24V
6	GND
7	GND
8	GND
9	GND
10	GND
11	N.C
12	Inverter Enable (On : 5V, Off : 0V)
13	ADIM [Max : 3.3V, Min : 0V]
14	PDIM [Max : 100%, Min : 30%]

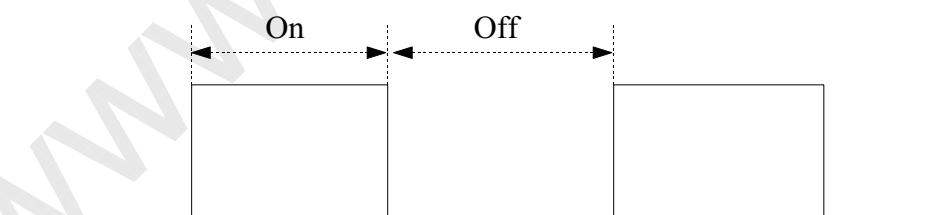
Product Information

5.3 Inverter Specification

Items	Symbol	Conditions	Specifications			Unit	비고
			Min.	Typ.	Max.		
Input Power	Vin	-	23	24	25	V	Ta=25℃
Input Current	Iin	Vin=24V		9		Adc	At 500nit(Typ)
Output Current (Single Lamp)	I _{o,max}	Adim=3.3V	(6.5)	(7.0)	(7.5)	mArms	After 2hour Warm-up, Note (1)
	I _{o,min}	Adim=0V	(3.5)	(4.0)	(4.5)		
Frequency	F _L	Vin=24V,Adim=3.3V	(55)	(60)	(65)	kHz	
Open Lamp Shutdown Time	Ts-d	No Load	1.0	1.5	2.0	sec	
Open Lamp Voltage	Vo	Vin=24V,ADIM=3.3V Each Transformer Output	(1400)			V	
On/Off control	On	ON/OFF=High	2.4	-	5.25	V	
	Off	ON/OFF=Low	0	-	0.8	V	
PWM signal	V _{pwm}	ON(high)	2.4		5.25	V	
	V _{pwm}	OFF(low)	0		0.8	V	
PWM duty	PD	Vin=24V,Adim=3.3V	30		100	%	Note (2)
		Vin=24V,Adim=0V	50		100		
Analog Dimmer	Max Lum	-	-	3.3	-	V	Note (3)
	Min. Lum	-	-	0	-		

Note(1) Controlled by Analog or PWM dimming

Note(2) High-duty = $\text{On}/(\text{On}+\text{Off}) * 100$

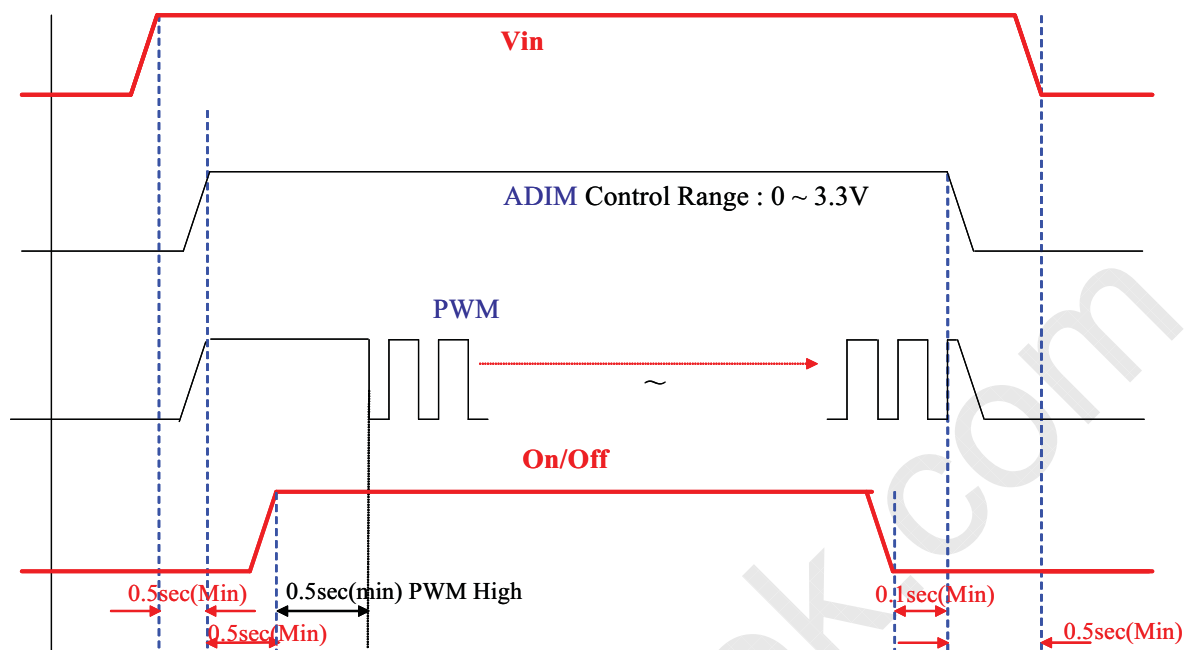


Note(3) - Controlled by Analog dimming only

- Analog dimming 0[V] (Minimum Lamp current)
- Analog dimming 3.3[V] (Maximum Lamp current)

Product Information

5.3.2 Inverter input Power Sequence



**Product Information****5.4 LVDS Interface**

- LVDS Receiver : Tcon (merged)
- JEIDA & Normal Data Format

	LVDS pin	JEIDA -DATA	VESA -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R2	R0
	TxIN/RxOUT1	R3	R1
	TxIN/RxOUT2	R4	R2
	TxIN/RxOUT3	R5	R3
	TxIN/RxOUT4	R6	R4
	TxIN/RxOUT6	R7	R5
	TxIN/RxOUT7	G2	G0
TxOUT/RxIN1	TxIN/RxOUT8	G3	G1
	TxIN/RxOUT9	G4	G2
	TxIN/RxOUT12	G5	G3
	TxIN/RxOUT13	G6	G4
	TxIN/RxOUT14	G7	G5
	TxIN/RxOUT15	B2	B0
	TxIN/RxOUT18	B3	B1
TxOUT/RxIN2	TxIN/RxOUT19	B4	B2
	TxIN/RxOUT20	B5	B3
	TxIN/RxOUT21	B6	B4
	TxIN/RxOUT22	B7	B5
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R0	R6
	TxIN/RxOUT5	R1	R7
	TxIN/RxOUT10	G0	G6
	TxIN/RxOUT11	G1	G7
	TxIN/RxOUT16	B0	B6
	TxIN/RxOUT17	B1	B7
	TxIN/RxOUT23	RESERVED	RESERVED

Product Information

5.5 Input Signal, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY	DATA SIGNAL																											GRAY SCALE LEVEL
		RED									GREEN									BLUE									
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4	B5	B6	B7				
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-		
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-		
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	-		
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-		
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-		
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-		
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	-		
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-		
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0		
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1		
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~ R252		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
	↓ LIGHT	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253	
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254		
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255		
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0		
	DARK ↑	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1		
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~ G252		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
	↓ LIGHT	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	G253		
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	G254		
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	G255		
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0		
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	B1		
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B2		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~ B252		
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
	↓ LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	B253		
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B254		
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	B255		

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

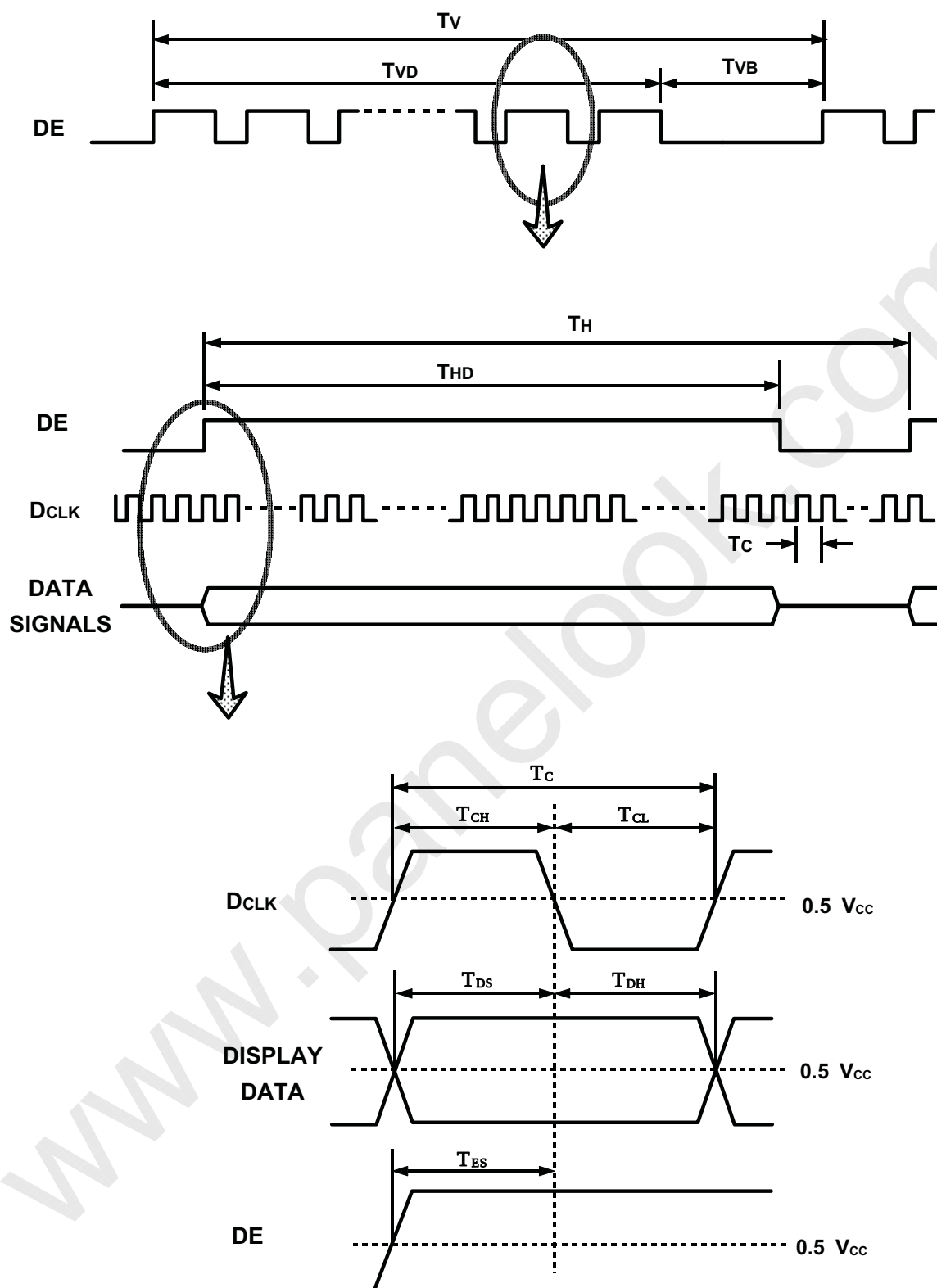
Input Signal : 0 = Low level voltage, 1 = High level voltage

**Product Information****6. Interface Timing****6.1 Timing Parameters (DE only mode)**

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Clock	Frequency	1/TC	65	80	90	Mhz	1pixel/clock
	Hgh Time	TCH	4	-	-	nsec	
	Low Time	TCL	4	-	-	nsec	
Data	Setup Time	TDS	4	-	-	nsec	
	Hold Time	TDH	4	-	-	nsec	
Data Enable	Setup Time	TES	4	-	-	nsec	
Frame Frequency	Cycle	Tv	-	16.7	-	msec	
Vertical Active Display Term	Display Period	TVD	-	768	-	lines	
	Vertical Total	TVB	773	838	~	lines	
Horizontal Active Display Term	Display Period	THD	-	1366	-	clocks	
	Horizontal Total	TH	1570	1600	1700	clocks	

Product Information

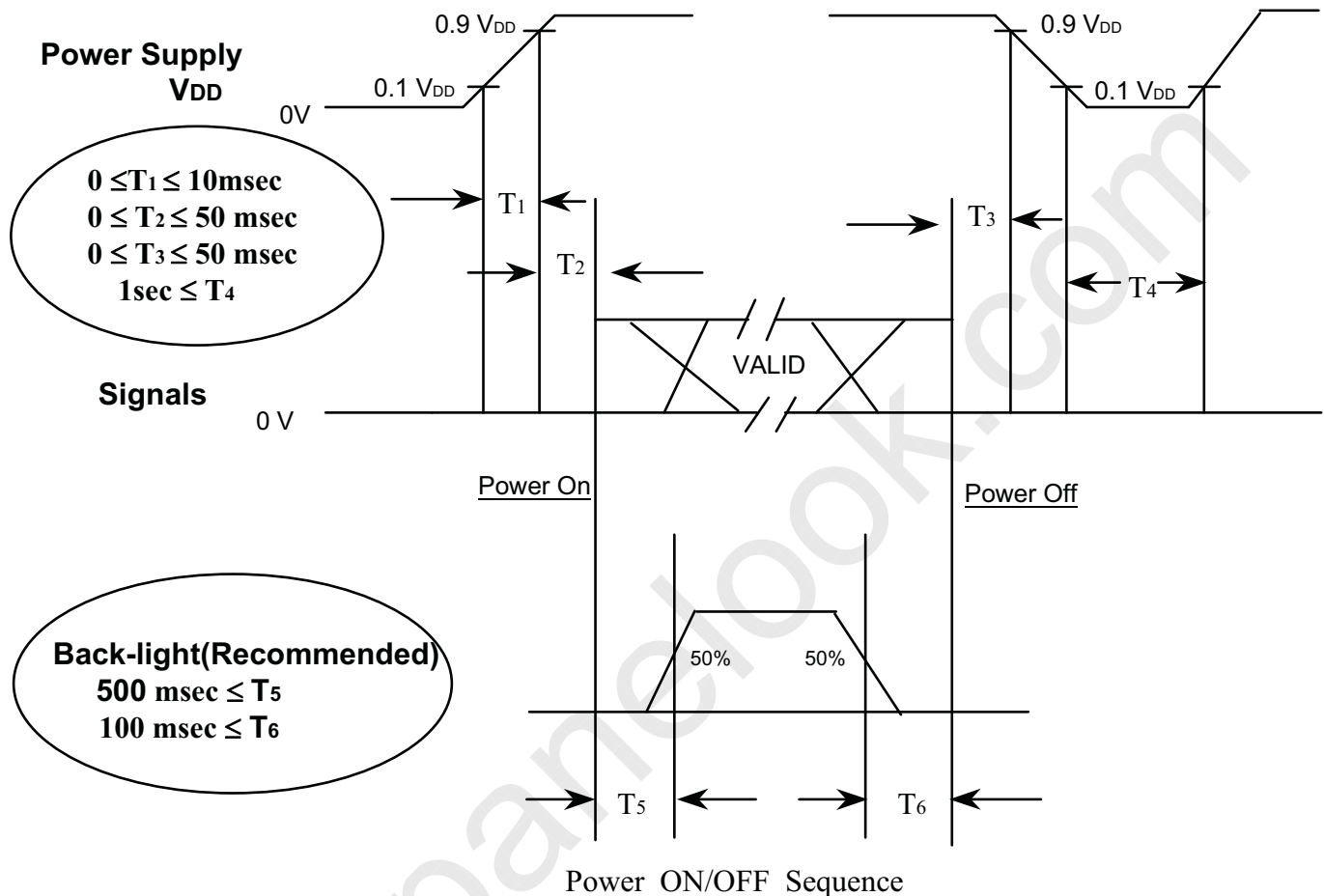
6.2 Timing diagrams of interface signal (DE only mode)



Product Information

6.3 Power ON/OFF Sequence

: To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.

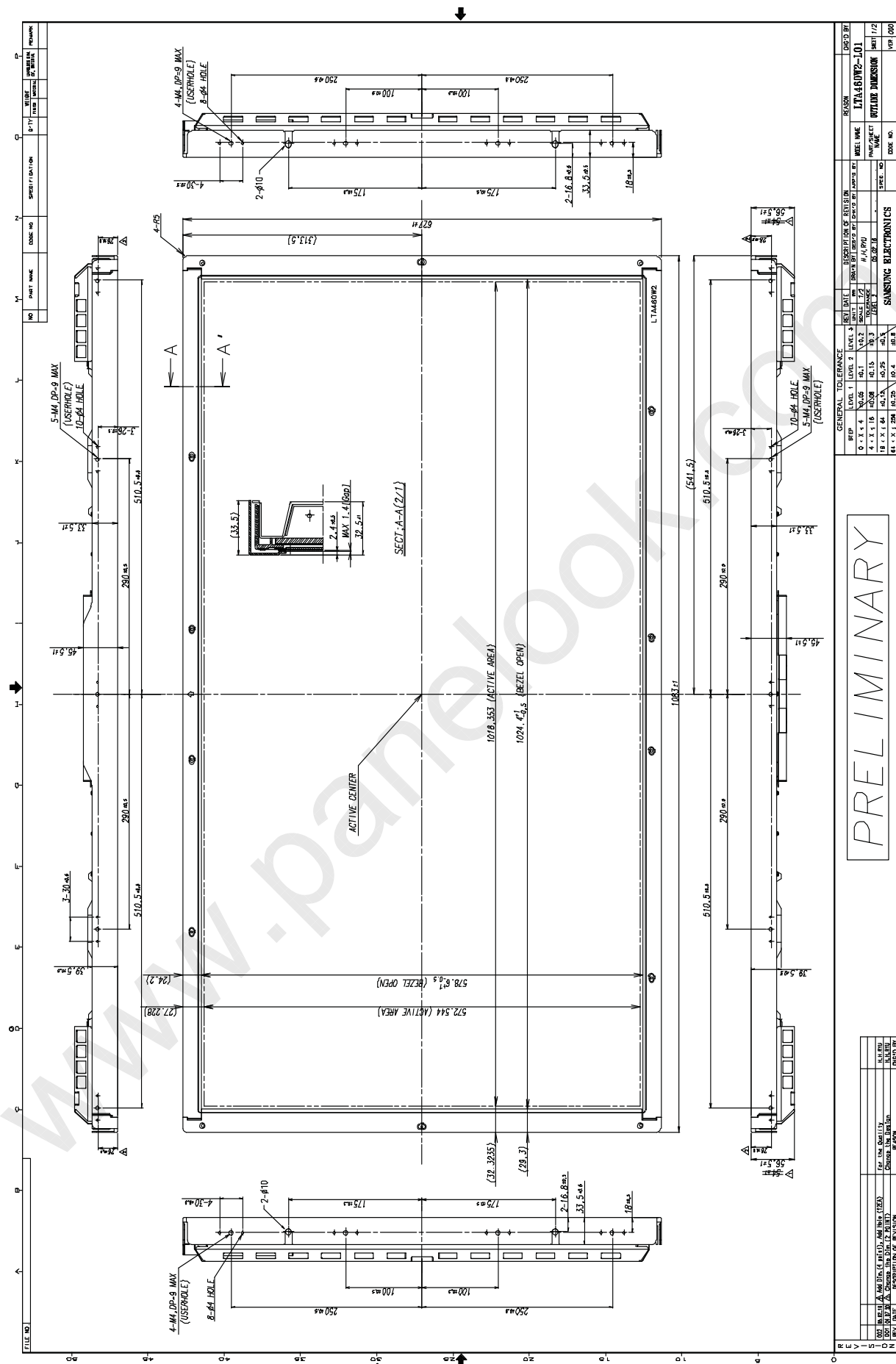


NOTE

- (1) The supply voltage of the external system for the module input should be the same as the definition of VDD.
- (2) Apply the lamp voltage within the LCD operation range. When the back-light turns on before the LCD operation or the LCD turns off before the back-light turns off, the display may momentarily become abnormal screen.
- (3) In case of VDD = off level, please keep the level of input signals on the low or keep a high impedance.
- (4) T₄ should be measured after the module has been fully discharged between power off and on period.
- (5) Interface signal shall not be kept at high impedance when the power is on.

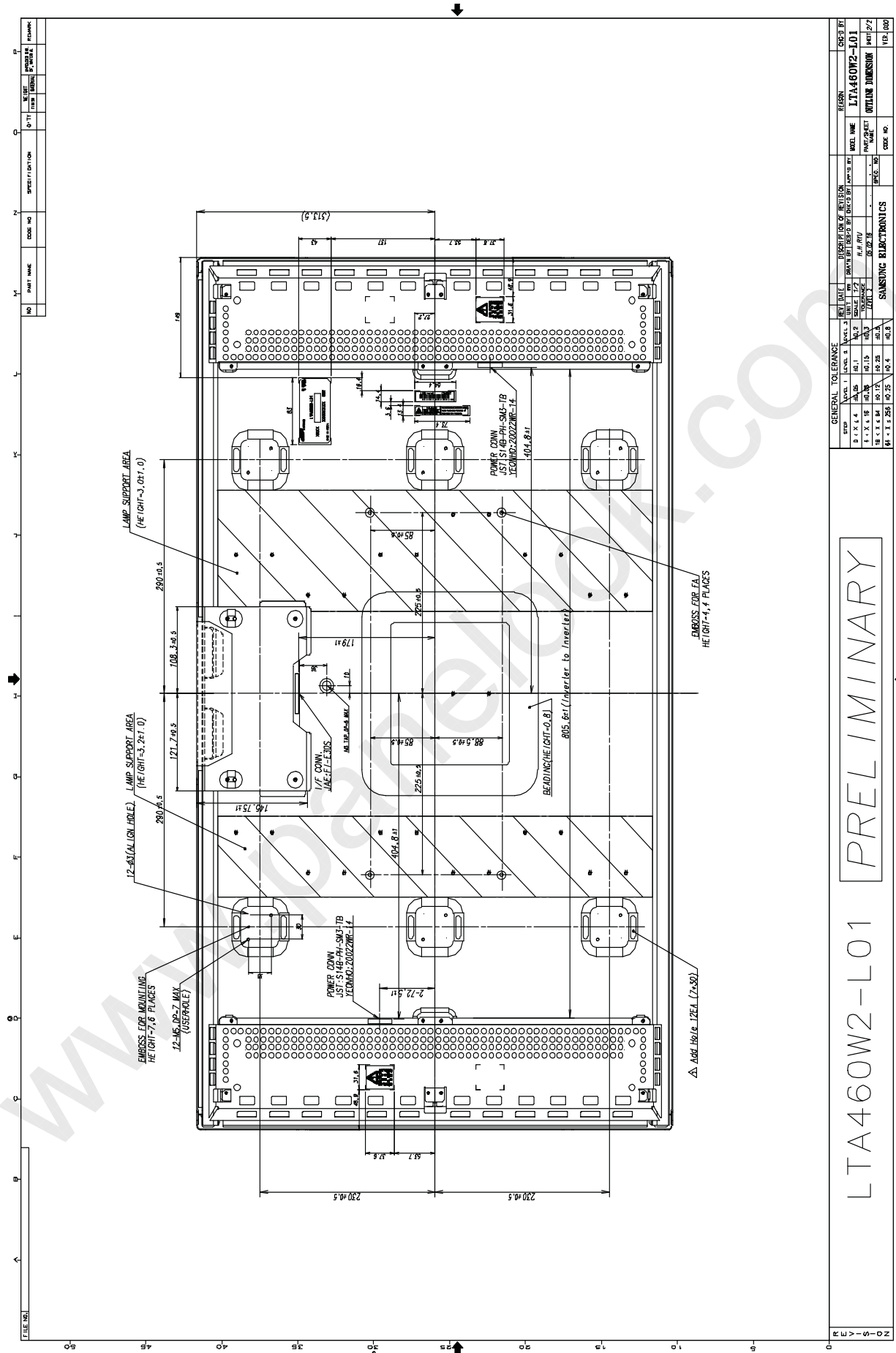
7. Outline Dimension - Front side

Product Information



- Rear side

Product Information



Product Information

8. Packing

8.1. CARTON(Internal Package)

(1) Packing Form

Corrugated fiberboard box and corrugated cardboard as shock absorber

(2) Packing Method

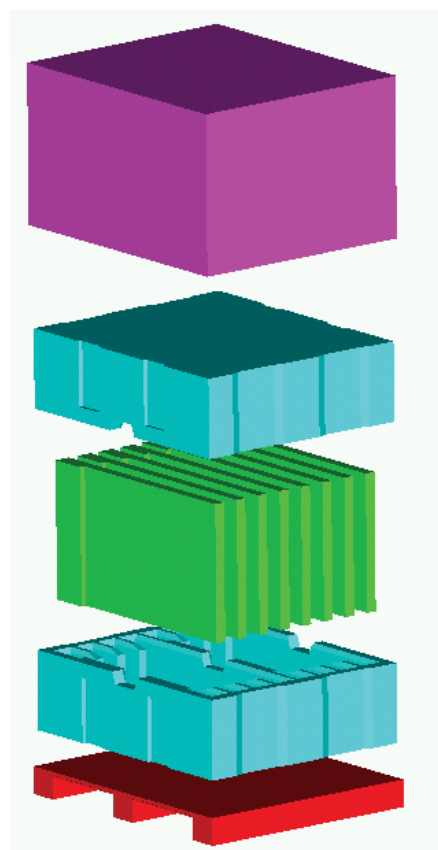
Packing
-PalletBox

Cushion-Foam

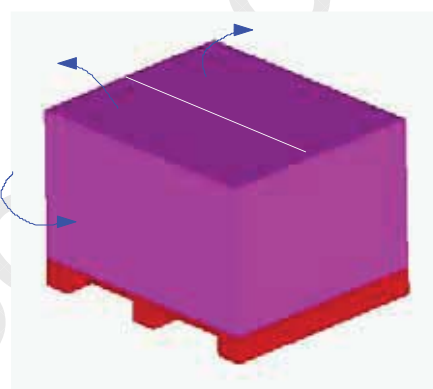
LCD Module

Cushion-Foam

Pallet



→ Direction: be able to open it



8.2. Packing Specification

ITEM	Specification	Remark
LCD Packing	10ea / (Packing-Pallet Box)	1. 140 Kg / LCD (10ea) 2. 10 Kg / Cushion-Foam (2ea) 3. 8 Kg / Packing-Pallet Box (1ea) 3. Cushion-Foam Material : EPS 4. Packing-Pallet Box Material : DW4
Pallet	1Box / Pallet	1. Pallet weight = 8.8kg 2. 8.8 Kg / Pallet
Packing Direction	Vertical	
Total Pallet size	H x V x height	1270mm(H) x 1150mm(V) x 844mm(height)
Total Pallet weight	167kg	Pallet(8.8kg) + Module(14*10=140) + Cushion(up+botton=10kg) + PALLET-BOX(8kg)

Product Information**9. Marking & Others**

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

(1) Parts number : LTA460W2-L01-XXXX

(2) Revision : One letter

(3) Control : One letter

(4) Lot number : 6 L 4 H 123 01 A
1 2 3 4 5 6 7

① 6 : Line

② L : Device

③ 4 : Year

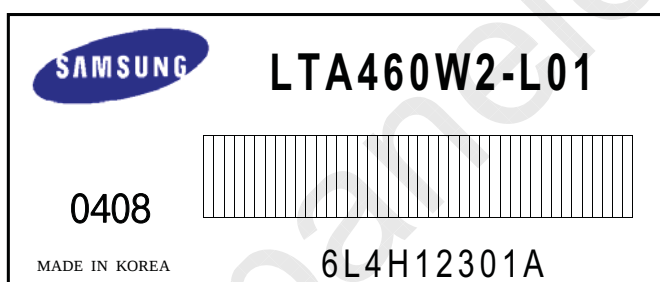
④ H : Month

⑤ 123 : LOT NO

⑥ 01 : GLASS NO

⑦ A : CELL NO

(5) Nameplate Indication



Product Information**(6) Bar code marking for Customer**

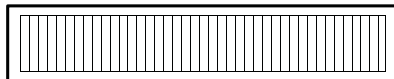
The bar code marking is attached to module backside.

- 1) MODEL NAME : LTA460W2-L01-XXXX
- 2) SAMSUNG
- 3) MADE IN KOREA
- 4) PRODUCTION NUMBER
- 5) USER MODEL NAME

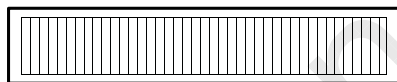
Bar code shows a) user model name, b) production number

a) User model name

LTA460W2-L01-XXXX

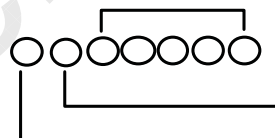
**b) Production Number**

SAMSUNG
MADE IN KOREA



6430008B

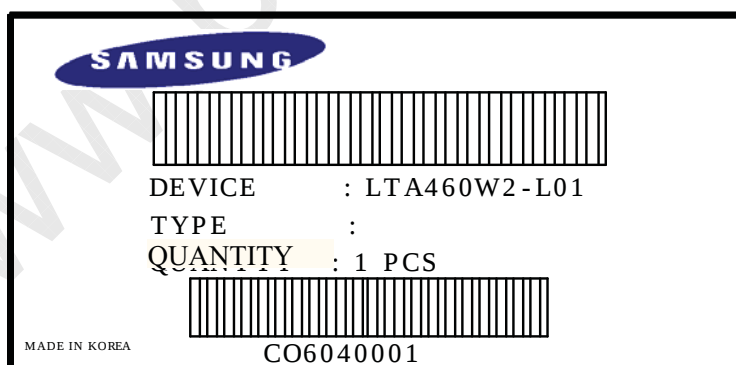
SERIAL NO



[B] REVISION CODE

PRODUCTION MONTH

PRODUCTION YEAR

(7) Packing box attach

10. General Precautions

10.1 Handling

- (a) When the module is assembled, It should be attached to the system firmly using every mounting holes. Be careful not to twist and bend the modules.
- (b) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and CCFT back-light.
- (c) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.
- (d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, Staining and discoloration may occur.
- (e) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (f) The desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (h) Protect the module from static , it may cause damage to the CMOS Gate Array IC.
- (i) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (j) Do not disassemble the module.
- (k) Do not pull or fold the lamp wire.
- (l) Do not adjust the variable resistor which is located on the module.
- (m) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (n) Pins of I/F connector shall not be touched directly with bare hands.

Product Information**10.2 Storage**

- (a) Do not leave the module in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD module in direct sunlight.
- (c) The module shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during the store.

10.3 Operation

- (a) Do not connect,disconnect the module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the item 6.3 "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back-light connector and its inverter power supply shall be a minimized length and be connected directly . The longer cable between the back-light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

10.4 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (the supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on) Otherwise the module may be damaged.
- (d) If the module displays the same pattern continuously for a long period of time,it can be the situation when the image "Sticks" to the screen.
We recommend that you should discuss SEC when you want the module to be operated in displaying the same pattern for a long time.
- (e) This module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.