



ELECTRONICS

Product Information

Customer :Warecube

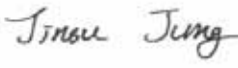
DATE : 23.Mar.2007

SAMSUNG TFT-LCD

MODEL : LTA400HT-LH1

Any Modification of Specification is not allowed without SEC's Permission.

NOTE :

Customer's Approval		APPROVAED BY	DATE
			23.Mar.2007
SIGNATURE	DATE	PREPARED BY	DATE
			23.Mar.2007

LCD Business

Samsung Electronics Co . , LTD.

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*** Revision History**

Date	Rev. No	Page	Summary
Mar 23, 2007	000	all	First issued

General Description

Description

LTA400HT-LH1 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT(Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 40.0" is 1920 x 1080 and this model can display up to 1.07 Billion colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide a excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

Features

- RoHS compliance (Pb-free)
- High contrast & aperture ratio with wide color gamut
- SPVA(Super Patterned Vertical Align) mode
- Wide viewing angle ($\pm 178^\circ$)
- High speed response
- FHD resolution (16:9)
- Low Power consumption
- Direct Type 20 CCFLs (Cold Cathode Fluorescent Lamp)
- DE(Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface (2pixel/clock)

General Information

Items	Specification	Unit	Note
Module Size	952.0(H _{TYP}) x 551.0(V _{TYP})	mm	± 1.0mm
	54.2(D _{MAX})		
Weight	11,500 (Max.)	g	
Pixel Pitch	0.461(H) x 0.461(W)	mm	
Active Display Area	885.6(H) X 498.15 (V)	mm	
Surface Treatment	Haze 2.4% , Hard-coating (3H)	-	
Display Colors	10 bit - 1.07B	Colors	
Number of Pixels	1920 x 1080	Pixel	
Pixel Arrangement	RGB vertical stripe	-	
Display Mode	Normally Black	-	
Luminance of White	600 (Typ.)	cd/m ²	

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1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item		Symbol	Min.	Max.	Unit	Note
Power Supply Voltage		V_{DD}	GND-0.5	16.5	V	(1)
Storage temperature		T_{STG}	-20	60		(2)
Glass surface temperature (Operation)	Center	T_{OPR}	0	50		(2),(5)
	T. Uniformity	T	-	10		
Shock (non - operating)		S_{nop}	-	50	G	(3)
Vibration (non - operating)		V_{nop}	-	1.5	G	(4)

Note (1) $T_a = 25 \pm 2 \text{ }^{\circ}\text{C}$

(2) Temperature and relative humidity range are shown in the figure below.

a. 90 % RH Max. ($T_a \leq 39 \text{ }^{\circ}\text{C}$)

b. Relative Humidity is 90% or less. ($T_a > 39 \text{ }^{\circ}\text{C}$)

c. No condensation

(3) 11ms, sine wave, one time for $\pm X$, $\pm Y$, $\pm Z$ axis

(4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

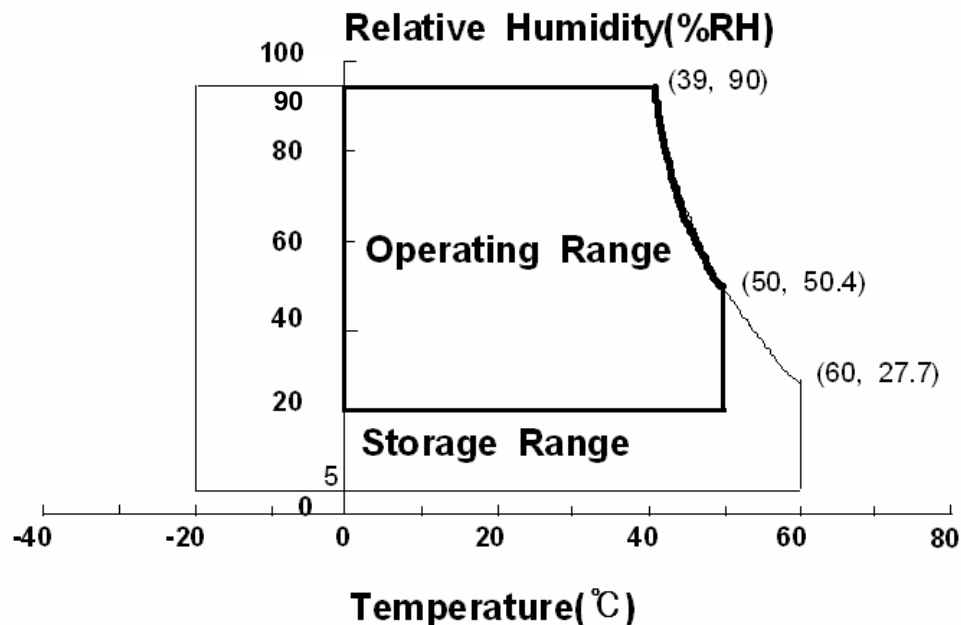
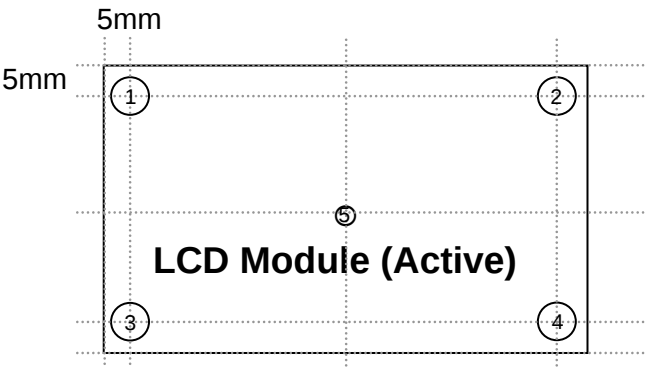


Fig. Temperature and Relative humidity range

(5) Definition of test point



T should be less than 10 ($T = |T_{\text{OPR}} - T_{\text{MAX}}|$)

- T_{OPR} : Temperature of the center of the glass surface (Test point 5)
- T1~ T4 : Temperature of each edge of the glass surface
- T_{MAX} : The highest temperature of the glass surface

2. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent.

Measuring equipment : TOPCON RD-80S, TOPCON SR-3 ,ELDIM EZ-Contrast

(Ta = 25 ± 2°C, VDD=12.0V, fv= 60Hz, f_{DCLK}=148.5 MHz, I_T =172mA)

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)		C/R	Normal $\theta L,R = 0$ $\theta U,D = 0$ Viewing Angle	1500	2000	-		(1) SR-3
Response Time	Rising	Tr		-	10	16	msec	(3) RD-80S
	Falling	Tf		-	6	10		
	G-to-G	Tg		-	8	-		
Luminance of White (Center of screen)		Y _L		500	600	700	cd/m ²	(4) SR-3
Color Chromaticity (CIE 1931)	Red	Rx		TYP. -0.03	0.655	TYP. +0.03		(5),(6) SR-3
		Ry			0.325			
	Green	Gx			0.205			
		Gy			0.670			
	Blue	Bx			0.145			
		By			0.070			
	White	Wx			0.280			
		Wy			0.290			
Color Gamut		-		-	92	-	%	(5) SR-3
Color Temperature		-		-	10000	-	K	
Viewing Angle	Hor.	θ_L	C/R ≥ 10	79	89	-	Degree	(6) EZ-Contrast
		θ_R		79	89	-		
	Ver.	θ_U		79	89	-		
		θ_D		79	89	-		
Brightness Uniformity (9 Points)		B _{uni}		-	-	25	%	(2) SR-3

- Test Equipment Setup

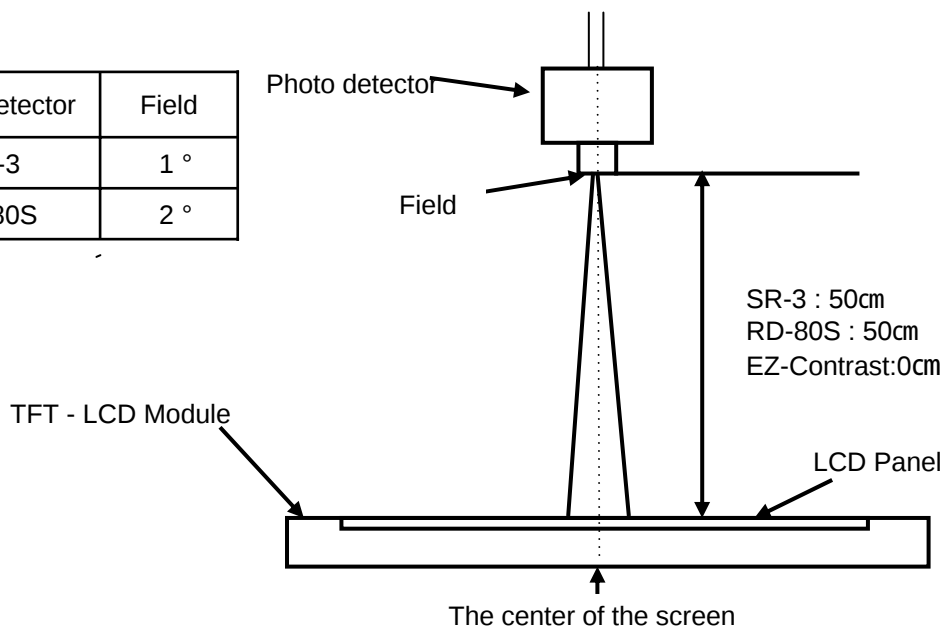
The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

Total Lamp current : 172 mA

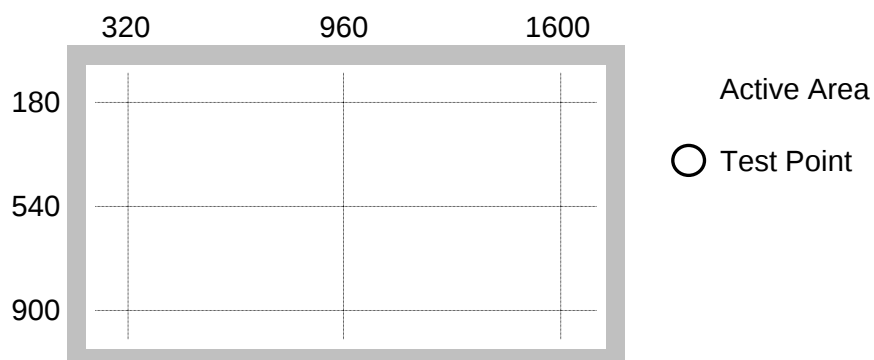
Environment condition : Ta = 25 ± 2 °C

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Photo detector	Field
SR-3	1 °
RD-80S	2 °



- Definition of test point



Note (1) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point of the panel

$$C / R = \frac{G \max}{G \min}$$

Gmax : Luminance with all pixels white

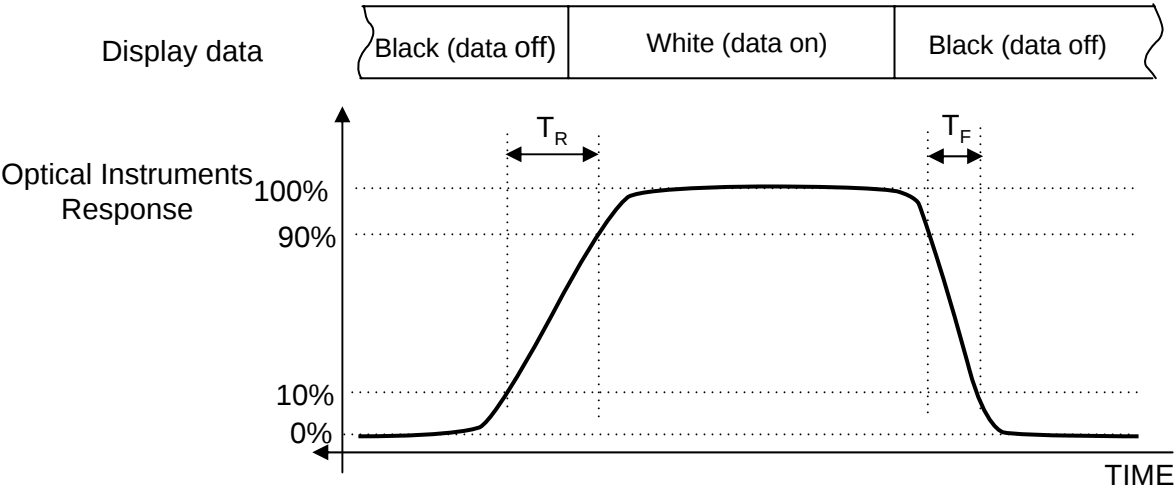
Gmin : Luminance with all pixels black

Note (2) Definition of 9 points brightness uniformity (Test pattern : Full White)

$$B_{uni} = 100 * \frac{(B_{max} - B_{min})}{B_{max}}$$

Bmax : Maximum brightness
Bmin : Minimum brightness

Note (3) Definition of Response time : Sum of Tr, Tf

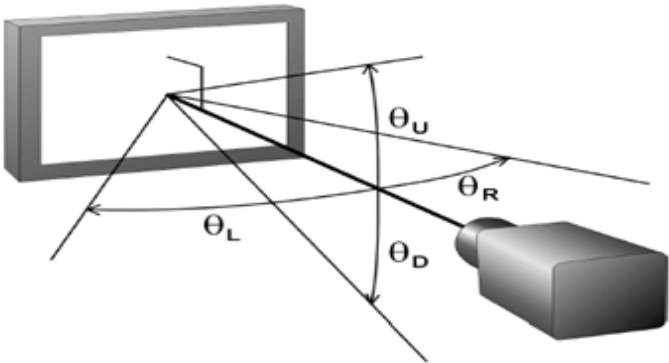


G-to-G : Average response time between Gray to Gray (Scale)

Note (4) Definition of Luminance of White : Luminance of white at center point

Note (5) Definition of Color Chromaticity (CIE 1931)
Color coordinate of Red, Green, Blue & White at center point

Note (6) Definition of Viewing Angle
: Viewing angle range (C/R ≥ 10)



3. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

$T_a = 25^{\circ}\text{C} \pm 2^{\circ}\text{C}$

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply		V _{DD}	10.8	12.0	13.2	V	(1)
Current of Power Supply	(a) Black	I _{DD}	-	600	-	mA	(2),(3)
	(b) White		-	1150	-	mA	
	(c) N-Pattern		-	1260	1400	mA	
Vsync Frequency		f _V	45	60	65	Hz	
Hsync Frequency		f _H	48	67.5	75	kHz	
Main Frequency		Fdclk	130	148.5	155	MHz	
Rush Current		I _{RUSH}	-	-	4	A	(4)

Note (1) The ripple voltage should be controlled under 10% of V_{DD} .

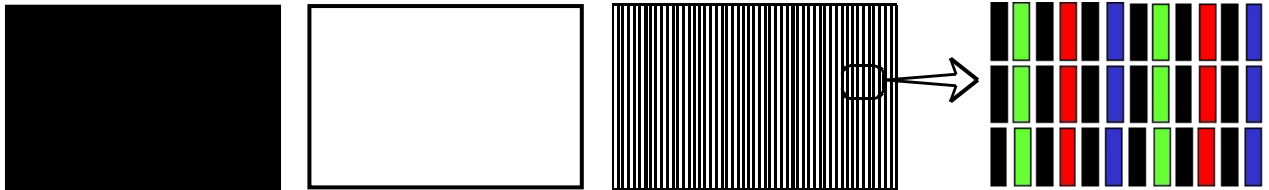
(2) $f_V=60\text{Hz}$, $f_{DCLK} = 148.5\text{MHz}$, $V_{DD} = 12.0\text{V}$, DC Current.

(3) Power dissipation check pattern (LCD Module only)

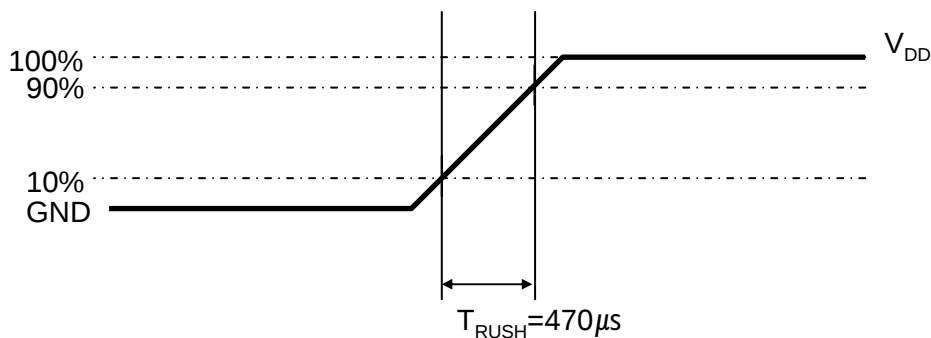
a) Black Pattern

b) White Pattern

c) N-Pattern



(4) Measurement Conditions

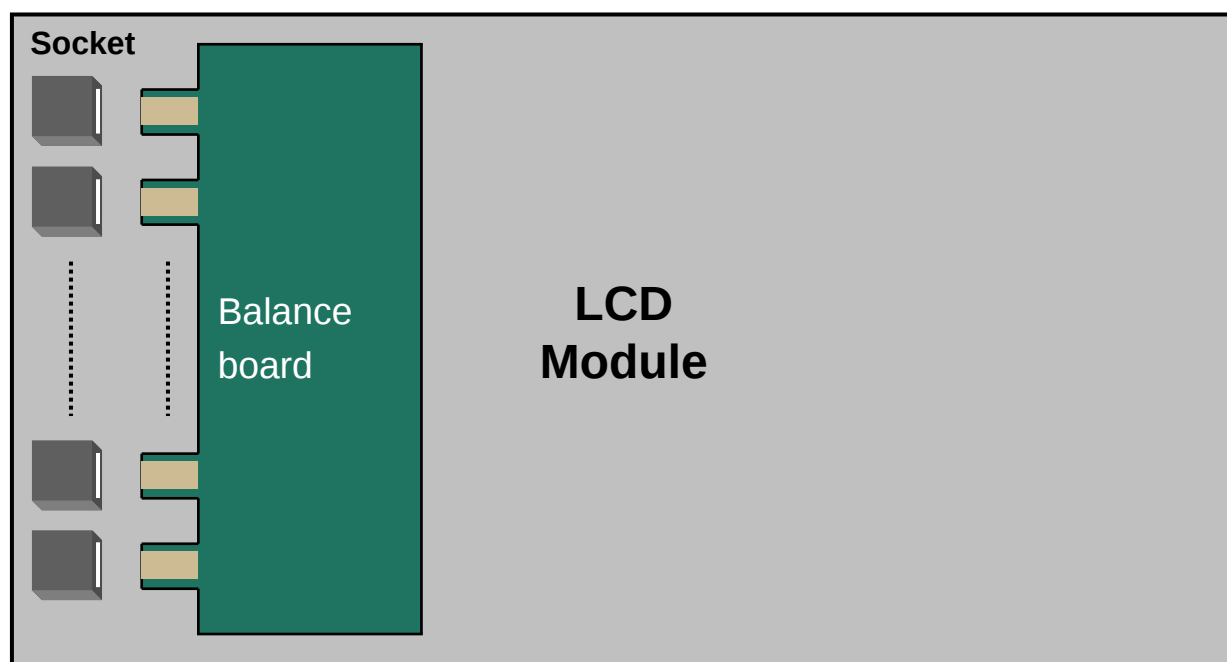


Rush Current I_{RUSH} can be measured when T_{RUSH} is $470\mu\text{s}$.

3.2 Back Light Unit

The back light unit contains 20 direct-lighting type CCFLs (Cold Cathode Fluorescent Lamp).

Ta=25 ± 2°C

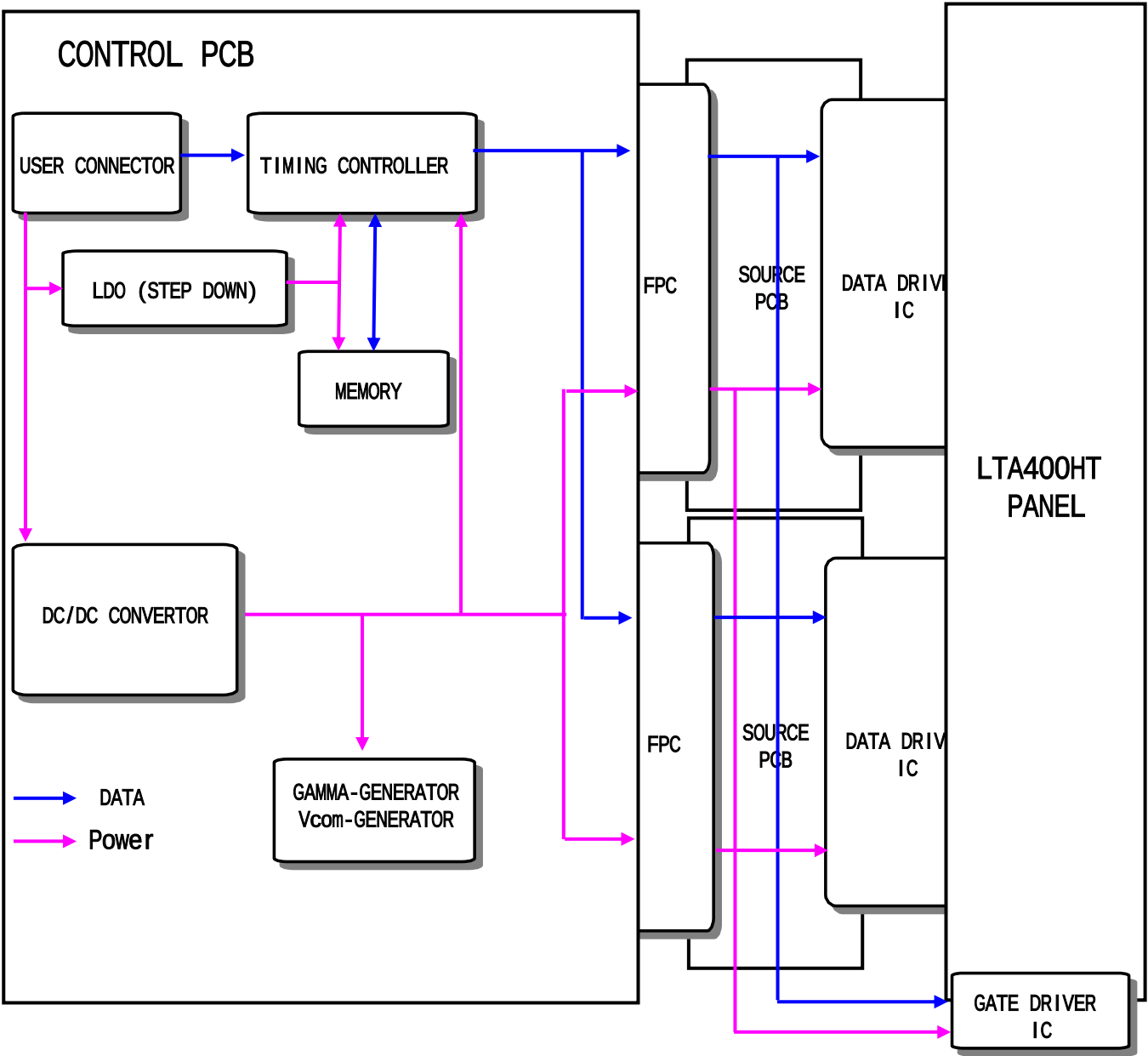


Item	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Life Time	Hr	30,000	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Operating condition : Ta = 25±2 , I_L = 7mA , For single lamp only.]

4. Block Diagram



5. Input Terminal Pin Assignment

5.1. Input Signal & Power

Connector : FI-RE51S-HF (JAE)

Pin	Symbol	Description	Pin	Symbol	Description
1	Power	DC 12V	26	RE[0]P	Even LVDS Signal +
2	Power	DC 12V	27	RE[1]N	Even LVDS Signal -
3	Power	DC 12V	28	RE[1]P	Even LVDS Signal +
4	Power	DC 12V	29	RE[2]N	Even LVDS Signal -
5	Power	DC 12V	30	RE[2]P	Even LVDS Signal +
6	GND	GND	31	GND	GND
7	GND	GND	32	ROCLK -	Even LVDS Signal -
8	GND	GND	33	ROCLK+	Even LVDS Signal +
9	GND	GND	34	GND	GND
10	RO[0]N	Odd LVDS Signal -	35	RE[3]N	Even LVDS Signal -
11	RO[0]P	Odd LVDS Signal +	36	RE[3]P	Even LVDS Signal +
12	RO[1]N	Odd LVDS Signal -	37	RE[4]N	Even LVDS Signal -
13	RO[1]P	Odd LVDS Signal +	38	RE[4]P	Even LVDS Signal +
14	RO[2]N	Odd LVDS Signal -	39	GND	
15	RO[2]P	Odd LVDS Signal +	40	NC	NOTE 1
16	GND	GND	41	NC	
17	ROCLK -	Odd LVDS Signal -	42	NC	
18	ROCLK+	Odd LVDS Signal +	43	NC	
19	GND	GND	44	NC	
20	RO[3]N	Odd LVDS Signal -	45	LVDS_SEL	NOTE 2
21	RO[3]P	Odd LVDS Signal +	46	NC	NOTE 1
22	RO[4]N	Odd LVDS Signal -	47	NC	
23	RO[4]P	Odd LVDS Signal +	48	NC	
24	GND	GND	49	NC	
25	RE[0]N	Even LVDS Signal -	50	NC	
			51	NC	NOTE 1

Note1) No Connection: This PINS are only used for SAMSUNG internal using.

Note2) LVDS OPTION : If this PIN is HIGH (3.3 V) → Normal LVDS format

LOW (GND) → JEIDA LVDS format

SEQUENCE : On = $V_{DD}(T1) \geq \text{LVDS Option} \geq \text{Interface Signal}(T2)$

OFF = $\text{Interface Signal}(T3) \geq \text{LVDS Option} \geq V_{DD}$

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Note(1) Pin number starts from Right side

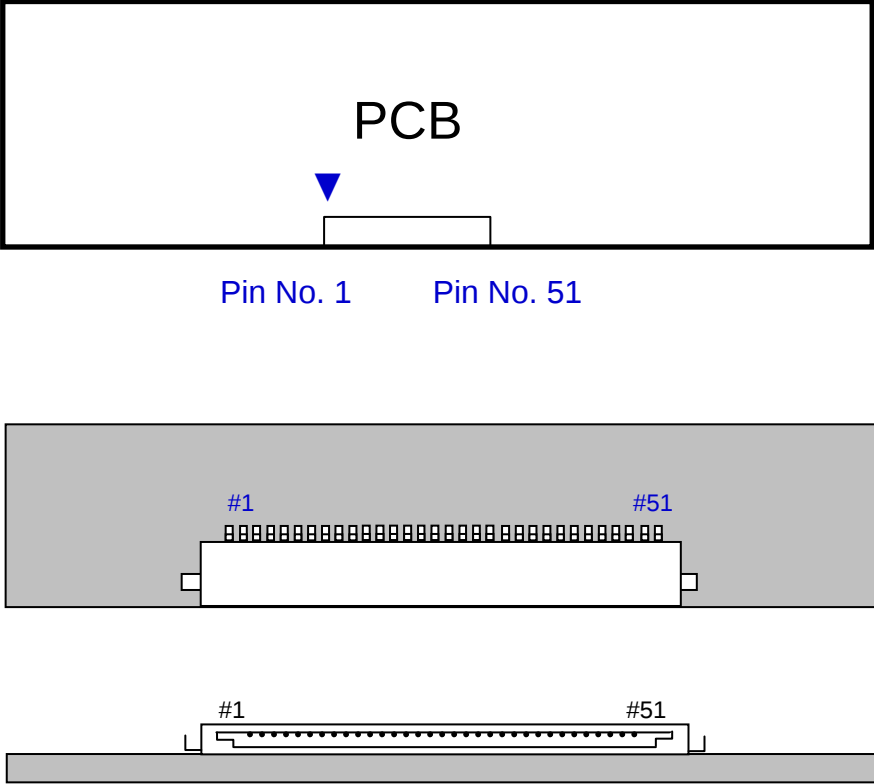


Fig. Connector diagram

- a. Power GND pins should be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All NC pin should be separated from other signal or power.

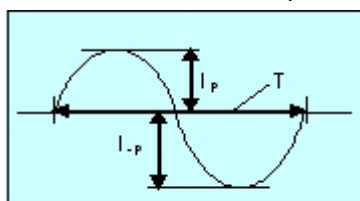
5.2. Balance Board

5.2.1 Operation Condition

Item	Symbol	Recommendation			Unit	Note	Remark
		Min.	Typ.	Max.			
Total Input Current	I_T	162	172	182	mA	(1)	Max Dimming
Inverter Frequency	F_{OP}	42	44	46	kHz		Switching Frequency
Dimming Frequency	F_{DIM}	140	150	160	Hz		-
Dim Duty Ratio	D_{PWM}	20	-	100	%	-	Bright Control
Striking Voltage	HV_{STRIKE}	2300	-	-	Vrms	(2)	-
Shutdown time	$T_{SHUTDOWN}$	1.0	-	2.0	Sec		Striking Mode

Note (1) Asymmetric ratio must less than 10 % ($|I_p - I_{-p}| / I_{rms} < 0.1$)

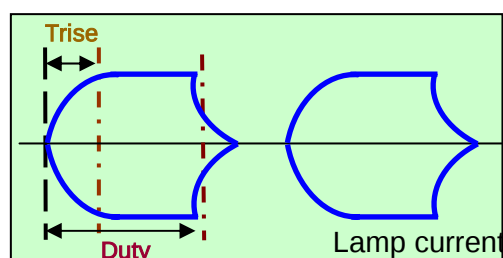
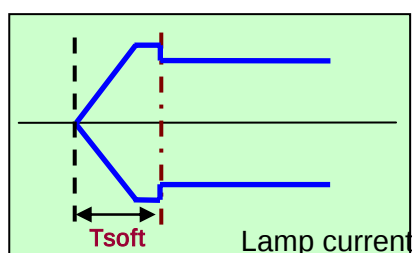
Crest factor must be from 90 % to 110 % ($0.9 < I_p$ or $I_{-p} / (I_{rms} \times \sqrt{2}) < 1.1$)



(2) Lamp current at Striking mode based on CCFL spec. for ambient temperature.

Soft start must be

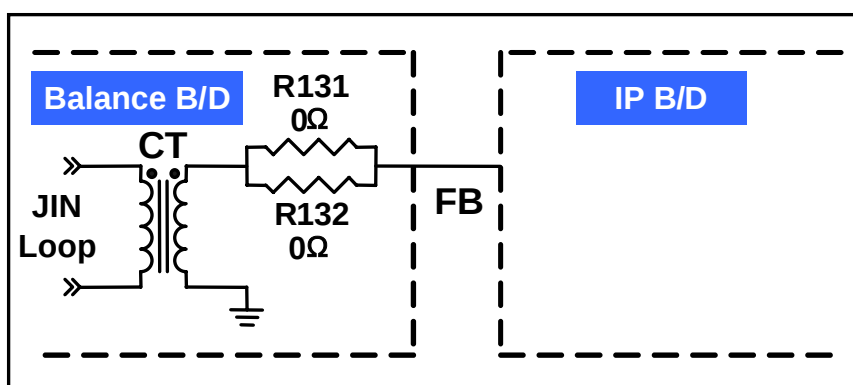
- at starting time $T_{soft} > 5$ msec at PWM Dimming 80% including soft start period



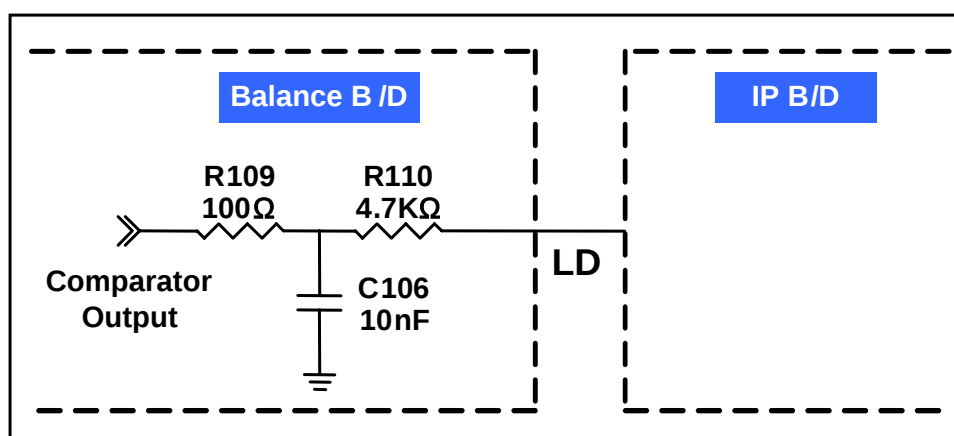
5.2.2 Feedback I/O Specification

ITEM		SYMBOL	Recommendation			UNIT	NOTE	REMARKS
			Min.	Typ.	Max.			
Supply Voltage		V_{CC}	10.8	12	13.2	Vdc		-
Input Current		I_C	20	-	100	mA		at Recommended Load Condition
Lamp Detection	High	V_{LD}	5	$V_{CC}-1$	V_{CC}	Vdc	(2)	Normal : High Abnormal : Low
	Low		-	-	1.5			

Note (1) Balance Board FB Output



(2) Balance Board LD Output



5.2.3 Balance Board Pin map

1) CN1101 Connector : SM02-BDAS(JST)

Pin NO	SYMBOL	REMARK
1	HOT	High Voltage Input A
2	HOT	High Voltage Input B (It is reverse polarity to A)

2) CN 103 Connector : KN30-5P-1.25C (HIROSE)

Pin NO	SYMBOL	REMARK
1	VCC	Power Supply for Protection Circuit
2	FB	Feedback
3	GND	Signal Ground
4	NC	NC
5	LD	CCFL Connector Open & Non-lighting signal

5.3 LVDS Interface

- LVDS Receiver : Tcon (merged)
- Data Format (JEIDA&VESA)

	LVDS pin	JEIDA -DATA	VESA-DATA
TxOUT/RxIN0	TxIN/RxOUT0	R4	R0
	TxIN/RxOUT1	R5	R1
	TxIN/RxOUT2	R6	R2
	TxIN/RxOUT3	R7	R3
	TxIN/RxOUT4	R8	R4
	TxIN/RxOUT6	R9	R5
	TxIN/RxOUT7	G4	G0
TxOUT/RxIN1	TxIN/RxOUT8	G5	G1
	TxIN/RxOUT9	G6	G2
	TxIN/RxOUT12	G7	G3
	TxIN/RxOUT13	G8	G4
	TxIN/RxOUT14	G9	G5
	TxIN/RxOUT15	B4	B0
	TxIN/RxOUT18	B5	B1
TxOUT/RxIN2	TxIN/RxOUT19	B6	B2
	TxIN/RxOUT20	B7	B3
	TxIN/RxOUT21	B8	B4
	TxIN/RxOUT22	B9	B5
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R2	R6
	TxIN/RxOUT5	R3	R7
	TxIN/RxOUT10	G2	G6
	TxIN/RxOUT11	G3	G7
	TxIN/RxOUT16	B2	B6
	TxIN/RxOUT17	B3	B7
	TxIN/RxOUT23	RESERVED	RESERVED
TxOUT/RxIN4	TxIN/RxOUT28	R0	R8
	TxIN/RxOUT29	R1	R9
	TxIN/RxOUT30	G0	G8
	TxIN/RxOUT31	G1	G9
	TxIN/RxOUT32	B0	B8
	TxIN/RxOUT33	B1	B9
	TxIN/RxOUT34	RESERVED	RESERVED

5.4 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY (8bit)	DATA SIGNAL																												GRAY SCALE LEVEL		
		RED										GREEN										BLUE										
		R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	G0	G1	G2	G3	G4	G5	G6	G7	G8	G9	B0	B1	B2	B3	B4	B5	B6	B7		B8	B9
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-	
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	-	
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	-	
	CYAN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-	
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-	
	MAGENTA	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0	
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1	
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~ R1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓ LIGHT	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1021
		0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1022
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1023
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0	
	DARK ↑	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~ G1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓ LIGHT	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1021
		0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1022
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	G1023
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0	
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	B1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	B2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~ B1020
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	↓ LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	B1021
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	B1022
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	B1023

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

6. Interface Timing

6.1 Timing Parameters (DE only mode)

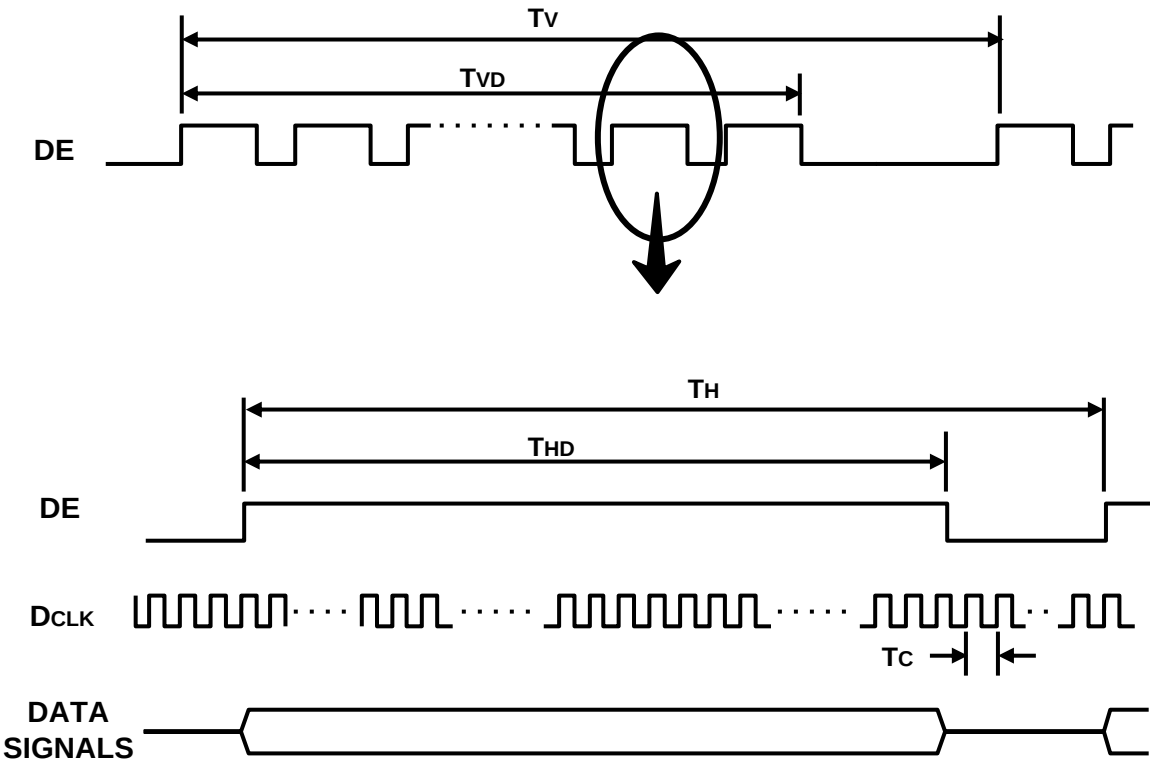
SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	130	148.5	155	MHz	-
Hsync		F_H	48	67.5	75	KHz	-
Vsync		F_V	45	60	65	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	-	1080	-	Lines	-
	Vertical Total	T_V	1118	1125	1550	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	-	1920	-	Clock s	-
	Horizontal Total	T_H	2160	2200	2650	clocks	-

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

(1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system

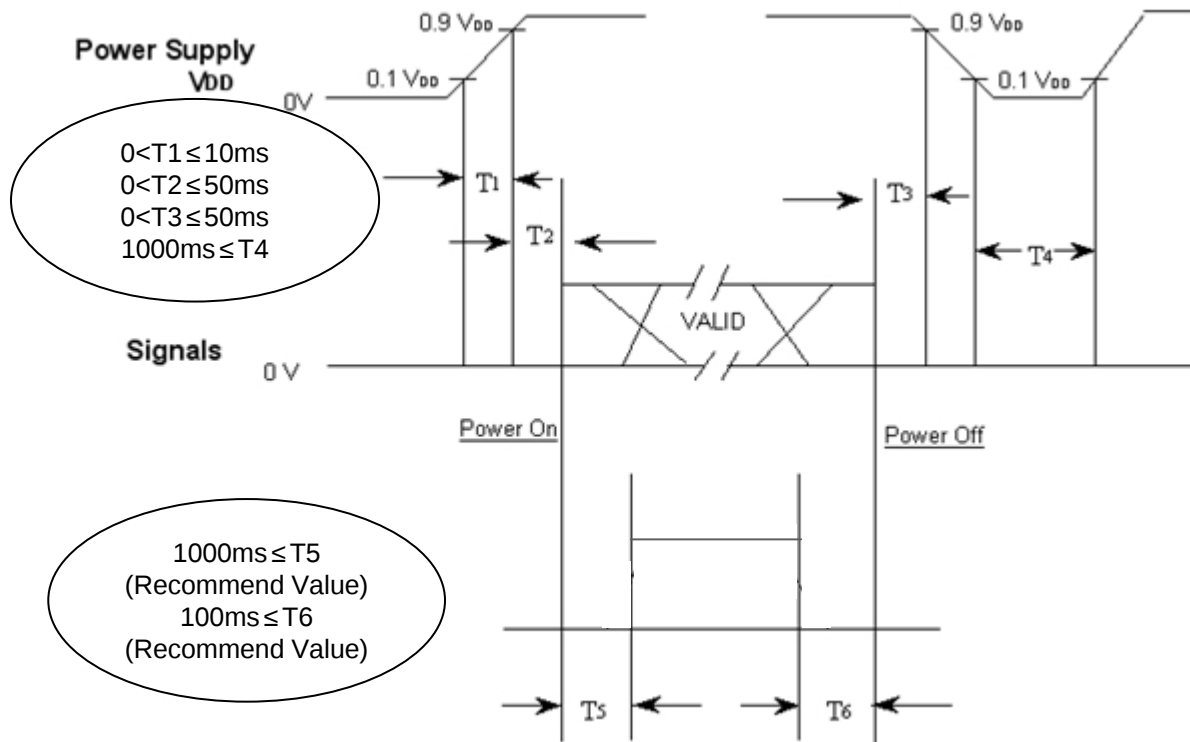
(2) Internal $V_{DD} = 3.3V$

6.2 Timing diagrams of interface signal (DE only mode)



6.3 Power ON/OFF Sequence

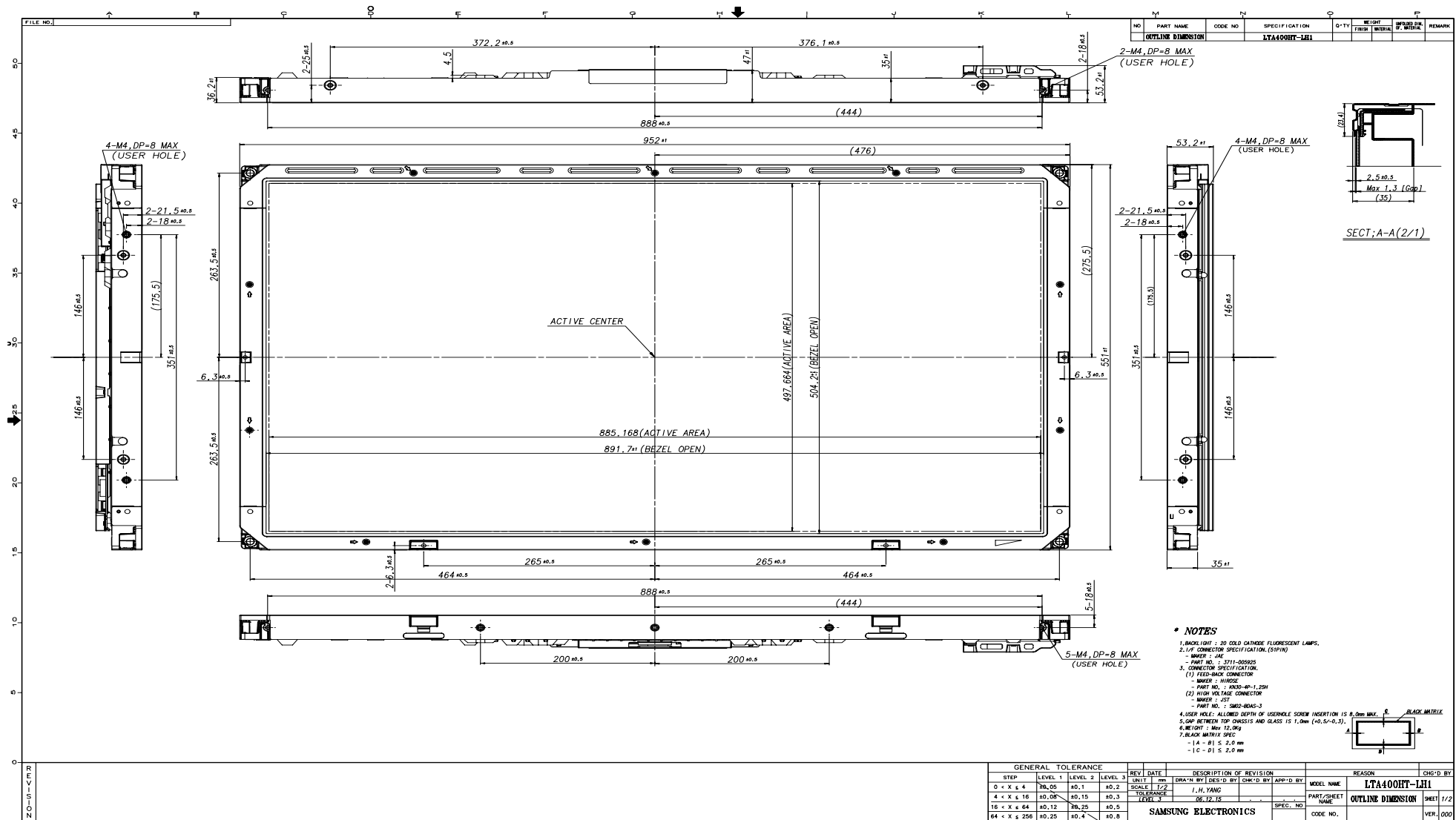
To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



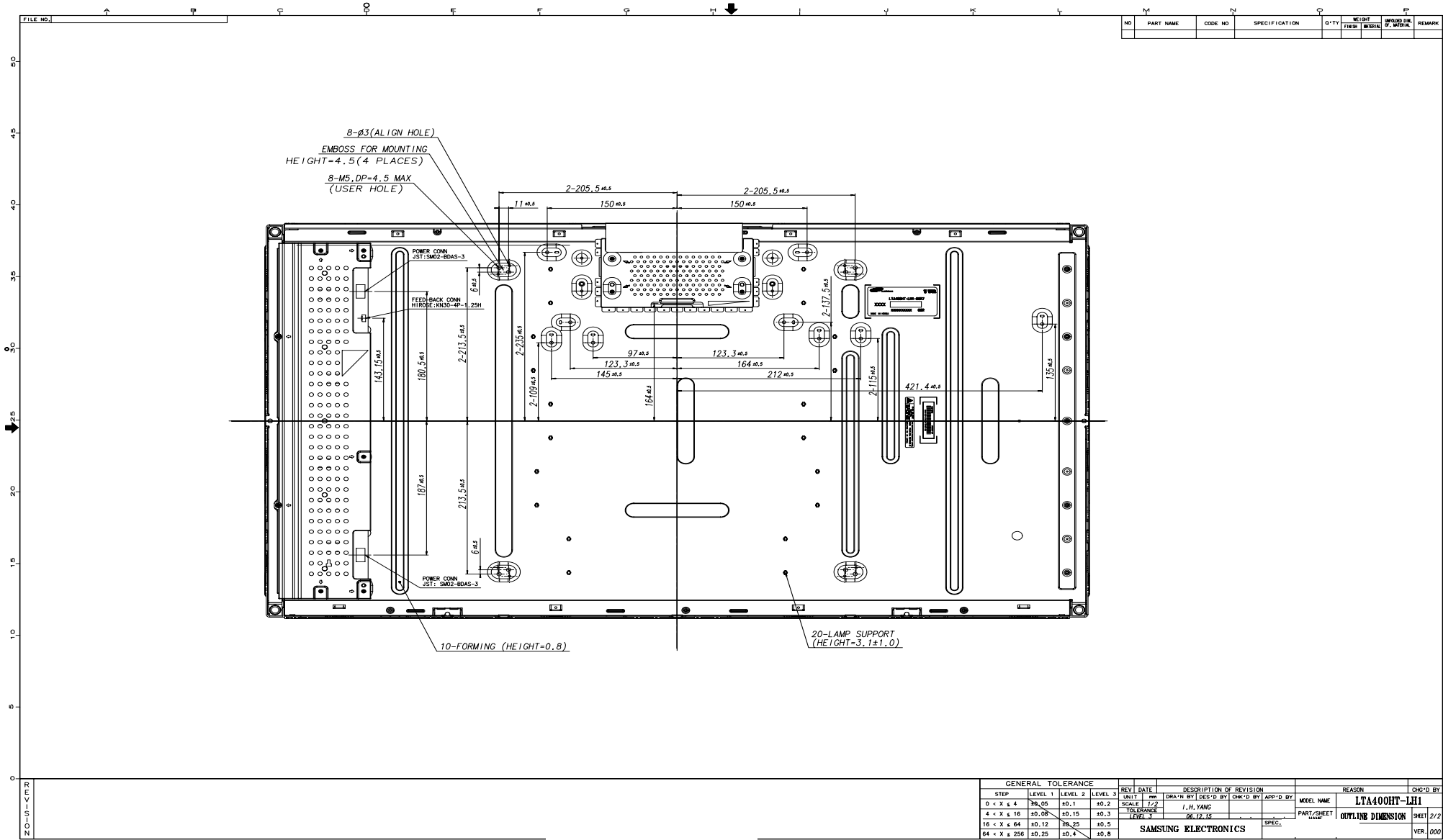
- T1 : V_{DD} rising time from 10% to 90%
- T2 : The time from V_{DD} to valid data at power ON.
- T3 : The time from valid data off to V_{DD} off at power Off.
- T4 : V_{DD} off time for Windows restart
- T5 : The time from valid data to B/L enable at power ON.
- T6 : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

7. Outline Dimension (Front View)



7. Outline Dimension (Rear View)



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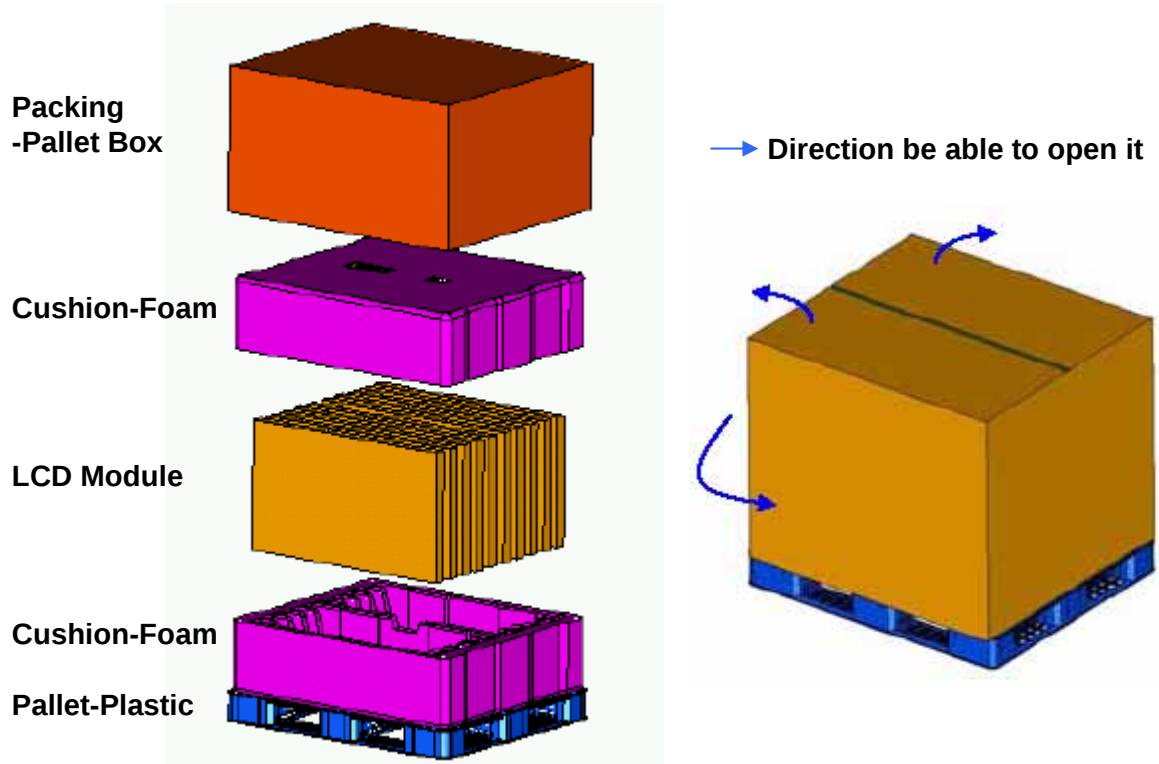
8. PACKING

8.1 CARTON (Internal Package)

(1) Packing Form

Corrugated fiberboard box and corrugated cardboard as shock absorber

(2) Packing Method



8.2 Packing Specification

Item	Specification	Remark
LCD Packing	10ea / (Packing-Pallet Box)	1. 110 Kg / LCD (10ea) 2. 7 Kg / Cushion-pallet (2ea) 3. 6.7 Kg / Packing-Pallet Box (1ea) 4. Cushion-pallet Material : EPS 5. Packing-Pallet Box Material : DW4
Pallet	1Box / Pallet	1. Pallet weight = 8kg 2. 8Kg/Pallet
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1150mm(H) x 985mm(V) x 609mm(height)
Total Pallet Weight	131.7 kg	Pallet(8kg) + Module(11*10=110) + Cushion(up+bottom=7kg) + Pallet-BOX(6.7kg)

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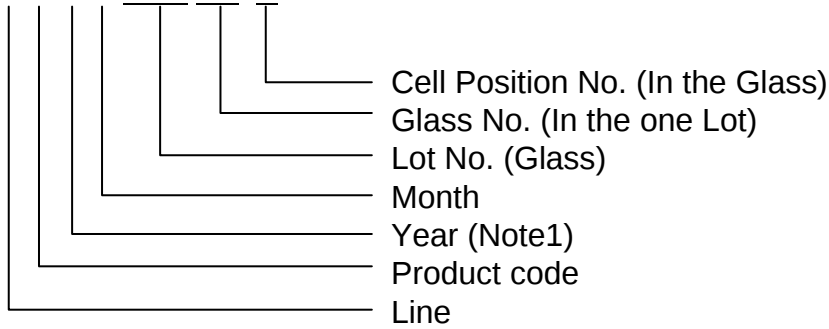
9. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

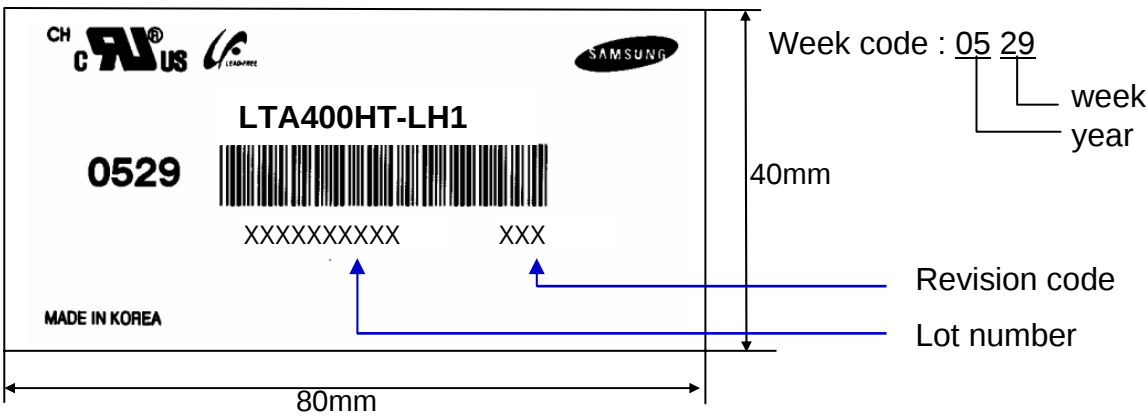
(1) Part number : LTA400HT-LH1

(2) Revision: Three letters

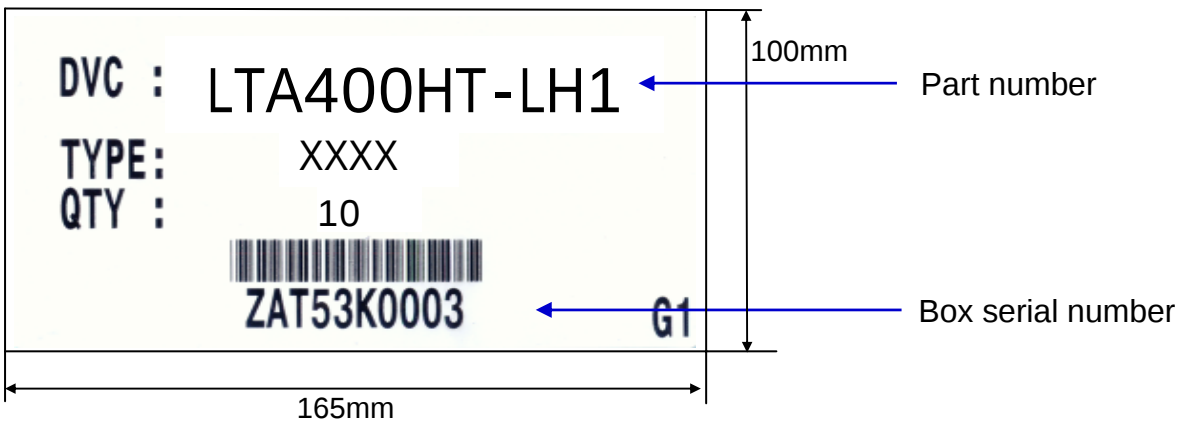
(3) Lot number : X X X X XXX XX X



(4) Nameplate Indication



(5) Packing box attach



(6) Others

1. After service part

Lamps cannot be replaced because of the narrow bezel structure.

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10. General Precautions

10.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module.
In addition to damage, this may cause improper operation or damage to the Module and CCFL back light.
- (d) Note that polarizers are very fragile and could be damage easily.
Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane.
Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or Semiconductor would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (l) Do not disassemble shield case of inverter & LVDS board.
- (m) Do not connect N.C pins. (Samsung internal use only)
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handle a module
- (o) Pins of I/F connector should not be touched directly with bare hands.

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10.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35 and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

10.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

10.4 Operation Condition Guide

- (a) The LCD product should be operated under normal conditions.
Normal condition is defined as below;
 - Temperature : 20 ± 15
 - Humidity : $55 \pm 20\%$
 - Display pattern : continually changing pattern (Not stationary)
- (b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

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10.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.
To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.

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