



**Samsung Secret**

Product Information

Model No. :  
Parts Code : V33A00053600

**Customer : Toshiba**

**DATE : 21. Dec. 2011**

**SAMSUNG TFT-LCD**  
**MODEL : LTA400HM22**

The Information Described in this Specification is Preliminary and can be changed without prior notice

APPROVED BY	DATE	PREPARED BY	DATE
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LCD Business

Samsung Electronics Co . , LTD.

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## General Description

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### Description

LTA400HM22 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT (Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 40.0" is 1,920 x 1,080 and this model can display up to 1.07 Billion colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide an excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

### Features

- RoHS compliance (Pb-free)
- High contrast ratio & aperture ratio with wide color gamut
- SPVA (Super Patterned Vertical Align) mode
- Wide viewing angle ( $\pm 178^\circ$ )
- High speed response
- FHD resolution (16:9)
- Low Power consumption
- Edge Type LED (Light Emitting Diode) BLU
- DE (Data Enable) mode
- 2ch LVDS (Low Voltage Differential Signaling) interface (2pixel/clock)

### General Information

Items	Specification	Unit	Note
Module Size	912.4 (W) x 527.6(V)	mm	$\pm 1$
	33.2 (Max)		T/C~Converter
Weight	8700	g	Typ.
Pixel Pitch	0.46(H) x 0.46(V)	mm	
Active Display Area	885.6(H) x 498.15(V)	mm	
Surface Treatment	Haze 2 %	-	Anti Glare
Display Colors	1.07 Billion	colors	8bit FRC
Number of Pixels	1,920 x 1,080	pixel	
Pixel Arrangement	RGB vertical stripe	-	
Display Mode	Normally Black	-	
Luminance of White	350 (Typ.)	cd/m <sup>2</sup>	

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## 1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

(V<sub>DD</sub>=12V)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V <sub>DD</sub>	GND-0.3	13.2	V	(1)
Storage temperature	T <sub>STG</sub>	-20	60	°C	(2)
Surface temperature	T <sub>SUR</sub>	0	65	°C	(3)
Operation temperature	T <sub>OPR</sub>	0	50	°C	(2)
Shock ( non - operating )	S <sub>nop</sub>	-	50	G	(4)
Vibration ( non - operating )	V <sub>nop</sub>	-	1.5	G	(5)

Note (1) Ta= 25 ± 2 °C

(2) Temperature and relative humidity range are shown in the figure below.

a. 90 % RH Max. (Ta ≤ 39 °C)

b. Relative Humidity is 90% or less. (Ta > 39 °C)

c. No condensation

(3) Although abnormal visual problems can be occurred in Tsur range, the polarizer is not damaged in this range.

(4) 11ms, sine wave, one time for ±X, ±Y, ±Z axis

(5) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

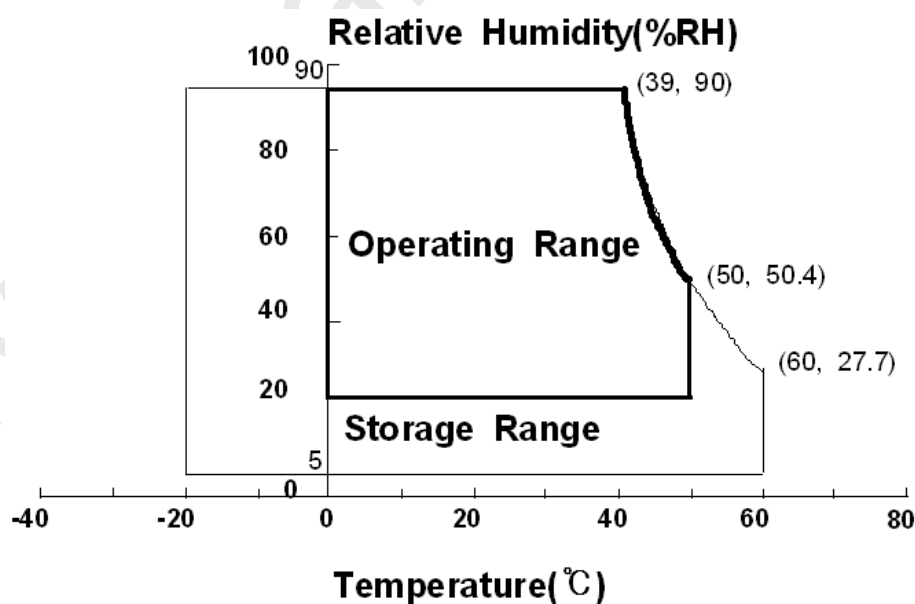


Fig. Temperature and Relative humidity range

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## 2. Optical Characteristics

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The optical characteristics should be measured in a dark room or equivalent.

Measuring equipment : TOPCON RD-80S, TOPCON SR-3, ELDIM EZ-Contrast

(Ta = 25 ± 2°C, VDD=12V, fv= 60Hz, fDCLK = 148.5 MHz, LED Current = 140 mA)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio (Center of screen)	C/R		4000	5000	-		(1) SR-3
Response Time G-to-G	Tg		-	10	16	msec	(3) RD-80S
Luminance of White (Center of screen)	Y <sub>L</sub>		300	350	-	cd/m <sup>2</sup>	(4) SR-3
Color Chromaticity (CIE 1931)	Red	Rx	Normal q <sub>L,R</sub> = 0 q <sub>U,D</sub> = 0  Viewing Angle	TYP. -0.03	0.642	TYP. +0.03	(5),(6) SR-3
		Ry			0.332		
	Green	Gx			0.318		
		Gy			0.604		
	Blue	Bx			0.152		
		By			0.053		
	White	Wx			0.280		
		Wy			0.290		
Color Gamut	-		-	70	-	%	(5) SR-3
Color Temperature	-		-	10,000	-	K	
Viewing Angle	Hor.	q <sub>L</sub>	C/R ≥ 10	75	89	-	Degree
		q <sub>R</sub>			89		
	Ver.	q <sub>U</sub>			89		
		q <sub>D</sub>			89		
White Brightness Uniformity (9 Points)	B <sub>uni</sub>		-	-	30	%	(2) SR-3

### - Test Equipment Setup

The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

Environment condition : Ta = 25 ± 2 °C

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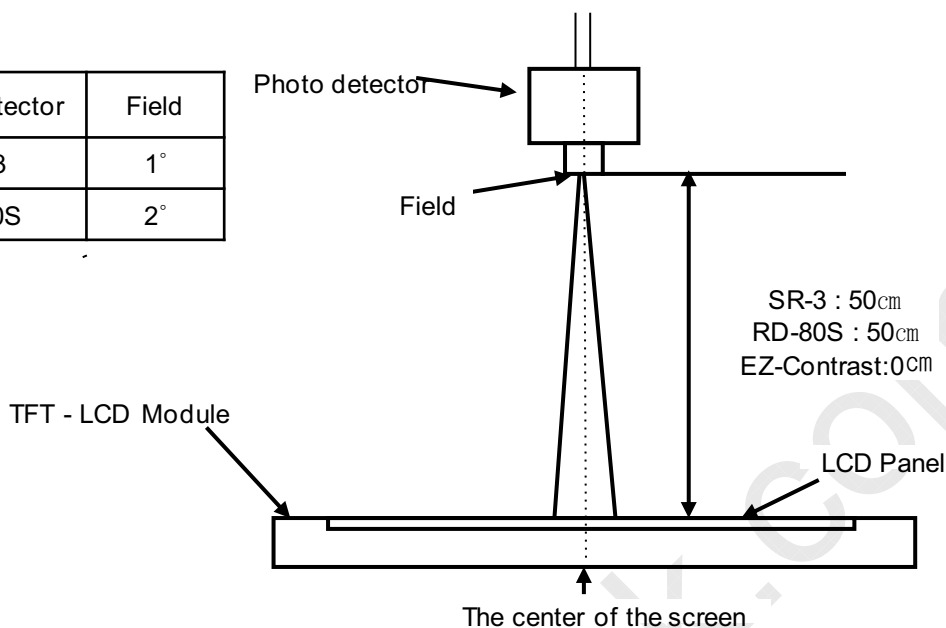
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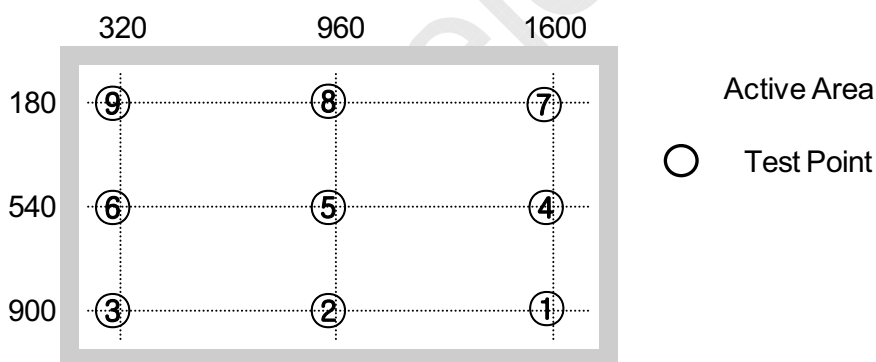
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Photo detector	Field
SR-3	1°
RD-80S	2°



- Definition of test point



Note (1) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G_{max}}{G_{min}}$$

Gmax : Luminance with all pixels white

Gmin : Luminance with all pixels black

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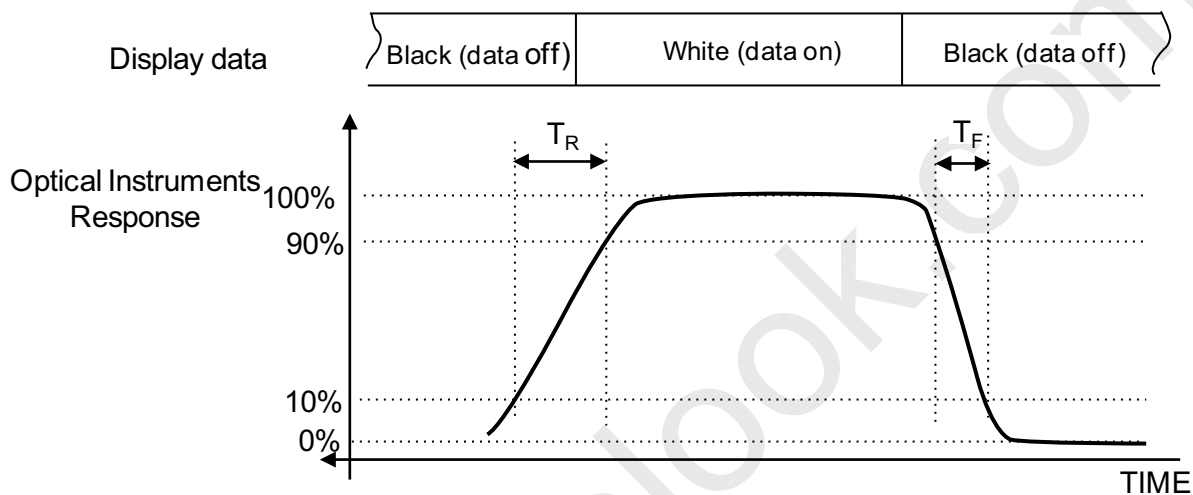
Note (2) Definition of 9 points brightness uniformity (Test pattern : Full White)

$$Buni = 100 * \frac{(B_{max} - B_{min})}{B_{max}}$$

Bmax : Maximum brightness

Bmin : Minimum brightness

Note (3) Definition of Response time : Sum of Tr, Tf



※ G-to-G : Average response time between Gray to Gray (Scale)

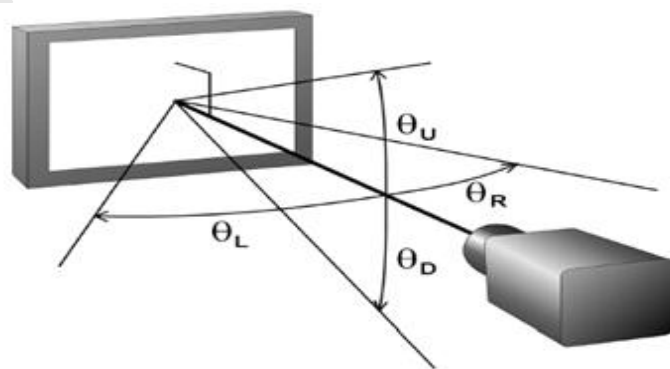
Note (4) Definition of Luminance of White : Luminance of white at center point ⑤

Note (5) Definition of Color Chromaticity (CIE 1931)

Color coordinate of Red, Green, Blue & White at center point ⑤

Note (6) Definition of Viewing Angle

: Viewing angle range (C/R ≥ 10)



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### 3. Electrical Characteristics

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#### 3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

$T_a = 25^\circ\text{C} \pm 2^\circ\text{C}$

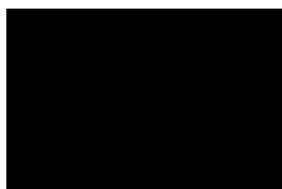
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply	$V_{DD}$	10.8	12.0	13.2	V	(1)
Current of Power Supply	(a) Black	-	330	-	mA	(2),(3)
	(b) White	-	350	-	mA	
	(c) Sub V-Stripe	-	525	600	mA	
Vsync Frequency	$f_V$	47	60	63	Hz	
Hsync Frequency	$f_H$	50	67.5	75	kHz	
Main Frequency	$f_{DCLK}$	130	148.5	160	MHz	
Rush Current	$I_{RUSH}$	-	-	3	A	(4)

Note (1) The ripple voltage should be controlled under 10% of  $V_{DD}$ .

(2)  $f_V = 60\text{ Hz}$ ,  $f_{DCLK} = 148.5\text{ MHz}$ ,  $V_{DD} = 12.0\text{V}$ , DC Current.

(3) Power dissipation check pattern (LCD Module only)

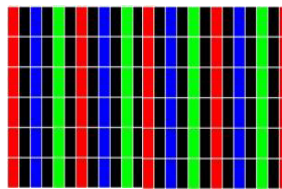
a) Black Pattern



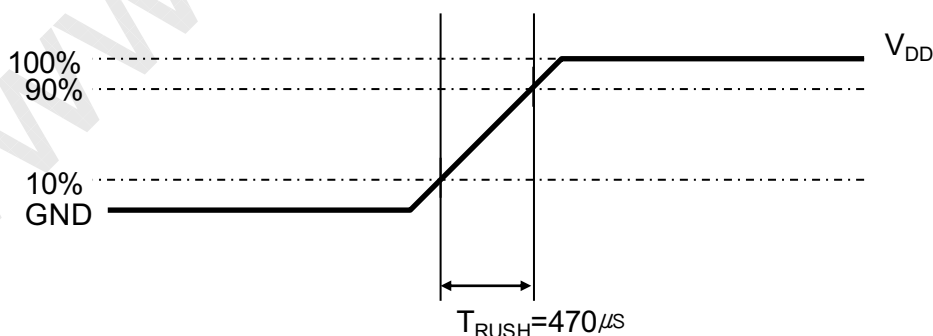
b) White Pattern



c) N-Pattern



(4) Measurement Conditions



Rush Current  $I_{RUSH}$  can be measured when  $T_{RUSH}$  is  $470\mu\text{s}$ .

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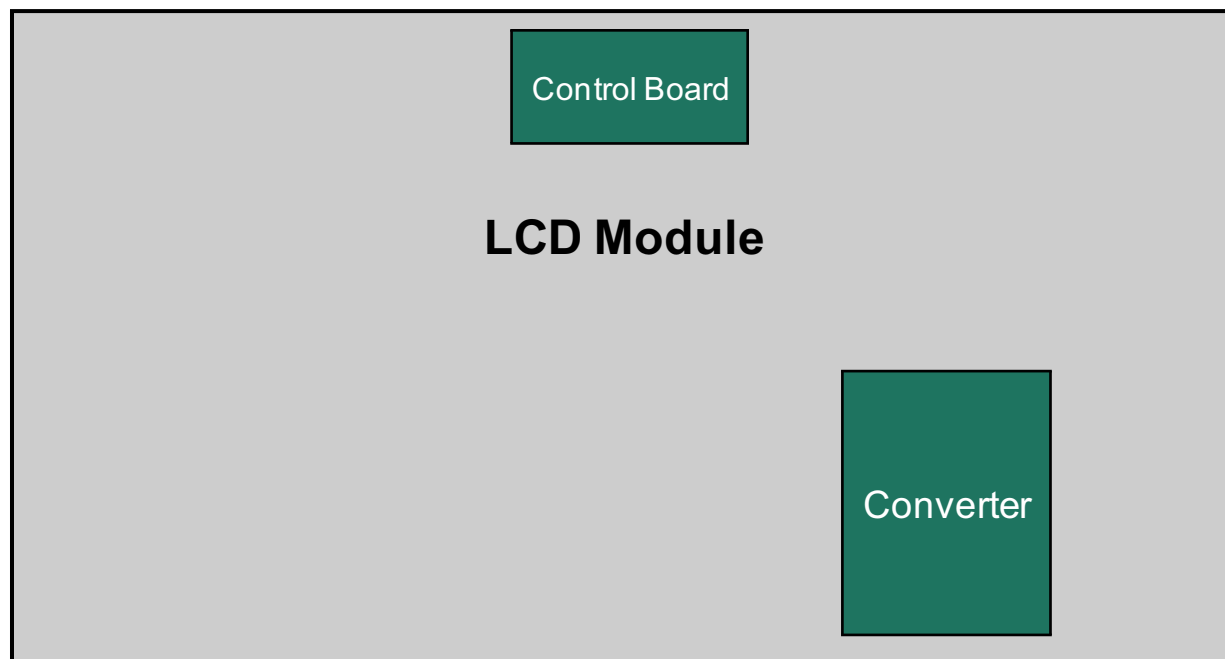


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### 3.2 Back Light Unit

The back light unit contains Edge type White LEDs (Light Emitting Diode)

$T_a = 25 \pm 2^\circ\text{C}$



Item	Symbol	Min.	Typ.	Max.	Unit	Note
Operating Life Time	Hr	-	31,000	-	Hour	(1),(2)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value. [Operating condition :  $T_a = 25 \pm 2^\circ\text{C}$ , For LED Package only.]

(2) Test Condition :  $T_j : 110^\circ\text{C}$

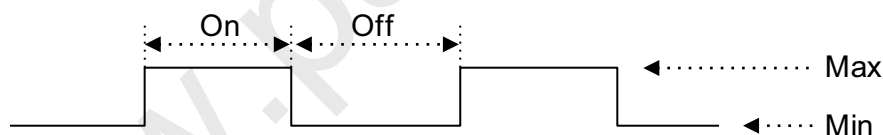
## 3.3 Converter Input Condition &amp; Specification

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Items	Symbol	Conditions	Specifications			Unit	Note
			Min.	Typ.	Max.		
Input Voltage	V <sub>in</sub>	-	22	24	26	V	Ta=25±2 °C
Input Current	I <sub>RUSH (2D)</sub>	V <sub>in</sub> =24.0V Dim = MAX	-	-	3.84	A	Note (1)
Output Current	I <sub>O (2D)</sub>	V <sub>in</sub> =24.0V Dim = MAX	133	140	147	mA	Note (2)
Backlight On/Off	ON	V <sub>in</sub> =24.0 V	2.4	-	5.5	V	
	OFF		-0.3	-	0.8		
Internal Dimming	V <sub>INT_DIM</sub>	V <sub>in</sub> =24.0 V	0	-	3.3	V	Note (3,5)
	F <sub>INT_DIM</sub>		140	150	160	Hz	
	D <sub>INT_DIM</sub>		1	-	100	%	
External Dimming	D <sub>EXT_DIM</sub>	V <sub>in</sub> =24.0V	1	-	100	%	Note (3,5)
	F <sub>EXT_DIM</sub>		-	100	-	Hz Note(4)	
			-	120	-	Hz Note(4)	
External Dimming Signal Level	V <sub>EXT_DIM</sub>	High (ON)	2.4	-	5.5	V	
		Low (Off)	-0.3	-	0.8		

Note) Power Consumption is measured when 350 [cd/m ] of luminance which is the typical luminance. Lamp Current is measured at the point before Lamp.

- (1) I<sub>RUSH</sub> is measured within BLU on 10ms after leaving the BLU as it is at least 1hr or more at 25°C.  
(2) All data was approved after running 120 minutes.  
(3) Internal PWM mode and External PWM mode are not available at the same time.  
(4) External Frequency : PAL Input ( 100Hz ), NTSC Input ( 120Hz )  
(5) Duty = On/(On+Off) \* 100



- Additional Appendix for Supply Current

Items	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Current	lin_overshoot	V <sub>in</sub> = 24V, Dim=3.3V (Within 1hr at BLU on)	-	2.4	2.47	A
	lin_saturation	V <sub>in</sub> = 24V, Dim=3.3V (After 1hr Aging)	-	2.33	2.4	A

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## 4. Input Terminal Pin Assignment

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### 4.1. Input Signal & Power

Connector :FI-RE51S-HF (JAE/UJU)

PIN No.	Description		PIN No.	Description	
1	VDD(12V)		26	Even LVDS Signal	RE[0]P
2	VDD(12V)		27		RE[1]N
3	VDD(12V)		28		RE[1]P
4	VDD(12V)		29		RE[2]N
5	VDD(12V)		30		RE[2]P
6	No Connection * Note (1)		31		GND
7	GND		32		RE[CLK]N
8	GND		33		RE[CLK]P
9	GND		34		GND
10	Odd LVDS Signal	RO[0]N	35		RE[3]N
11		RO[0]P	36		RE[3]P
12		RO[1]N	37		RE[4]N
13		RO[1]P	38		RE[4]P
14		RO[2]N	39		GND
15		RO[2]P	40	No Connection	
16		GND	41	No Connection	
17		RO[CLK]N	42	No Connection	
18		RO[CLK]P	43	No Connection	
19		GND	44	No Connection	
20		RO[3]N	45	LVDS Option *Note (2)	
21		RO[3]P	46	No Connection	
22		RO[4]N	47	No Connection	
23		RO[4]P	48	No Connection	
24	GND		49	No Connection	
25	Even LVDS	RE[0]N	50	No Connection	
			51	No Connection	

Note)(1) No Connection: This PINS are only used for SAMSUNG internal using.

(2) LVDS OPTION : If this PIN : HIGH (3.3 V) → Normal LVDS format  
: LOW (GND) → JEIDA LVDS format

SEQUENCE : On =  $V_{DD}(T1) \geq \text{LVDS Option} \geq \text{Interface Signal}(T2)$

OFF =  $\text{Interface Signal}(T3) \geq \text{LVDS Option} \geq V_{DD}$

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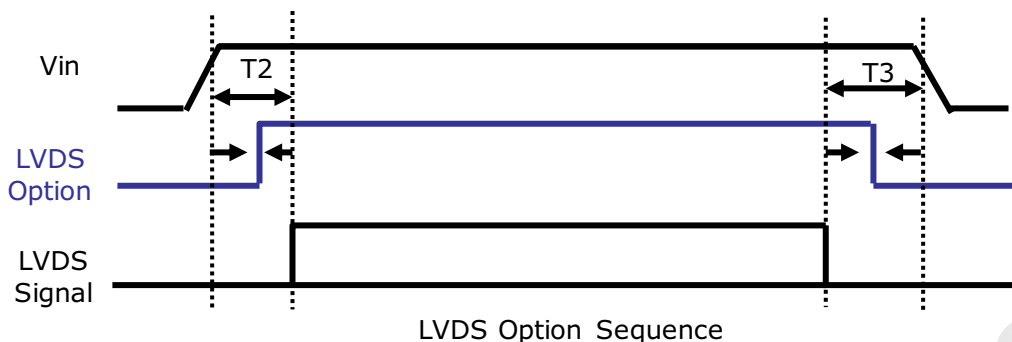
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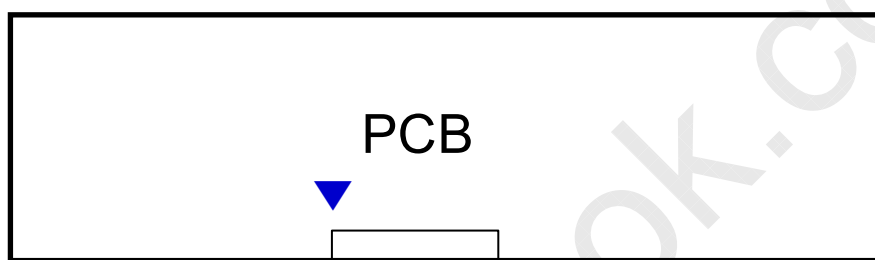
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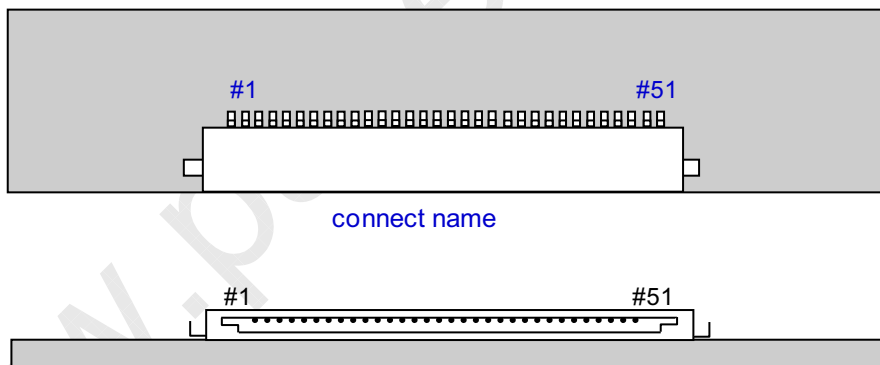
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Note(3) Pin number starts from Right side



Pin No. 1      Pin No. 51



**Fig. Connector diagram**

- a. All GND pins should be connected together and also be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All NC pins should be separated from other signal or power.

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## 4.2. Converter Input Pin Configuration

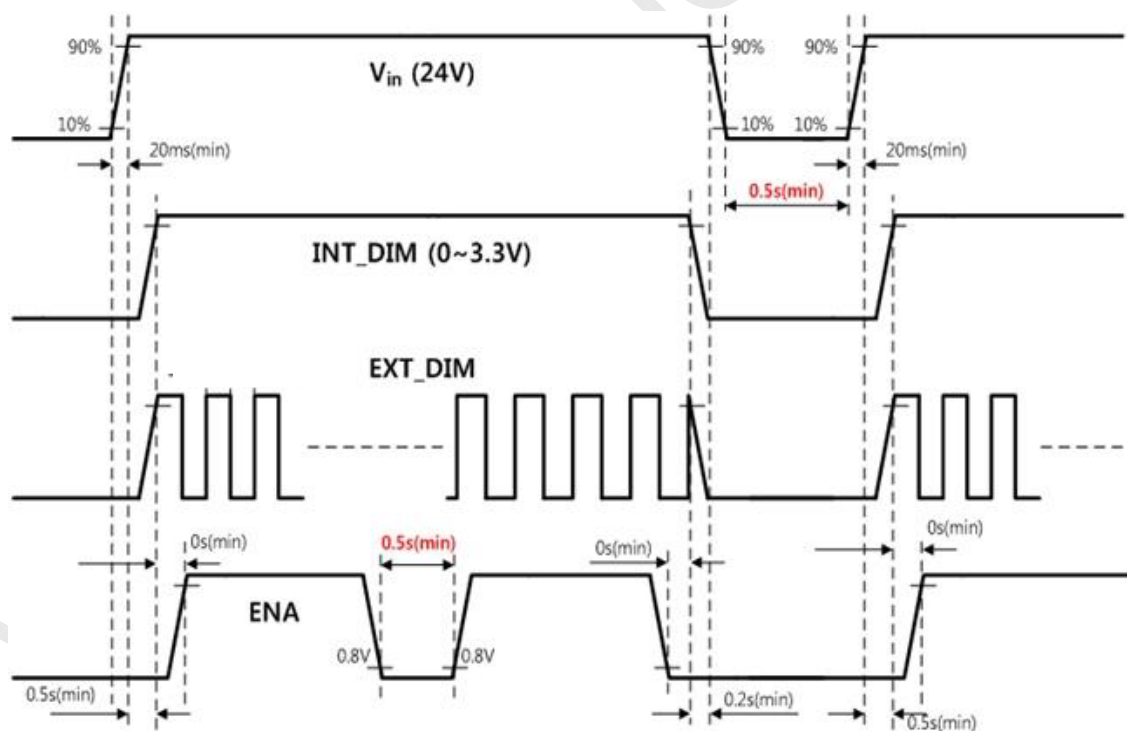
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Connector : Yeon-ho, 20022WR-14B1

Pin No.	Pin Configuration (FUNCTION)
	Master
1~5	24 V
6~10	GND
11	Error Out
12	Backlight On /Off
13	Internal Dimming
14	External PWM

Note(1) If use **Internal Dimming**, Pin 14 Must be N.C  
 If use External PWM, Pin 13 Must be N.C

## 4.3. Converter Input Power Sequence



Note) SEQUENCE : ON =  $V_{in}(24V) > \text{Dimming Control} \geq \text{Backlight On/Off}$   
 OFF =  $\text{Backlight On/Off} \geq \text{Dimming Control} > V_{in}(24V)$

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## 4.4 LVDS Interface

- LVDS Receiver : Tcon (merged)
- Data Format (JEIDA & Normal)

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	LVDS pin	JEIDA -DATA	Normal -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R4	R0
	TxIN/RxOUT1	R5	R1
	TxIN/RxOUT2	R6	R2
	TxIN/RxOUT3	R7	R3
	TxIN/RxOUT4	R8	R4
	TxIN/RxOUT6	R9	R5
	TxIN/RxOUT7	G4	G0
TxOUT/RxIN1	TxIN/RxOUT8	G5	G1
	TxIN/RxOUT9	G6	G2
	TxIN/RxOUT12	G7	G3
	TxIN/RxOUT13	G8	G4
	TxIN/RxOUT14	G9	G5
	TxIN/RxOUT15	B4	B0
	TxIN/RxOUT18	B5	B1
TxOUT/RxIN2	TxIN/RxOUT19	B6	B2
	TxIN/RxOUT20	B7	B3
	TxIN/RxOUT21	B8	B4
	TxIN/RxOUT22	B9	B5
	TxIN/RxOUT24	HSYNC	HSYNC
	TxIN/RxOUT25	VSYNC	VSYNC
	TxIN/RxOUT26	DEN	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R2	R6
	TxIN/RxOUT5	R3	R7
	TxIN/RxOUT10	G2	G6
	TxIN/RxOUT11	G3	G7
	TxIN/RxOUT16	B2	B6
	TxIN/RxOUT17	B3	B7
	TxIN/RxOUT23	RESERVED	RESERVED
TxOUT/RxIN4	TxIN/RxOUT28	R0	R8
	TxIN/RxOUT29	R1	R9
	TxIN/RxOUT30	G0	G8
	TxIN/RxOUT31	G1	G9
	TxIN/RxOUT32	B0	B8
	TxIN/RxOUT33	B1	B9
	TxIN/RxOUT34	RESERVED	RESERVED

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### 4.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY (10bit)	DATA SIGNAL																													GRAY SCALE LEVEL					
		RED										GREEN										BLUE														
		R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	G0	G1	G2	G3	G4	G5	G6	G7	G8	G9	B0	B1	B2	B3	B4	B5	B6	B7	B8		B9				
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0	
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1	
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R1020
		↓ LIGHT	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1021
	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1022	
	RED	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1023
	GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
DARK ↑		0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G1020
		↓ LIGHT	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	G1021
0		0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	G1022	
GREEN	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1023	
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0	
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	B1	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	B2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B1020
		↓ LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	1	1	1	B1021
	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	B1022	
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	B1023

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

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## 5. Interface Timing

### 5.1 Timing Parameters ( DE mode )

SIGNAL	ITEM	SYMBOL	MIN.	TYP.	MAX.	Unit	NOTE
Clock	Frequency	$1/T_C$	130	148.5	160	MHz	-
Hsync		$F_H$	50.0	67.5	75.0	KHz	-
Vsync		$F_V$	47.0	60.0	63.0	Hz	-
Vertical Display Term	Active Display Period	$T_{VD}$	-	1080	-	Lines	-
	Vertical Total	$T_V$	1092	1125	1380	Lines	-
Horizontal Display Term	Active Display Period	$T_{HD}$	-	1920	-	Clocks	-
	Horizontal Total	$T_H$	2090	2200	2350	clocks	-

Note) This product is DE mode. But the Hsync & Vsync signal must be inputted

- (1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system
- (2) Internal VDD = 3.3V
- (3) Spread spectrum
  - Modulation rate (max) :  $\pm 1.5\%$
  - Modulation Frequency : 30~ 100KHz

### 5.2 LVDS Input Data Characteristics

ITEM		SYMBOL	Min.	Typ.	Max.	UNIT	NOTE
Input Data Position	$F_{IN}=75\text{MHz}$	$t_{RSRM}$	-	-	500	ps	
		$t_{RSLM}$	-500	-	-	ps	
Input common mode voltage		$V_{CM}$	0.55	-	1.8	V	-
Differential Input Voltage		$ V_{ID} $	100	-	-	mV	-

Note) When the skew is measured the Spread Spectrum should be 0%

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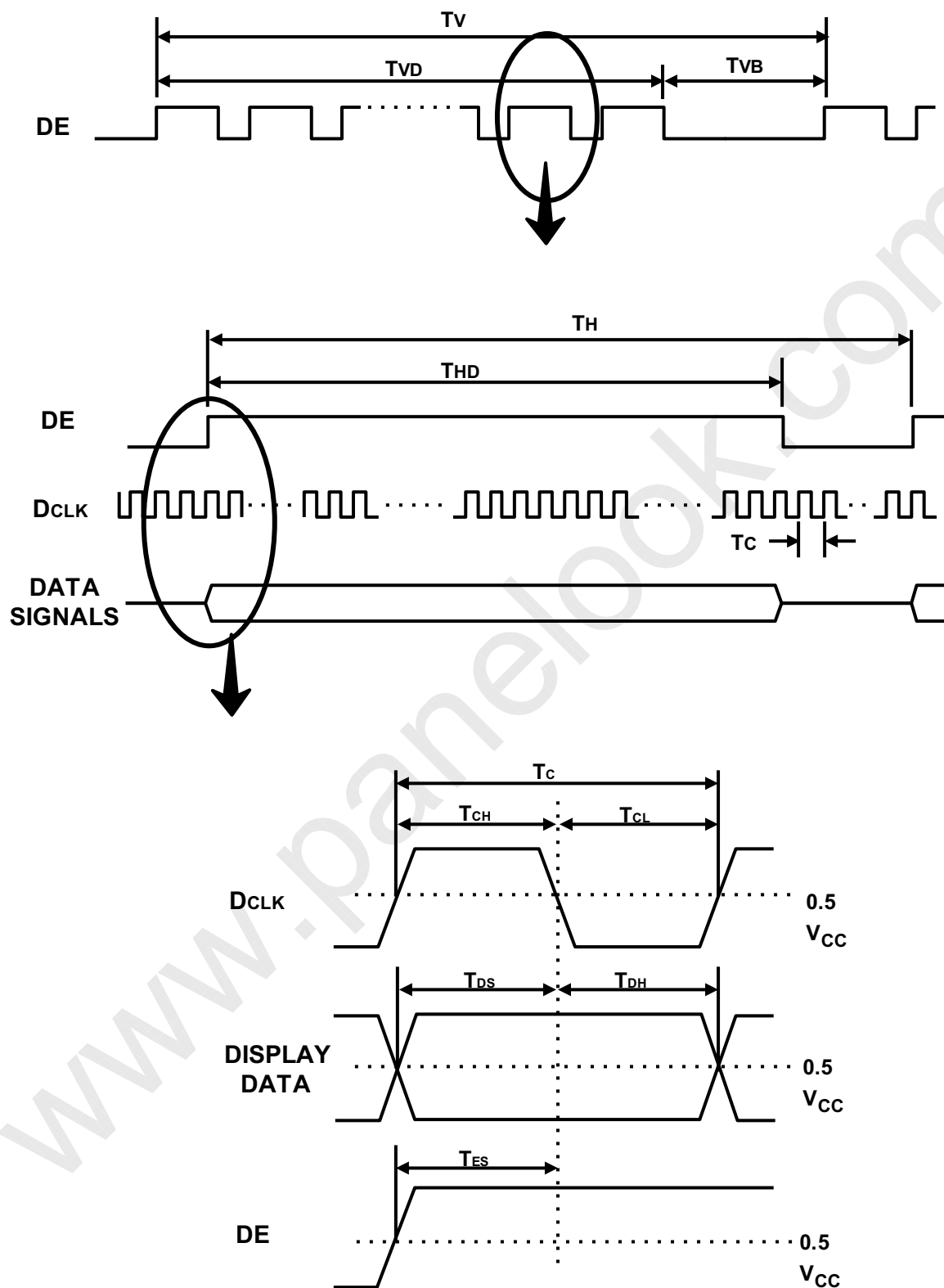
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## 5.3 Timing diagrams of interface signal ( DE only mode )

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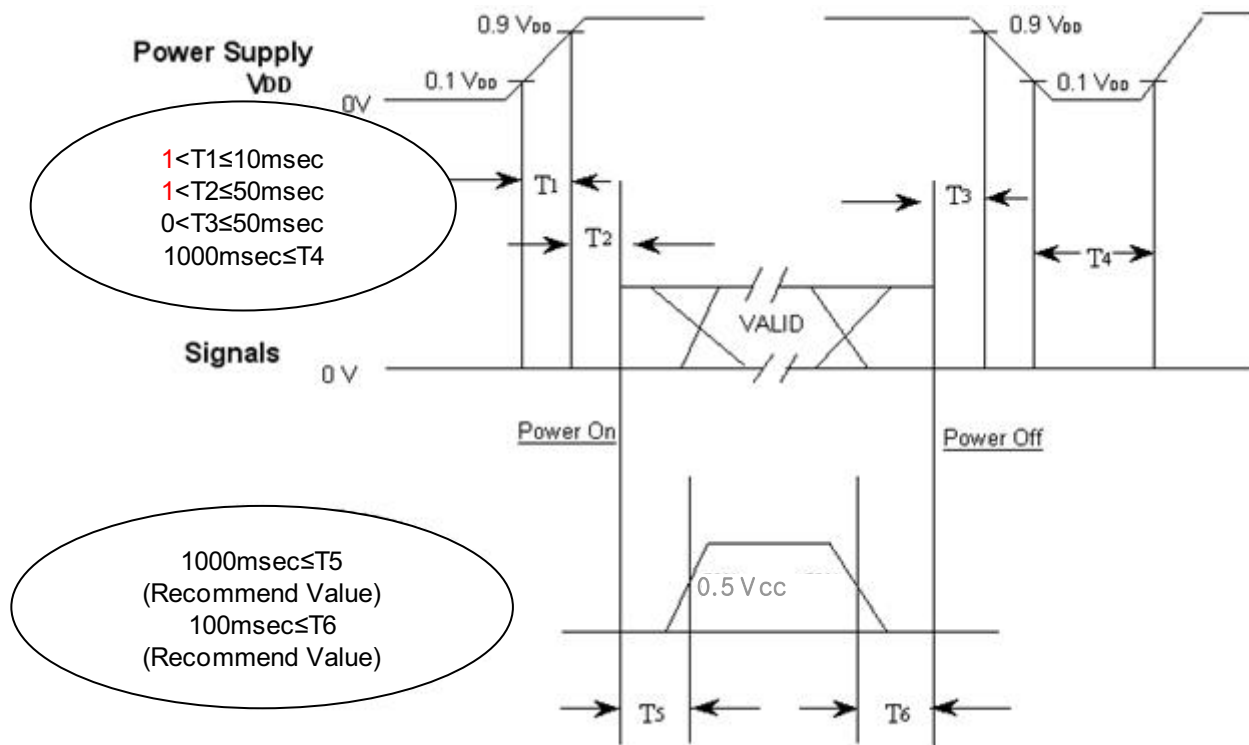
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## 5.4 Power ON/OFF Sequence

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To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



$T1$  :  $V_{DD}$  rising time from 10% to 90%

$T2$  : The time from  $V_{DD}$  to valid data at power ON.

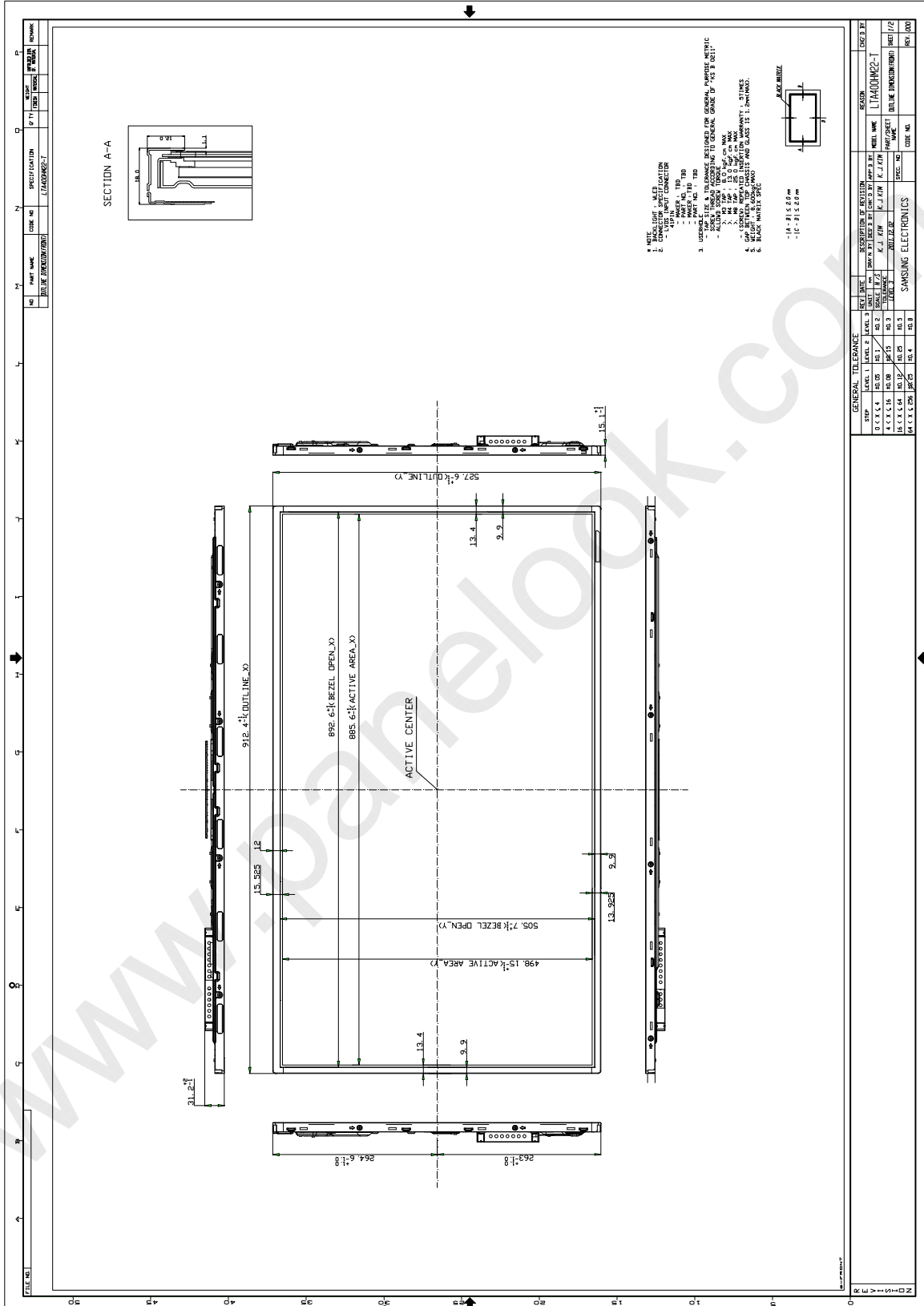
$T3$  : The time from valid data off to  $V_{DD}$  off at power Off.

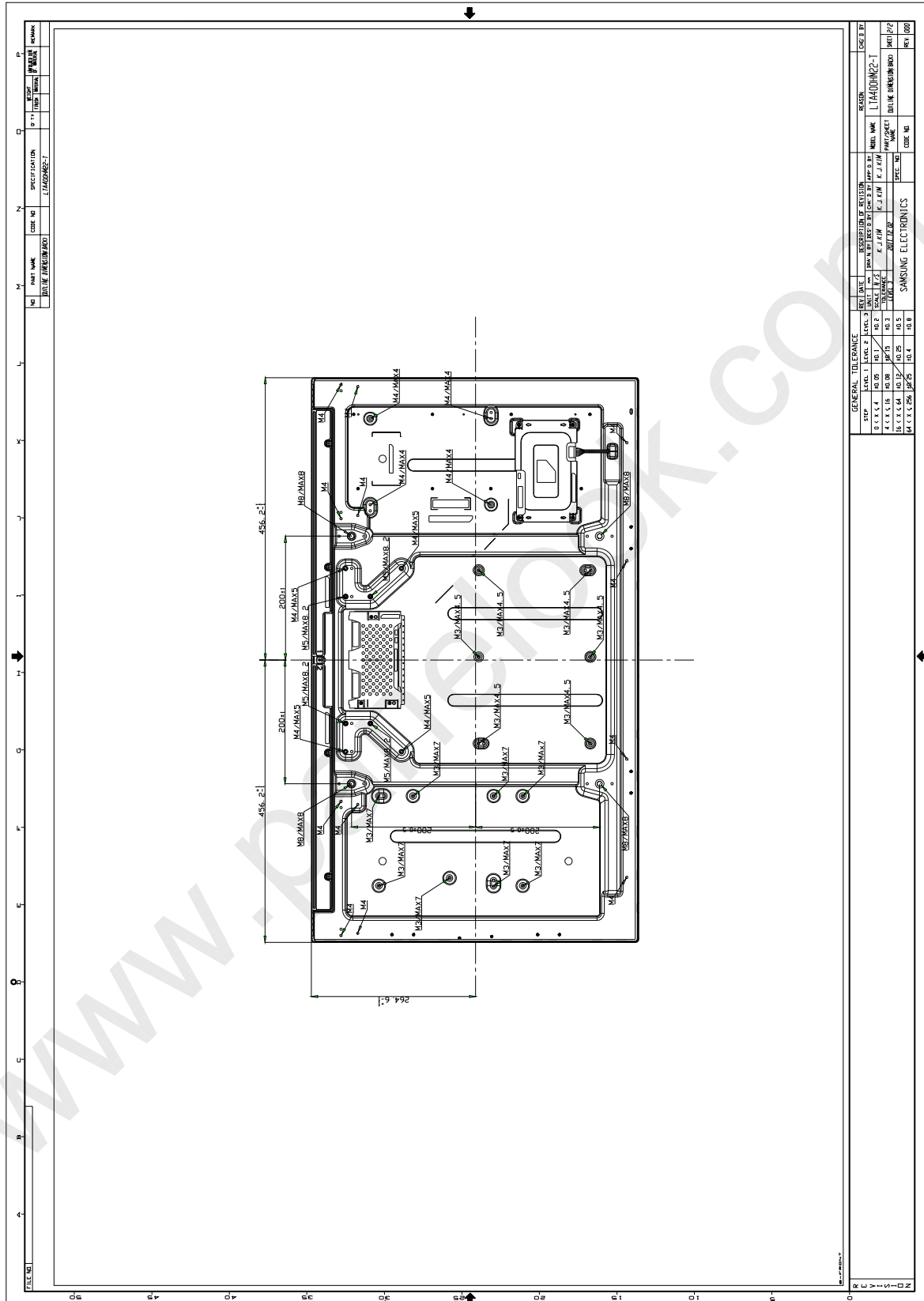
$T4$  :  $V_{DD}$  off time for Windows restart

$T5$  : The time from valid data to B/L enable at power ON.

$T6$  : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of  $V_{DD}$ .
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of  $V_{DD} = \text{off level}$ , please keep the level of input signals low or keep a high impedance.
- $T4$  should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.
- In Case  $T5$  is less than 1000msec and  $T6$  is less than 100msec, Garbage Display can be seen. (It is not related to electrical function issue, Just for recommendation to prevent Garbage Display )

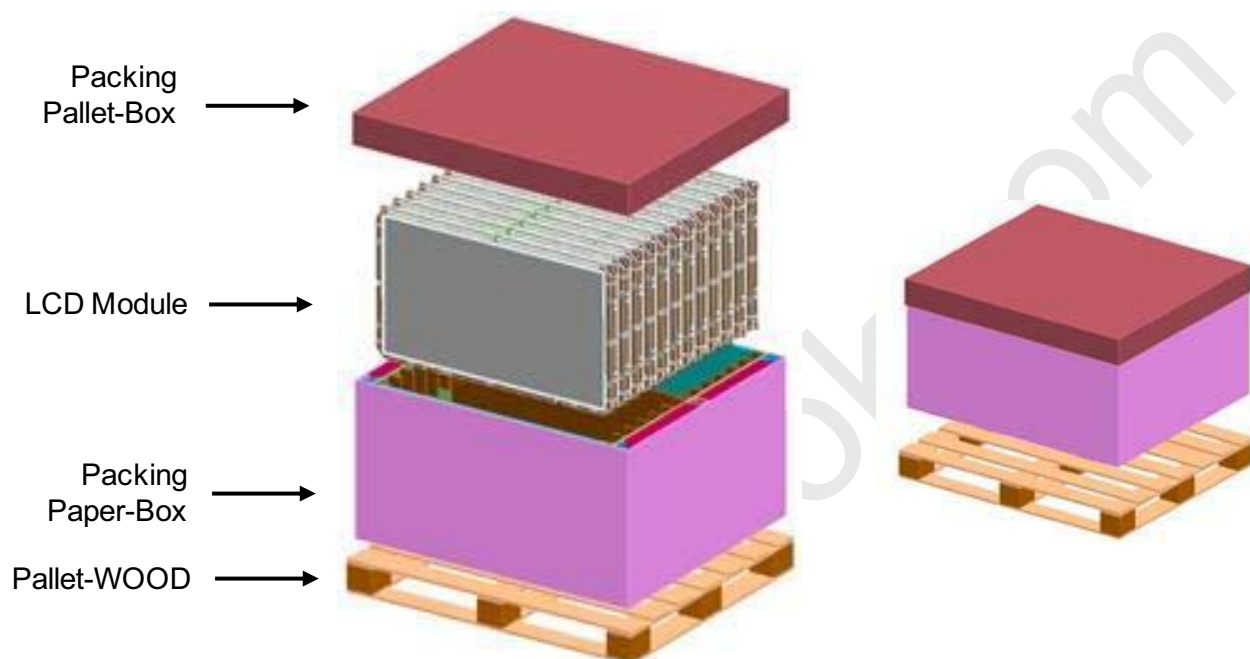




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## 7. PACKING

### 7.1 CARTON (Internal Package)



### 7.2 Packing Specification

Item	Specification	Remark
LCD Packing Pallet-Box	21ea / Box (Packing-Pallet Box)	1. 8.7Kg / LCD (21ea) 2. 14Kg / Packing Set 3. Packing Material : Paper
Pallet-Wood	1box / pallet	1. Pallet weight = 14.5kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1150mm(H) x 985mm(V) x 711mm(height)
Total Pallet Weight	212.04kg	Module(8.7kgx21=182.7kg) + Pallet(14.5kg)+ Packing SET(14kg) + Desiccant(0.04kgx21=0.84kg)

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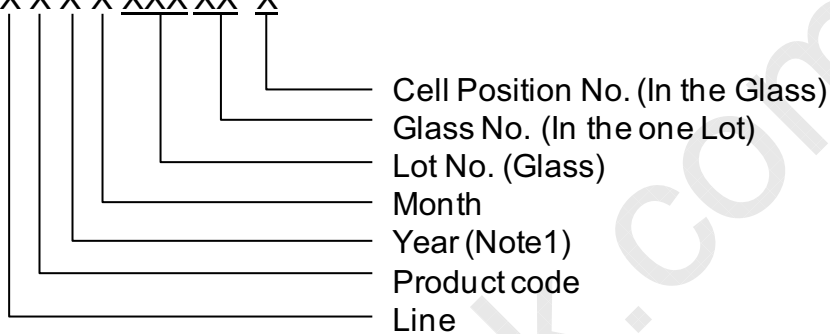
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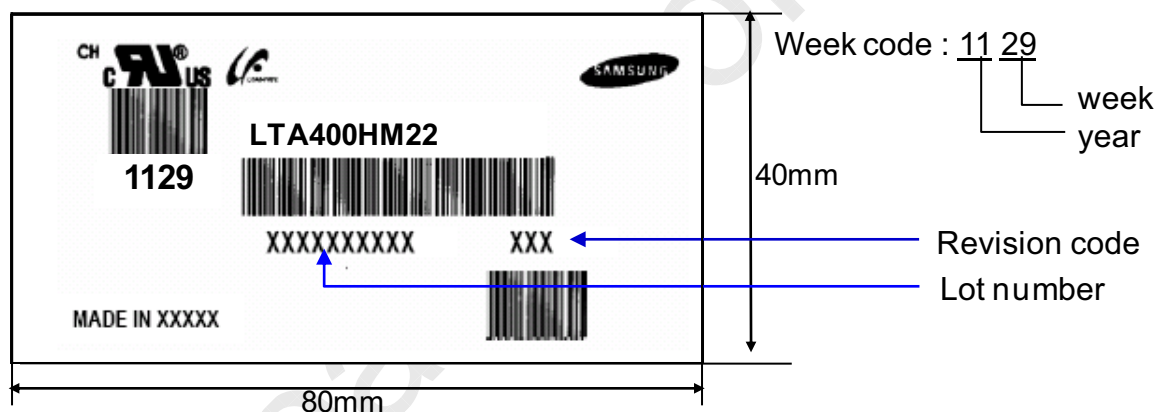
### 8. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

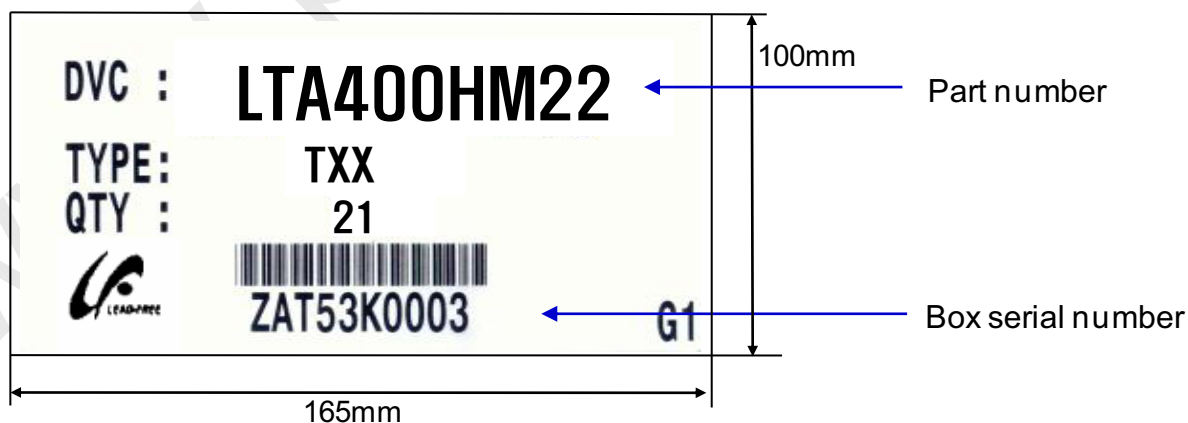
- (1) Part number : LTA400HM22
- (2) Revision: Three letters
- (3) Lot number : X X X X XXX XX X



#### (4) Nameplate Indication



#### (5) Packing box attach



#### (6) Others

- 1. After service part  
 Lamps cannot be replaced because of the narrow bezel structure.

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## 9. General Precautions

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### 9.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module.  
In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.
- (d) Note that polarizers are very fragile and could be damage easily.  
Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane.  
Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or semiconductor would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (l) Do not disassemble shield case of inverter & LVDS board
- (m) Do not connect N.C pins. (Samsung internal use only)
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handling a module
- (o) Pins of I/F connector should not be touched directly with bare hands.

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## 9.2 Storage

Item	Unit	Min.	Max.
Storage Temperature	(°C)	5	40
Storage Humidity	(%rH)	35	75
Storage Life	12 Months		
Storage Condition	<ul style="list-style-type: none"> <li>- The storage room should provide good ventilation and temperature control.</li> <li>- Products should not be placed on the floor, but on the Pallet away from a wall.</li> <li>- Prevent products from direct sunlight, moisture nor water; Be cautious of a build up of condensation.</li> <li>- Avoid other hazardous environment while storing goods.</li> <li>- If products delivered or kept in conditions of over the storage period of 3 months, the recommended temperature or humidity range, we recommend you leave them at a temperature of 20 °C and a humidity of 50% for 24 hours.</li> </ul>		

## 9.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(LED) and may require higher startup voltage(Vs).



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## 9.4 Operation Condition Guide

(a) The LCD product should be operated under normal conditions.

Normal condition is defined as below;

- Temperature :  $20 \pm 15^{\circ}\text{C}$
- Humidity :  $55 \pm 20\%$
- Display pattern : continually changing pattern (Not stationary)

(b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

## 9.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. ( supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)  
Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.  
To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.

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