Laurel Electronics Co., Ltd.

SPECIFICATION FOR TFT LCD MODULE

MODEL NO.: LT043C-50A

4.3", 480(RGB) x 272 TFT LCM WITH MCU INTERFACE

| REVISION | PREPARED | CHECKED | APPROVED | | |
|----------|----------|---------|----------|--|--|
| 0.2 | Y.D.Y. | L.Y.J. | L.Y. | | |

RECORD OF REVISION

| Date | Revision | Page | Revision Items |
|------------|----------|-------|---|
| 2012-08-28 | 0.1 | - | New release Change serial font ROM from GT23L24T3Y to GT30L24T3Y. |
| 2014-09-23 | 0.2 | 5 | |
| 자세 | 한 내 | 용은 | (주)가나시이스, |
| TEL: | 02-2 | 681-5 | 611로 주시기 바랍니다. |

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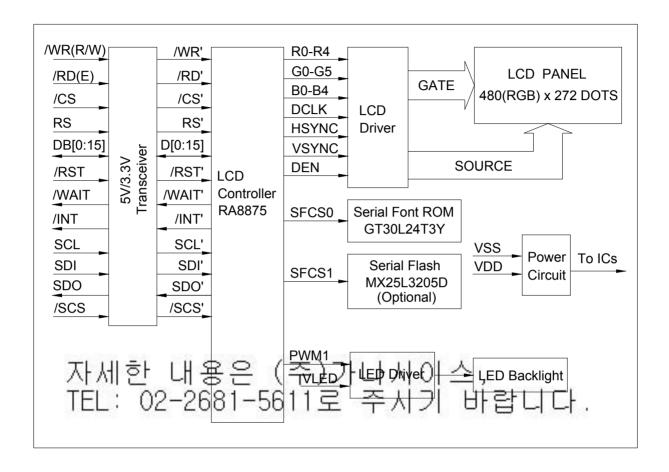
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1. General Description

| Item | Specifications | Unit |
|---------------------------|----------------------------------|-----------------|
| LCD Type | Digital TFT | - |
| Display Color | 256/65K | - |
| Active Area (WxH) | 95.04 x 53.86 | mm |
| Screen Size | 4.3(Diagonal) | inch |
| Number of Dots | 480(RGB) x 272 | dot |
| Color Configuration | RGB-Stripe | - |
| Dot Pitch (WxH) | 0.066 x 0.198 | mm |
| Outline Dimension (WxHxT) | 117.0 x 80.2 x 9.5 | mm |
| Polarizer Mode | Transmissive | - |
| Viewing Direction | 6:00 | O'clock |
| LCD Controller | RA8875 | - |
| Serial Forthook 내 중 는 | | |
| VDD TEL: 02-2681 | -56 11도 3.g t/g.e기 바랍니 | Lt _V |
| Interface | 8/16-bit 8080/6800 or 4-wire SPI | - |
| Backlight | White LED | - |
| Touch Panel | None | - |
| Operating Temperature | -20 to +70 | °C |
| Storage Temperature | -30 to +80 | °C |
| Weight | 86 | g |

Note: Please refer to RA8875 datasheet and GT30L24T3Y datasheet for details.

2. Block Diagram



3. Interface Specifications

3.1 Set Bus Mode by on Board Jumpers

The interface bus mode is determined by the status of on board jumpers. The relationship between the status of jumpers and interface bus mode is below.

| | Jun | nper St | Interface Bus Mode | | | | | | | |
|----|-----|---------|--------------------|----|----|----|----|-----|-----|--|
| J2 | J3 | J4 | J5 | J6 | J7 | J8 | J9 | J10 | J11 | interface bus wode |
| 0 | С | 0 | С | С | 0 | 0 | С | 0 | С | 8080 8/16-bit <default></default> |
| 0 | С | C | 0 | 0 | C | 0 | C | 0 | С | 6800 8/16-bit |
| С | 0 | 0 | С | С | 0 | 0 | С | С | 0 | 4-wire SPI |

Note: This Icm do not support 3-wire SPI or IIC mode.

3.2 Terminals Functions (CN1/CN2)

| Pin No. | Symbol | Level | Function |
|---------|---------|----------------------|--|
| 1 | Vss | 0V | Ground (Connects to frame ground via jumper J1) |
| 2 | VDD | 3.3V to 5V | Power supply for logic |
| 3 | VLED | 3.3V to 5V | Power supply for LED backlight drivers. |
| 4 | DO | 110 | Data or command selection |
| 4 | RS | H/L | RS="H": Command; RS="L": Display data |
| | | | /WR(Write) signal for 8080 series MPU: |
| 5 | /WR | H/L | Bus data is latched at the rising edge of /WR. |
| | (/RW) | 1 1/2 | R/W(Read/Write control) signal for 6800 series MPU: |
| | | | R/W="H": Read operation; R/W="L": Write operation. /RD (Read) signal for 8080 series MPU: |
| | /RD | | Read bus data when /RD is "L". |
| 6 | | H/L | E (Enable) signal for 6800 series MPU: |
| | (E) | | Read bus data when E is "H", write bus data at falling edge of E. |
| 7 | /CS | L | Chip selection signal. Active "L". |
| 8 | /RST | L | Reset signal. Active "L" |
| | | | Wait signal output. Active "L". This is a /WAIT output to indicate |
| 9 | /WAIT | L | RA8875 is in busy state. Keep /WAIT open when it is not used. |
| 10 万 | 同奏さ | 내용 | Interrupt signal output. Active "L". The interrupt output for MCU to indicate the status of RA8875. Keep /INT open when it is not used. |
| 11 T | ELDBO (|)2 _{F/} 268 | |
| 12 | DB1 | H/L | The state of the s |
| 13 | DB2 | H/L | |
| 14 | DB3 | H/L | |
| 15 | DB4 | H/L | |
| 16 | DB5 | H/L | |
| 17 | DB6 | H/L | Data bus. |
| 18 | DB7 | H/L | For 8-bit data bus mode, keep DB8 to DB15 open. (D8 to D15 inputs to RA8875 are pulled to HIGH by on board |
| 19 | DB8 | H/L | (D8 to D15 inputs to RA8875 are pulled to High by on board 100 KΩ resistors). |
| 20 | DB9 | H/L | 1001 (12 1001010). |
| 21 | DB10 | H/L | |
| 22 | DB11 | H/L | |
| 23 | DB12 | H/L | |
| 24 | DB13 | H/L | |
| 25 | DB14 | H/L | |
| 26 | DB15 | H/L | |

3.3 Terminals Functions (CN3/CN4)

| Pin No. | Symbol | Level | Function |
|---------|--------|------------|---|
| 1 | Vss | 0V | Ground (Connects to frame ground via jumper J1) |
| 2 | VDD | 3.3V to 5V | Power supply for logic |
| 3 | VLED | 3.3V to 5V | Power supply for LED backlight drivers. |
| 4 | SCL | H/L | SPI clock. Active at rising edge. |
| 5 | SDI | H/L | SPI data input. |
| 6 | SDO | H/L | SPI data output. |
| 7 | /SCS | L | SPI chip selection. Active "L". |
| 8 | /WAIT | L | Wait signal output. Active "L". This is a /WAIT output to indicate RA8875 is in busy state. Keep /WAIT open when it is not used. |
| 9 | /INT | L | Interrupt signal output. Active "L". The interrupt output for MCU to indicate the status of RA8875. Keep /INT open when it is not used. |
| 10 | /RST | L | Reset signal. Active "L" |

Note: SCL, SDI, /SCS terminals are pulled to "H" by on board resistors.

4. Absolute Maximum Ratings

| Item | Symbol | Condition | _ Min | Max | Unit | Remark |
|--------------------|--------|----------------|---------|--------------------------|------|---------------------|
| Input Power Supply | 내사용는 | · (个) | J-b.8-t | ∤ 5(3) <u> </u> | _ ,> | For Logic |
| Input Power Supply | -2681 | -56 <u>113</u> | RF-0.3 | N _{6:} | 바라 | For LED B/L drivers |
| Input Voltage | VI | - | -0.3 | VDD+0.3 | V | |

5. Electrical Characteristics

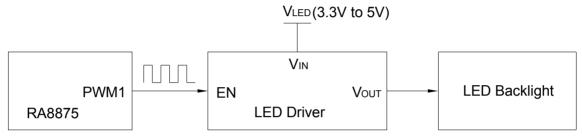
5.1 DC Characteristics (Ta=25°C)

| Item | Symbol | Condition | Min | Тур | Max | Unit |
|---------------------|--------|------------|---------|-----|-----|----------|
| Power Supply | VDD | | 3.3 | 5.0 | 5.5 | V |
| Input High Voltage | VIH | | 2.0 | - | VDD | V |
| Input Low Voltage | VIL | | 0 | 1 | 0.8 | V |
| Output High Voltage | Voн | IOH=-0.1mA | VDD-0.2 | 1 | VDD | V |
| Output Low Voltage | Vol | IOL= 0.1mA | 0 | - | 0.2 | V |
| Current Consumption | IDD | VDD=5.0V | - | 65 | 80 | mA |
| (Logic) | IDD | VDD=3.3V | - | 60 | 75 | mA |
| Current Consumption | li ED | VLED=5.0V | - | 80 | 100 | mA |
| (LED B/L Drivers) | ILED | VLED=3.3V | - | 130 | 150 | mA |

5.2 LED Backlight Control

The LED backlight is driven by 1 pcs LED driver IC. The enable signal of LED drivers is connected with PWM1 of RA8875. The LED driver is turned on (when PWM1=1) or off (when PWM1=0) by PWM1 signal. The LED backlight operates at either zero or full current. The average LED backlight current increases proportionally with the duty cycle of the PWM1 signal. A 0% duty cycle will turn off the LED driver and corresponds to zero LED current. A 100% duty cycle corresponds to full current. The typical frequency range of the PWM1 signal can be 1 KHz or less.

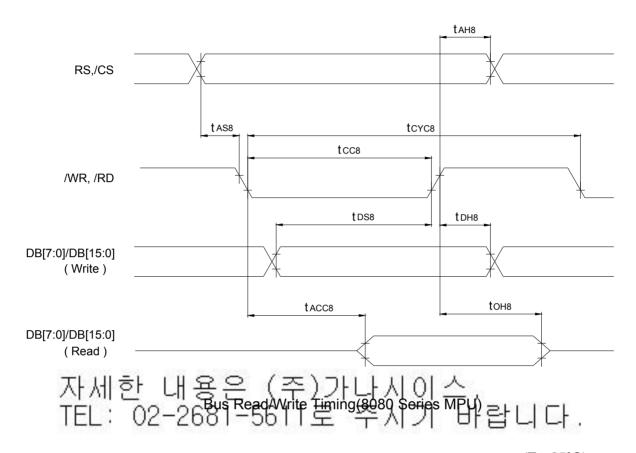
(External Crystal=24MHz)



PWM1=1: LED B/L On VLED : Power supply for LED drivers

PWM1=0: LED B/L Off

5.3 8080 8/16-bit Interface Read/Write Timing



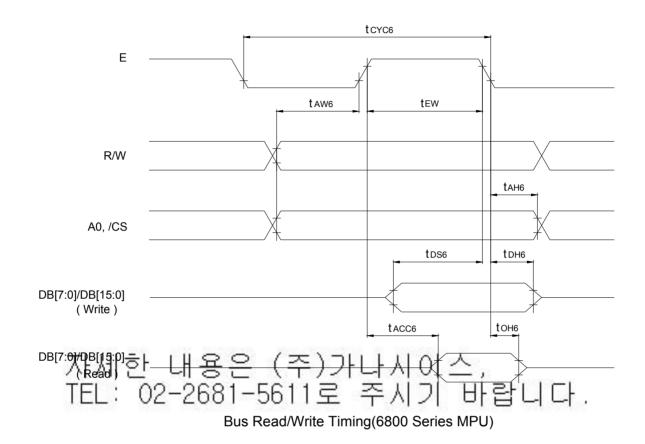
(Ta=25°C)

| Characteristic | Symbol | Min. | Max. | Unit |
|-------------------------|------------------|------|------|------|
| Cycle time | tcyc8 | 50 | - | ns |
| Strobe Pulse width | tcc8 | 20 | - | ns |
| Address setup time | tas8 | 0 | - | ns |
| Address hold time | t _{AH8} | 10 | - | ns |
| Data setup time | tos8 | 20 | - | ns |
| Data hold time | t _{DH8} | 10 | - | ns |
| Data output access time | tACC8 | 0 | 20 | ns |
| Data output hold time | t он8 | 0 | 20 | ns |

Notes:

- 1. tc=1/SYS_CLK
- 2. External Crystal=24MH

5.4 6800 8/16-bit Interface Read/Write Timing



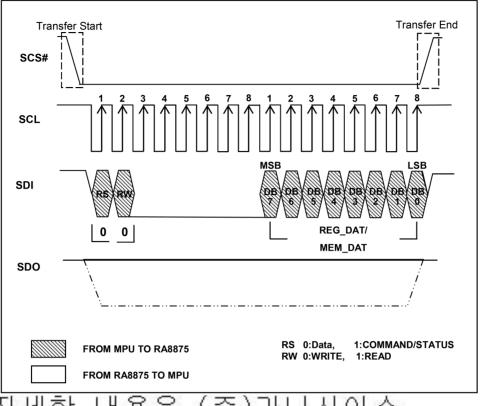
(Ta=25°C)

| | | | (= 0 0) |
|------------------|------------------------------------|--|---|
| Symbol | Min. | Max. | Unit |
| tcYC6 | 50 | - | ns |
| tew | 20 | - | ns |
| t _{AW6} | 0 | - | ns |
| t _{AH6} | 10 | - | ns |
| tDS6 | 20 | - | ns |
| tDH6 | 10 | - | ns |
| tACC6 | 0 | 20 | ns |
| toн6 | 0 | 20 | ns |
| | tcyc6 tew taw6 tah6 tds6 tdh6 tdc6 | tcyce 50 tew 20 tawe 0 tahe 10 tbse 20 tbhe 10 tacce 0 | tcyc6 50 tew 20 tawe 0 tahe 10 tDS6 20 tDH6 10 tacce 0 20 |

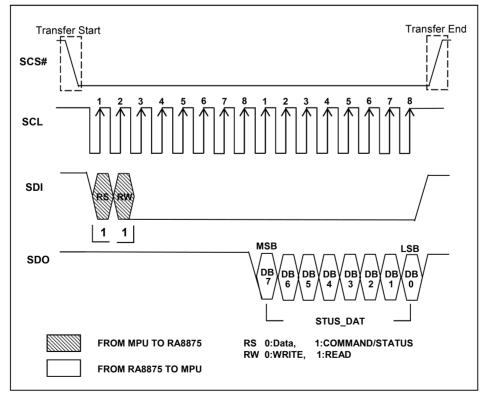
Notes:

- 1. tc=1/SYS_CLK
- 2. External Crystal=24MH

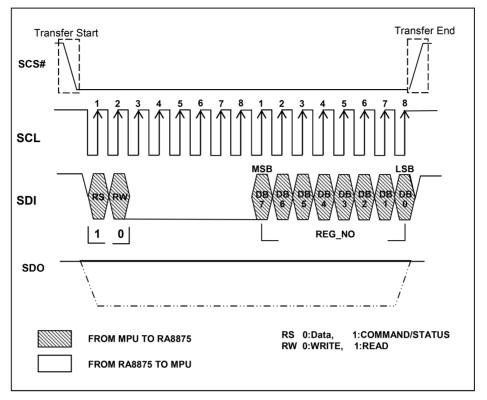
5.5 4-Wire SPI Timing Diagram



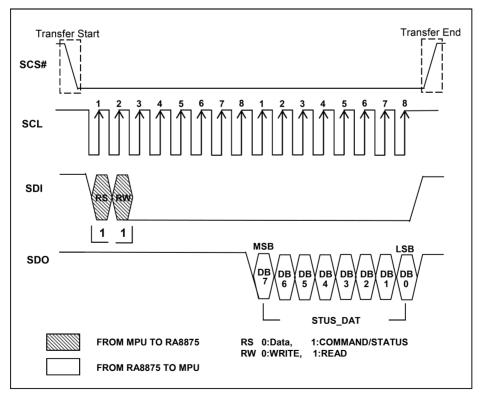
자세한 내용**윤·(주·)과많시이스, TEL: 02-2681-5611로 주시기 바랍니다.



Data Read on 4-Wire SPI Bus



Command Write on 4-Wire SPI Bus



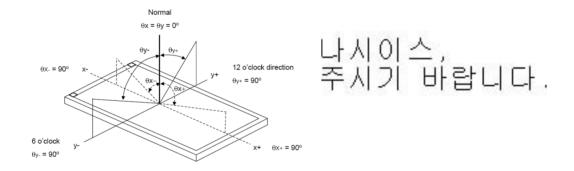
Status Read on 4-Wire SPI Bus

6. Optical Specifications

| Item | Symbol | | Condition | Min. | Тур. | Max. | Unit | Note |
|----------------|----------------|-----|-----------|-------|-------|-------|-------------------|------|
| Brightness | - | | - | - | 100 | - | cd/m ² | 1 |
| | Hor. | Θх+ | | - | 45 | - | deg. | 2 |
| Viowing Anglo | пог. | Θх- | Center, | - | 45 | - | | |
| Viewing Angle | Ver. | Θу+ | Cr≥10 | - | 15 | - | | |
| | | Θу- | | - | 35 | - | | |
| Contrast Ratio | С | r | | 150 | 250 | - | - | 3 |
| Doopongo Timo | Tr | | Өх=Өу=0 | - | 15 | 30 | ms | 4 |
| Response Time | T _f | | | - | 35 | 50 | ms | 4 |
| Chromaticity | White | Х | Өх=Өу=0 | 0.282 | 0.312 | 0.342 | - | _ |
| | | у | | 0.319 | 0.349 | 0.379 | - | |

Note 1: Measured at the center area of the panel when the LCD on the "White" state.

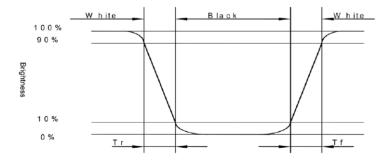
Note 2: The above "Viewing Angle" is the measuring position with largest contrast ratio, not for good image quality. Viewing direction for good image quality is 12 O'clock.



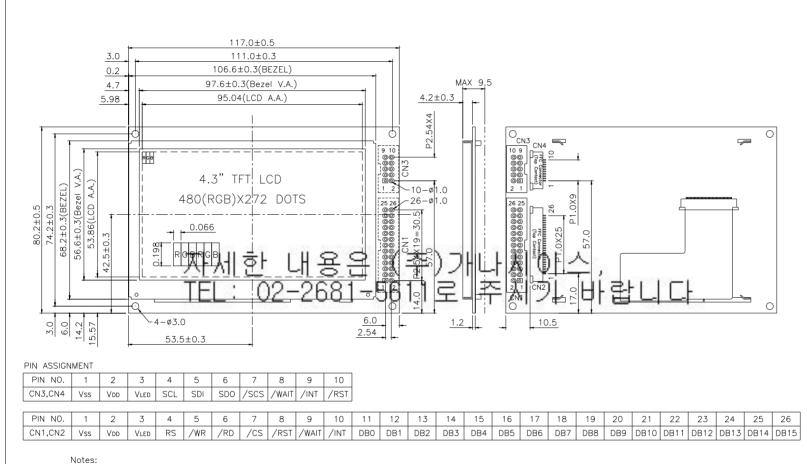
Note 3: The contrast ration is defined as the following expression.

 $Contrast\ Ratio\ (Cr) = \frac{Brightness\ measured\ when\ LCD\ on\ "White"\ State}{Brightness\ measured\ when\ LCD\ on\ "Black"\ state}$

Note 4: The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time Tr is the time between photo detector output intensity changed from 90% to 10%. Fall time Tf is the time between photo detector output intensity changed from 10% to 90%.



7. Outline Dimensions



- 1. 4.3" Transmissive, Normally-White TFT-LCD Module
- 2. Viewing Direction: 6:00 3. LCD Controller: RA8875
- 4. Built-in LED Driver IC
- 5. Operation Temp.: -20°C to +70°C 6. Storage Temp.: -30°C to +80°C

| DŴŊ. | LY | TITLE | LCM OUTLINE DIMENSION | | | | |
|-------|------------|----------|-----------------------|------------|--------|--|--|
| CHK. | LYJ | PART NO. | LTO43C | | | | |
| APPD. | | D₩G. NO. | LTO43C-WXA | | | | |
| REV. | Α | UNIT | mm | PROJECTION | ⊕-□ | | |
| DATE | 2012.08.16 | SCALE | NTS | SHEET | 1 OF 1 | | |

8. Precautions for Use of LCD Module

8.1 Handing Precautions

- 1) The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.
- 2) If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth. If the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.
- 3) Do not apply excessive force on the surface of display or the adjoining areas of LCD module since this may cause the color tone to vary.
- 4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
- 5) If the display surface of LCD module becomes contaminated, blow on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents.
- · Isopropyl alcohol
- · Ethyl alcohol Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

- Water
- Ketone

| Aromatic Solvents | 내용은 | (주) | 가나시 | 이스. | |
|-------------------|-----|---------|-----|------------------------------|--|
| A 31 A 11 C | | 1 / / - | | \sim 1 \longrightarrow 1 | |

- 6) When mounting the LGD module make sure that it is free of twisting, warping, and distortion. Distortion has great influence upon display quality. Also keep the stiffness enough regarding the outer case.
- 7) Be sure to avoid any solvent such as flux for soldering never stick to Heat-Seal. Such solvent on Heat-Seal may cause connection problem of heat-Seal and TAB.
- 8) Do not forcibly pull or bend the TAB I/O terminals.
- 9) Do not attempt to disassemble or process the LCD module.
- 10) NC terminal should be open. Do not connect anything.
- 11) If the logic circuit power is off, do not apply the input signals.
- 12) To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
- · Be sure to ground the body when handling the LCD module.
- · Tools required for assembly, such as soldering irons, must be properly grounded.
- · To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.
- The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

8.2 Storage Precautions

- 1) When storing the LCD module, avoid exposure to direct sunlight or to the light of fluorescent lamps and high temperature/high humidity. Whenever possible, the LCD module should be stored in the same conditions in which they were shipped from our company.
- 2) Exercise care to minimize corrosion of the electrodes. Corrosion of the electrodes is accelerated by water droplets or a current flow in a high humidity environment.

8.3 Design Precautions

- 1) The absolute maximum ratings represent the rated value beyond which LCD module can not exceed. When the LCD modules are used in excess of this rated value, their operating characteristics may be adversely affected.
- 2) To prevent the occurrence of erroneous operation caused by noise, attention must be paid to satisfy VIL, VIH specification values, including taking the precaution of using signal cables that are short.
- 3) The liquid crystal display exhibits temperature dependency characteristics. Since recognition of the display becomes difficult when the LCD is used outside its designated operating temperature range, be sure to use the LCD within this range. Also, keep in mind that the LCD driving voltage levels necessary for clear displays will vary according to temperature.
- 4) Sufficiently notice the mutual noise interference occurred by peripheral devices.
- 5) To cope with EMI, take measures basically on outputting side.
- 6) If DC is impressed by the figure crystal display panel, display definition is rapidly deteriorated by the electrochemical reaction that occurs inside the liquid crystal display panel. To eliminate the opportunity of DC impressing, be sure to maintain the AC characteristics of the input signals sent to the LCD Module.

8.4 Others

- 1) Liquid crystals solidify under low temperatures (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the LCD module is subjected to a strong shock at a low temperature.
- 2) If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and aslight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- 3) To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity, etc., exercise care to avoid touching the following sections when handling the module:
- · Terminal electrode sections.
- Part of pattern wiring on TAB.