





Linear Systems replaces discontinued Siliconix PAD20

LSPAD20 is a low leakage Pico-Amp Diode packaged in hermetic TO-72

The LSPAD20 extremely low-leakage diode provides a superior alternative to conventional diode technology when reverse current (leakage) must be minimized. The LSPAD20 features a leakage current of -20 pA and is well suited for use in applications such as input protection for operational amplifiers.

LSPAD20 Benefits:

- Negligible Circuit Leakage Contribution
- Circuit "Transparent" Except to Shunt High-Frequency Spikes
- Simplicity of Operation

LSPAD20 Applications:

- Op Amp Input Protection
- Multiplexer Overvoltage Protection

FEATURES					
DIRECT REPLACEMENT FOR SILICONIX PAD20					
REVERSE BREAKDOWN VOLTAGE BV _R ≥ -45V					
ULTRALOW LEAKAGE	≤ 20 pA				
REVERSE CAPACITANCE	C _{rss} ≤ 2.0pF				
ABSOLUTE MAXIMUM RATINGS	×				
@ 25°C (unless otherwise noted)	<u> </u>				
Maximum Temperatures					
Storage Temperature	-65°C to +150°C				
Operating Junction Temperature	-55°C to +135°C				
Maximum Power Dissipation	7				
Continuous Power Dissipation	300mW				
MAXIMUM CURRENT					
Forward Current (Note 1)	50mA				

LSPAD20 ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	CHARACTERISTICS	MIN.	TYP.	MAX.	UNITS	CONDITIONS
BV _R	Reverse <mark>Br</mark> eakdown Voltage	-4 5-	-		V	
V _F	Forward Voltage		0.8	1.5	V	I _F = 5 mA
C _{rSS}	Total Reverse Capacitance		1.5	2	рF	$V_R = -5V$, $f = 1$ MHz
I _R	Maximum Reverse Leakage Current			-20	pA	V _R = - 20V

Notes:

1. Absolute maximum ratings are limiting values above which LSPAD20 serviceability may be impaired.

Available Packages:

LSPAD20 in TO-72 LSPAD20 available as bare die

Please contact Micross for full package and die dimensions



Micross Components Europe

Tel: +44 1603 788967

Email: chipcomponents@micross.com
Web: http://www.micross.com/distribution.com

1 3 3

C

C

TO-72 (Bottom View)

Information furnished by Linear Integrated Systems and Micross Components is believed to be accurate and reliable. However, no responsibility is assumed for its use; nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of Linear Integrated Systems.