

LS846 N-CHANNEL JFET



Linear Systems Low Leakage Low Noise JFET

The LS846 is a high-performance JFET featuring extremely low noise and low leakage and is targeted for use in a wide range of precision instrumentation applications.

The 6 Pin SOT-23 package provides ease of manufacturing, and a lower cost assembly option.

(See Packaging Information).

LS846 Applications:

- Wideband Differential Amps
- High-Speed,Temp-Compensated Single-Ended Input Amps
- High-Speed Comparators
- Impedance Converters and vibrations detectors.

FEATURES							
LOW LEAKAGE		I _G = 15pA TYP.					
LOW NOISE		$e_n = 3nV/VHz TYP.$					
ABSOLUTE MAXIMUM RATINGS @ 25°C (unless otherwise noted)							
Maximum Temperatures							
Storage Temperature			-65°C to +150°C				
Operating Junction Temperature			+135°C				
Maximum Voltage and Current- Note 1							
-V _{GSS}	Gate Voltage to Drain or So	Gate Voltage to Drain or Source					
-V _{GDS}	Gate Voltage to Drain or So	Gate Voltage to Drain or Source					
-V _{DSO}	Drain to Source Voltage	Drain to Source Voltage					
-I _{G(f)}	Gate Forward Current	50mA <					
Maximum Power Dissipation							
Device Dissipation @ Free Air – Total 350mW @ +125°C							

ELECTRICAL CHARACTERISTICS @ 25°C (unless otherwise noted)

SYMBOL	CHARACTERISTICS @ 25 C (unless o	MIN.				
		60			V V	
BV _{GSS}	Breakdown Voltage	60			V	$V_{DS} = 0$ $I_D = 1nA$
.,	TRANSCONDUCTANCE	4=00				
Y _{fSS}	Full Conduction	1500			μmho	V_{DG} = 15V V_{GS} = 0V f = 1kHz
Y_{fS}	Typical Operation	1000	1500		μmho	V_{DG} = 15V I_D = 500 μ A
	DRAIN CURRENT					
I _{DSS}	Full Conduction	1.5	5	15	mA	$V_{DG} = 15V$ $V_{GS} = 0V$
	GATE VOLTAGE					
V _{GS} (off) or V _p	Pinch <mark>of</mark> f vol <mark>ta</mark> ge	1		3.5	V	$V_{DS} = 15V$ $I_D = 1nA$
V _{GS} (on)	Operating Range	0.5		3.5	V	V _{DS} =15 V I _D =500μA
	GATE CURRENT					
-l _g max.	Operating		15	50	pA	$V_{DG} = 15VI_{D} = 500\mu A$
-l _G max.	High Temperature			50	nA	T _A = +125°C
-I _G max.	Reduced V _{DG}		5	30	pA	$V_{DG} = 3V I_{D} = 500 \mu A$
-I _{GSS} max.	At Full Conduction			100	рА	V_{DG} = 15V , V_{DS} =0
	OUTPUT CONDUCTANCE					
Y _{oss}	Full Conduction			20	μmho	V_{DG} = 15V V_{GS} = 0V
Y _{OS}	Operating		0.2	2	μmho	V _{DG} = 15V I _D = 500μA
	<u>NOISE</u>					V_{DS} = 15V V_{GS} = 0V R_{G} = 10M Ω
NF	Figure			0.5	dB	f= 100Hz NBW= 6Hz
e _n	Noise Voltage		3	7	nV/√Hz	V _{DS} =15V I _D =500μA f=1KHz NBW=1Hz
	CAPACITANCE					
C _{ISS}	Input			8		V _{DS} = 15V, I _D =500μA
C _{RSS}	Reverse Transfer			3	pF	
						V _{DG} = 15V, I _D =500μA

 $Note \ 1-These \ ratings \ are \ limiting \ values \ above \ which \ the \ service ability \ of \ any \ semiconductor \ may \ be \ impaired$

Available Packages:

LS846 / LS846 in SOT-23 LS846 / LS846 available as bare die

Please contact Micross for full package and die dimensions



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