SPECIFICATION FOR APPROVAL

() Preliminai	y Specification
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() Final Specification

BUYER	DELL
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LP154WP1
Suffix	TLA1

^{*}When you obtain standard approval, please use the above model name without suffix

SIGNATURE	DATE
/	
/	

Please return 1 copy for your confirmation with your signature and comments.

APPROVED BY	DATE		
J. H. Lee / S.Manager			
REVIEWED BY			
K.K.Jang / Manager			
PREPARED BY			
N.J. Seong / Engineer			
Product Engineering Dept. LG. Philips LCD Co., Ltd			



Contents

No	ITEM		
	COVER	1	
	CONTENTS	2	
	RECORD OF REVISIONS	3	
1	GENERAL DESCRIPTION	4	
2	ABSOLUTE MAXIMUM RATINGS	5	
3	ELECTRICAL SPECIFICATIONS		
3-1	ELECTRICAL CHARACTREISTICS	6	
3-2	INTERFACE CONNECTIONS	8	
3-3	SIGNAL TIMING SPECIFICATIONS	9	
3-4	SIGNAL TIMING WAVEFORMS	9	
3-5	COLOR INPUT DATA REFERNECE	10	
3-6	POWER SEQUENCE	11	
4	OPTICAL SFECIFICATIONS	12	
5	MECHANICAL CHARACTERISTICS	16	
6	RELIABLITY	20	
7	INTERNATIONAL STANDARDS		
7-1	SAFETY	21	
7-2	EMC	21	
8	PACKING		
8-1	DESIGNATION OF LOT MARK	22	
8-2	PACKING FORM	22	
9	PRECAUTIONS	23	



RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.0	May. 29. 2006	-	First Draft	V0.0

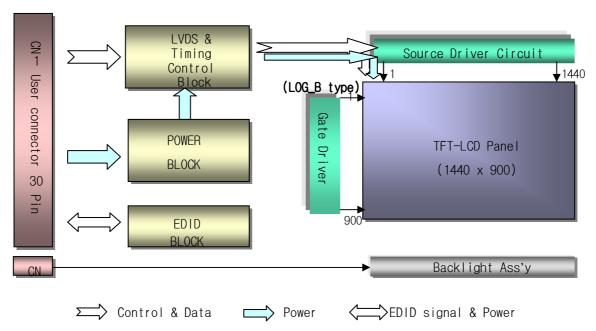


1. General Description

The LP154WP1 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp (CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has 15.4 inches diagonally measured active display area with WXGA resolution(1440 horizontal by 900 vertical pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP154WP1 has been designed to apply the interface method that enables low power, high speed, low EMI.

The LP154WP1 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP154W02 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	15.4 inches diagonal
Outline Dimension	344.0 (H) × 222.0 (V) × 6.4(D, max) mm
Pixel Pitch	0.2301 mm × 0.2301 mm
Pixel Format	1440 horiz. by 900 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	250 cd/m²(Typ.) , 5 point
Power Consumption	Total 5.77 Watt(Typ.) @ LCM circuit 1.35 Watt(Typ.), B/L input 4.42 Watt(Typ.)
Weight	505g (Typ.) w/o inverter & bracket
Display Operating Mode	Transmissive mode, normally white
Surface Treatment	Hard coating(2H) Glare treatment of the front polarizer

Ver. 0.0 May, 29, 2006 4 / 24



2. Absolute Maximum Ratings

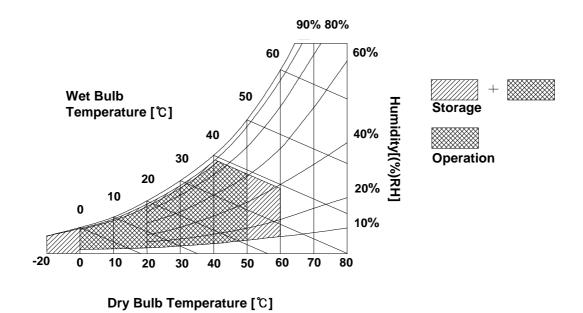
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
Farameter	Syllibol	Min	Max	Offics	Notes	
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Нѕт	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



Ver. 0.0 May, 29, 2006 5 / 24



3. Electrical Specifications

3-1. Electrical Characteristics

The LP154WP1 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Values Symbol Unit Parameter Notes Min Max Тур MODULE: Power Supply Input Voltage VCC 3.3 3.0 3.6 V_{DC} Power Supply Input Current 410 470 mΑ I_{CC} **Power Consumption** 1.35 Watt Рс 1.70 1 Differential Impedance 100 110 Ohm 2 Zm 90 LAMP : 680 895 665 Operating Voltage 3 V_{BL} V_{RMS} (7.0mA)(6.5mA)(2.0mA) 2.0 6.5 7.0 **Operating Current** $\mathsf{mA}_{\mathsf{RMS}}$ 4 I_{BL} 9 **Power Consumption** 4.42 P_{BL} 4.66 **Operating Frequency** 60 80 kHz 45 7 f_{BL} Discharge Stabilization Time 3 Min 5 Ts Life Time 15,000 Hrs 6 Established Starting Voltage 8 at 25 ℃ Vs 1170 V_{RMS} at 0 ℃ 1400 V_{RMS}

Table 2. ELECTRICAL CHARACTERISTICS

Note)

- 1. The specified current and power consumption are under the Vcc = 3.3V, $25^{\circ}C$, fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.
- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. The variance of the voltage is \pm 10%.
- 4. The typical operating current $\,$ is for the typical surface luminance (L_{WH}) in optical characteristics.
- 5. Define the brightness of the lamp after being lighted for 5 minutes as 100%, Ts is the time required for the brightness of the center of the lamp to be not less than 95%.
- 6. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.
- 7. The output of the inverter must have symmetrical (negative and positive) voltage waveform and symmetrical current waveform. (Asymmetrical ratio is less than 10%) Please do not use the inverter which has asymmetrical voltage and asymmetrical current and spike wave.

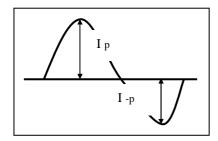
 Lamp frequency may produce interface with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore lamp frequency shall be as away possible from the horizontal synchronous frequency and from its harmonics in order to prevent interference.
- 8. The voltage above VS should be applied to the lamps for more than 1 second for start-up. Otherwise, the lamps may not be turned on. The used lamp current is the lamp typical current.
- 9. The lamp power consumption shown above does not include loss of external inverter. The applied lamp current is a typical one.

Ver. 0.0 May, 29, 2006 6 / 24



Note)

- Requirements for a system inverter design, which is intended to have a better display performance, a better power efficiency and a more reliable lamp, are following.
 It shall help increase the lamp lifetime and reduce leakage current.
 - a. The asymmetry rate of the inverter waveform should be less than 10%.
 - b. The distortion rate of the waveform should be within $\sqrt{2 \pm 10\%}$.
 - * Inverter output waveform had better be more similar to ideal sine wave.



Do not attach a conducting tape to lamp connecting wire.
If the lamp wire attach to a conducting tape, TFT-LCD Module has a low luminance and the inverter has abnormal action. Because leakage current is occurred between lamp wire and conducting tape.

Ver. 0.0 May, 29, 2006 7 / 24



3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.

The electronics interface connector is a model FI-XB30SRL-HF11 manufactured by JAE.

Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	GND	Ground	
2	VCC	Power Supply, 3.3V Typ.	
3	VCC	Power Supply, 3.3V Typ.	
4	V EEDID	DDC 3.3V power	1, Interface chips
5	BIST	Panel BIST control	1.1 LCD: DTML012(LCD Controller)
6	CIK EEDID	DDC Clock	including LVDS Receiver
7	DATA EEDID	DDC Data	1.2 System : THC63LVD823A or equivalent
8	0dd_R _{IN} 0-	Negative LVDS differential data input	* Pin to Pin compatible with THINE LVDS
9	0dd_R _{IN} 0+	Positive LVDS differential data input	
10	GND	Ground	2. Connector 2.1 LCD : FI-XB30SRL-HF11, JAE or
11	0dd_R _{IN} 1-	Negative LVDS differential data input	MDF76LARW-30S-1H, Hirose
12	0dd_R _{IN} 1+	Positive LVDS differential data input	equivalent. Locking design
13	GND	Ground	2.2 Mating : FI-X30M or equivalent. 2.3 Connector pin arrangement
14	0dd_R _{IN} 2-	Negative LVDS differential data input	2.3 Connector par arrangement
15	0dd_R _{IN} 2+	Positive LVDS differential data input	30 1
16	GND	Ground	<u> </u>
17	Odd_CLKIN-	Negative LVDS differential clock input	
18	0dd_CLK1N+	Positive LVDS differential clock input	
19	GND	Ground	[LCD Module Rear View]
20	Even_R _{IN} 0-	Negative LVDS differential data input	
21	Even_R _{IN} 0+	Positive LVDS differential data input	
22	GND	Ground	
23	Even_R _{IN} 1-	Negative LVDS differential data input	
24	Even_R _{IN} 1+	Positive LVDS differential data input	
25	GND	Ground	
26	Even_R _{IN} 2-	Negative LVDS differential data input	
27	Even_R _{IN} 2+	Positive LVDS differential data input	
28	GND	Ground	
29	Even_CLKIN-	Negative LVDS differential clock input	
30	Even_CLKIN+	Positive LVDS differential clock input	

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST or Compatible. The mating connector part number is SM02B-BHSS-1 or equivalent.

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION (J3)

			. ,
Pin Symbol Description		Notes	
1	HV	Power supply for lamp (High voltage side)	1
2	LV	Power supply for lamp (Low voltage side)	1

Notes: 1. The high voltage side terminal is colored pink and the low voltage side terminal is black.

		_ ,
Ver. 0.0	May 20, 2006	8 / 24
ver. 0.0	May, 29, 2006	0/27



3-3. Signal Timing Specifications

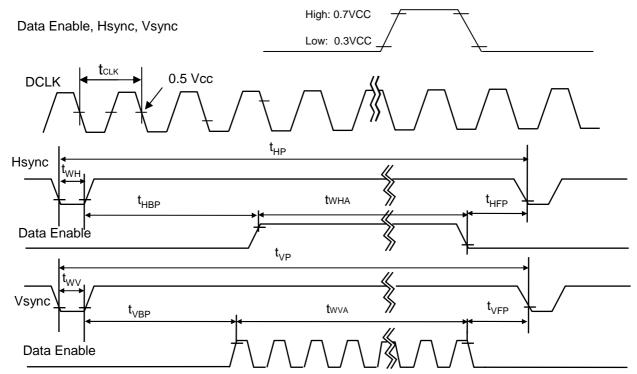
This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	fclk	83.00	88.75	95.00	MHz	
Hsync	Period	tHP	1520	1600	1680		
	Width	twn	24	32	40	tclk	
	Active		-	1440	-		
Vsync	Period	tvp	911	926	938		
	Width	tw∨	2	6	10	tHP	
	Active	twva	•	900	-		
Data	Horizontal back porch	tHBP	48	80	144	tour	
Enable	Horizontal front porch	tHFP	8	48	56	tCLK	
	Vertical back porch	tvbp	7	17	23	tup	
	Vertical front porch	tvfp	2	3	6	tHP	

3-4. Signal Timing Waveforms

Condition : VCC =3.3V



Ver. 0.0 May, 29, 2006 9 / 24



3-5. Color Input Data Reference

The brightness of each primary color (red,green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	D					GRE	EN					BL	UE		
`	30101	MSE	3				LSB	MSE	3				LSB	MSE	3				LSB
		R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	B 0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	.1	. 1	1		0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
RED		ļ			· · · · · ·						 						· · · · · ·		,
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN											 								
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE					• • • • • • •				• • • • •		 						 		• • • • •
	BLUE (62)	0	 0	0	0		0	 0	0	0	0	0		1	 1	1	1	1	0
	BLUE (63)	 0	 0					 0			ٽ	 0	0		 1	: 1	<u>:</u>	: 1	ĭ 1
	1 0 - (00)	<u> </u>						L											



3-6. Power Sequence

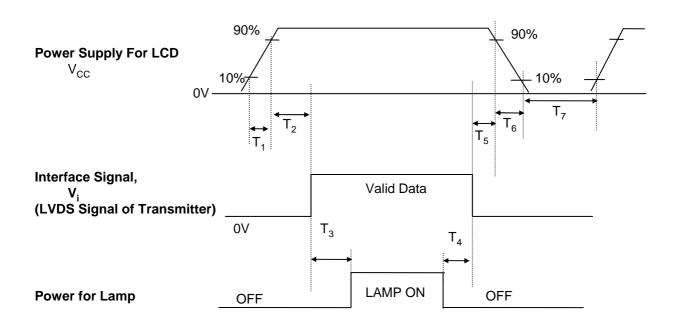


Table 8. POWER SEQUENCE TABLE

Parameter		Value		Units
	Min.	Тур.	Max.	
T ₁	-	•	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	•	1	(ms)
T ₄	200	•	-	(ms)
T ₅	0	•	50	(ms)
T ₆	0	-	10	(ms)
T ₇	400	-	-	(ms)

Note)

- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.



4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.



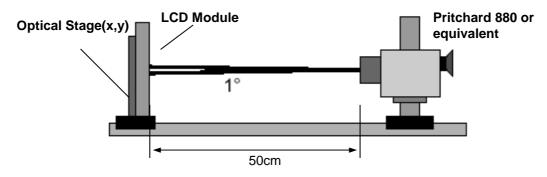


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, $f_{V}=60Hz$, $f_{CLK}=88.75MHz$, $f_{CLK}=6.5mA$

Davamatav	Curredo ed		Values	- , CLK	l linite	Notes
Parameter	Symbol	Min	Тур	MAx	Units	Notes
Contrast Ratio	CR	400				1
Surface Luminance, white	L _{WH}		250		cd/m ²	2
Luminance Variation	δ _{WHITE}	-	-	2.0		3
Response Time]					4
Rise Time+Decay Time	$Tr_{R +} Tr_{D}$	-	16	25	ms	
Color Coordinates]					±0.03
RED	RX		TBD			
	RY		TBD]	
GREEN	GX		TBD]	
	GY		TBD			
BLUE	BX		TBD			
	BY		TBD			
WHITE	WX	0.283	0.313	0.343		
	WY	0.299	0.329	0.359		
Viewing Angle]					5
x axis, right(Φ =0°)	Θr		65		degree	
x axis, left (Φ=180°)	ΘΙ		65	-	degree	
y axis, up (Φ =90 $^{\circ}$)	Θu		55	-	degree	
y axis, down (Φ =270 $^{\circ}$)	Θd		55		degree	
Gray Scale						6

Ver. 0.0 May, 29, 2006 12 / 24



Note)

Contrast Ratio(CR) is defined mathematically as
 Surface Luminance with all white pixels
 Contrast Ratio =

Surface Luminance with all black pixels

- 2. Surface luminance is the 5point (1~5)average across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 2. When I_{BL} = 6.3mA, L_{WH} =250cd/m²(min.)
- 3. Luminance % uniformity is measured for 13 point For more information see FIG 2. δ WHITE = Maximum(LN1,LN2, LN13) ÷ Minimum(LN1,LN2, LN13)
- 4. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

* f_{\/}=60Hz

Gray Level	Luminance [%] (Typ)
LO	TBD
L7	TBD
L15	TBD
L23	
L31	TBD
L39	TBD
L47	TBD
L55	TBD
L63	TBD



FIG. 2 Luminance

<measuring point for surface luminance & measuring point for luminance variation>

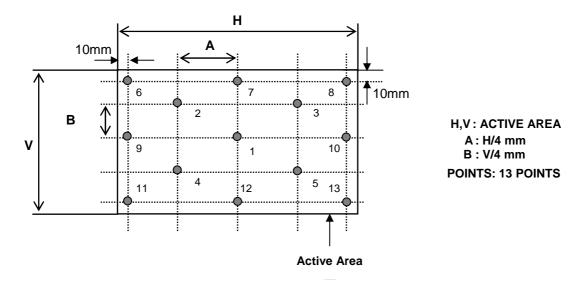
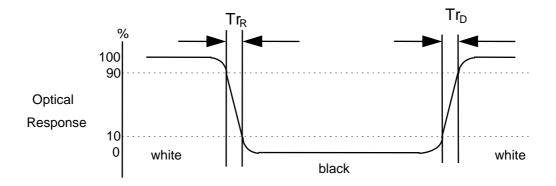


FIG. 3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

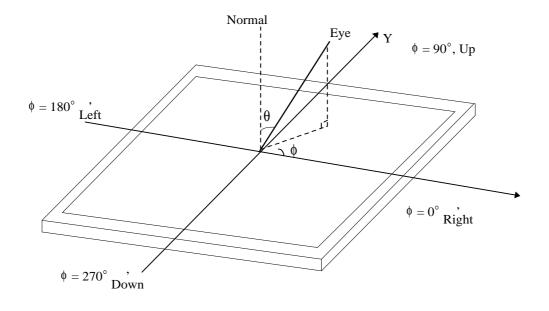


Ver. 0.0 May, 29, 2006 14 / 24



FIG. 4 Viewing angle

<Dimension of viewing angle range>





5. Mechanical Characteristics

The contents provide general mechanical characteristics for the model LP154WP1. In addition the figures in the next page are detailed mechanical drawing of the LCD.

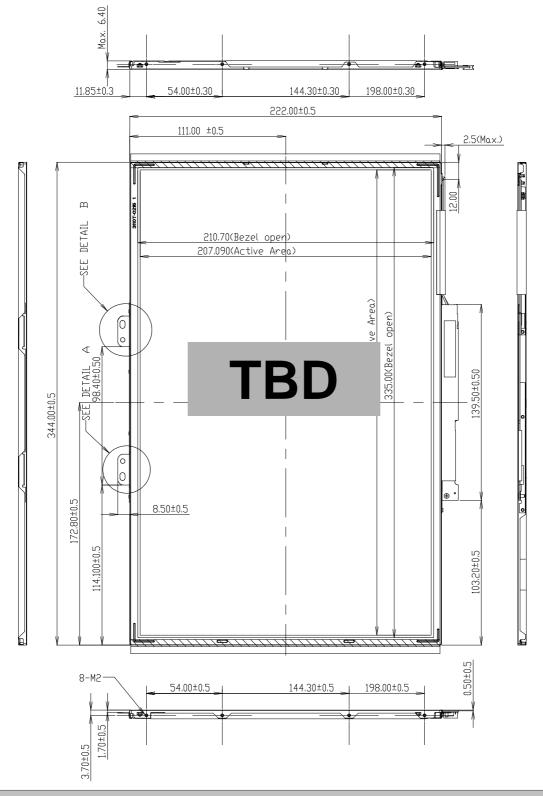
	Horizontal	344.0 ± 0.5mm				
Outline Dimension	Vertical	222.0 ± 0.5mm				
	Depth	6.1 ± 0.3mm				
Bezel Area	Horizontal	335.0 ± 0.5mm				
bezei Alea	Vertical	210.7 ± 0.5mm				
Active Dieplay Area	Horizontal	331.344 mm				
Active Display Area	Vertical	207.090 mm				
Weight	520g (Max) w/o inverter & bracket					
Surface Treatment	Hard coating(2H) Glare treatment of the front polarizer					

Ver. 0.0 May, 29, 2006 16 / 24



<FRONT VIEW>

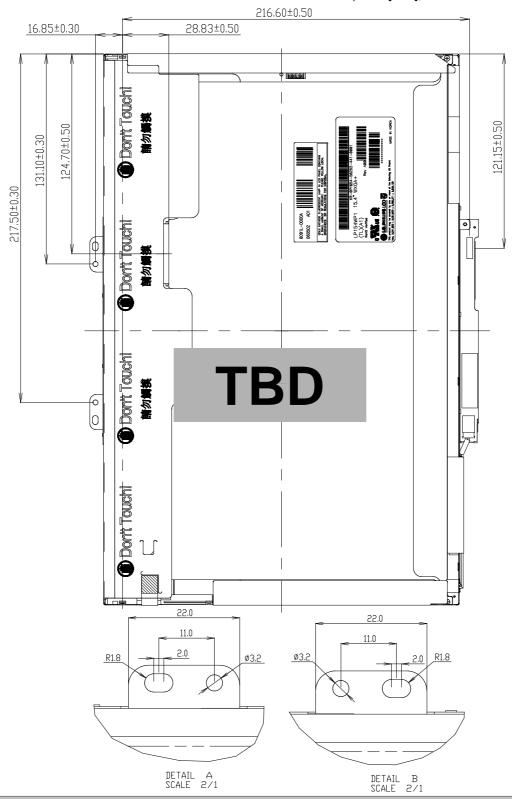
Note) Unit:[mm], General tolerance: ± 0.5mm





<REAR VIEW>

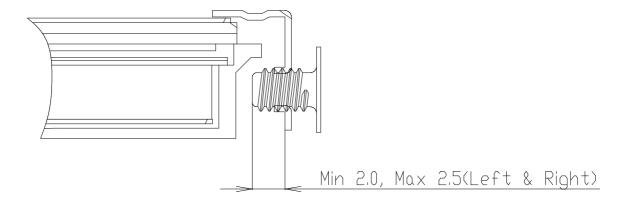
Note) Unit:[mm], General tolerance: ± 0.5mm





[DETAIL DESCRIPTION OF SIDE MOUNTING SCREW]

*Screw Torque (8 point): Max. 2Kgf.cm



Ver. 0.0 May, 29, 2006 19 / 24



6. Reliability

Environment test condition

No.	Test Item	Conditions
1	High temperature storage test	Ta= 60°C, 240h
2	Low temperature storage test	Ta= -20°C, 240h
3	High temperature operation test	Ta= 50°C, 50%RH, 240h
4	Low temperature operation test	Ta= 0°C, 240h
5	Vibration test (non-operating)	Sine wave, 5 ~ 150Hz, 1.5G, 0.37oct/min 3 axis, 30min/axis
6	Shock test (non-operating)	- No functional or cosmetic defects following a shock to all 6 sides delivering at least 180 G in a half sine pulse no longer than 2 ms to the display module - No functional defects following a shock delivering at least 200 g in a half sine pulse no longer than 2 ms to each of 6 sides. Each of the 6 sides will be shock tested with one each display, for a total of 6 displays
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr

[{] Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



7. International Standards

7-1. Safety

a) UL 1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995.

Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

b) CAN/CSA C22.2 No. 950-95 Third Edition, Canadian Standards Association, Jan. 28, 1995.

Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.

c) EN 60950 : 1992+A1: 1993+A2: 1993+A3: 1995+A1: 1997+A11: 1997

IEC 950: 1991+A1: 1992+A2: 1993+A3: 1995+A1: 1996

European Committee for Electrotechnical Standardization(CENELEC)

EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998



8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

Α	В	С	D	Е	F	G	Н	I	J	К	L	М
				1 1			1 1			1 1	1 1	

A,B,C : SIZE(INCH) D : YEAR

E: MONTH F: FACTORY CODE

G: ASSEMBLY CODE H, I, J, K, L, M: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
ſ	Mark	1	2	3	4	5	6	7	8	9	Α	В	С

3. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing	HEESUNG
Mark	K	С	D

4. SERIAL NO.

Mark	100001~199999, 200001~299999, 300001~399999,, A00001~A99999,, Z00001~Z99999
------	---

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 20 pcs

b) Box Size: 437mm ×369mm × 339mm

Ver. 0.0 May, 29, 2006 22 / 24



9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm\ 200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

Ver. 0.0 May, 29, 2006 23 / 24



9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.