SPECIFICATION FOR APPROVAL

(♦) Final Specification

Title			,	15.0" XGA 1	TFT LO	CD				
BUYER	ARII	MA		SUPPLIER	_G LCD Inc.					
MODEL	EXPL	ORER		MODEL	ı	_P150X1-B2AR				
SIGNATURE		DATE		APPROV	VED BY	DATE	.			
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Please return 1 copy	onfirmatio	n	Product Engineering Dept.							
with your signature	vith your signature and comments.									

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RECORDS OF REVISIONS

Version No	Date	Page	DESCRIPTION
2.0	1999. 3. 12	14	Alteration of Brightness Variation (δ WHITE) Spec.
			: Max. 1.75(13 point) -> Max. 1.5(5 point)
			_

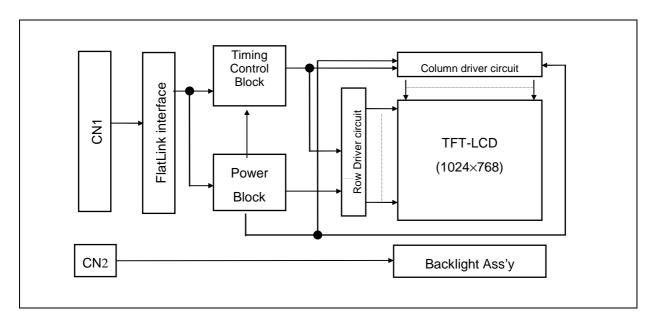
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1. General Description

The LP150X1 –B2AR is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Tube(CCFT) back light system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. This TFT-LCD has a 15.0 inch diagonally measured active display area with XGA resolution(768 vertical by 1024 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors.

The LP150X1 –B2AR has been designed to apply the interface method that enables low power, high speed low EMI. Flat Link must be used as a LVDS(Low Voltage Differential Signaling) chip.

The LP150X1-B2AR is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP150X1 –B2AR characteristics provide an excellent flat panel display for office automation products such as Notebook PC.



General Features

The following are general feature of the model LP150X1 LCD;
Active screen size 15.0inches (38.1cm) diagonal

Outline dimensions 315.5(H) \times 242.3(V) \times 7.0(D) mm (typ)

Pixel pitch 0.297 mm \times 0.297 mm Pixel format 1024 horiz. By 768 vert. pixels

RGB stripe arrangement 6-bit, 262,144 colors

Color depth 6-bit, 262,144 c Luminance,White 120 cd/m² (typ)

Power Consumption Total 4.82Watt,typ (1.22Watt @Vcc, 3.6Watt@120nit,Lamp)

Weight 685g (typ)

Display operating mode transmissive mode, normally white

Surface treatments hard coating(3H),

anti-glare treatment of the front polarizer

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2. Maximum Ratings

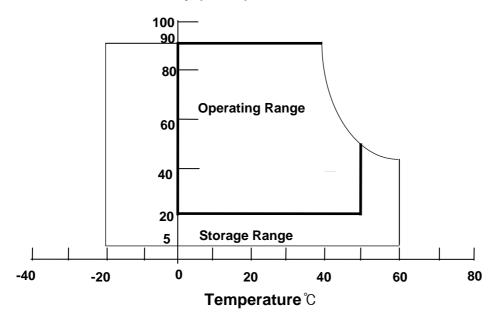
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1 ABSOLUTE MAXIMUM RATINGS

Parameter	symbol	Va	lues	Units	Notes
Farameter	Symbol	Min.	Max.	Offics	Notes
Power Input Voltage Operating Temperature Storage Temperature	V _{CC} T _{OP} T _{ST}	-0.3 0 -20	+3.6 +50 +60	Vdc ℃ ℃	at 25℃ 1 1

Note: 1. Temperature and relative humidity range are shown in the figure below.

Relative Humidity (% RH)



3. Electrical Specifications

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3-1. Electrical Characteristics

The LP150X1-B2AR requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the CCFL, is typically generated by an inverter. The inverter is an external unit to the LCD.

Table 2 ELECTRICAL CHARACTERISTICS:

Parameter	Symbol		Units	Notes		
		Min.	Тур.	Max.		
MODULE: Power Supply Input Voltage Power Supply Input Current Differential Impedance Power Consumption Rush current	V _{CC} I _{CC} Zm P _c I _{RUSH}	3.0 - 90 -	3.3 0.370 100 1.22 1.5	3.6 0.425 110 2.77 1.8	Vdc A ohm Watts A	1 1 2
LAMP Operating Voltage Operating Current Established Starting Voltage	V _{BL} I _{BL}	600 3.0	650 5.5	740 7.0	V _{RMS} mA	3 4
at 25 ℃ at 0 ℃ Operating Frequency Power Consumption Life Time	f _{BL} P _{BL}	40 - 10,000	- 55 3.6 15,000	1100 1300 70 4.2	V _{RMS} V _{RMS} kHz Watts Hrs	5 6

Notes: 1. The current draw and power consumption specified is for $3.3\,\text{Vdc}$ at $25\,^{\circ}\text{C}$ and fv at 60Hz.(at White $64\,\text{Gray}$ pattern displayed)

- 2. The duration of rush current is about 20ms
- 3. The variance of the voltage is $\pm 10\%$.
- 4. The output voltage at the transformer in the inverter must be high considering to the loss of the ballast capacitor in the

nverter

- 5. The lamp power consumption shown above does not include loss of external inverter.
- 6. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical

lamp current

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3-2. Interface Connections

Interface chip must be used FlatLink, part No. SN75LVDS84(Transmitter) made by Texas Instrument Inc.

This LCD employs two interface connections, a 20 pin connector is used for the module electronics and another connector is used for the integral backlight system.

The electronics interface connector is a model FI-SEB20P-HF13 manufactured by JAE or equivalent. The pin configuration for the connector is shown in the table below.

Table 3 MODULE CONNECTOR PIN CONFIGURATION (LVDS)

Pin	Symbol	Description	Notes
1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20	VCC VCC GND GND A1P GND A2P GND A3P GNC CLKP GNC CC GND GND	Power (3.3V) Power (3.3V) Ground Ground Difference Signal Difference Signal Ground Difference Signal Ofference Signal Ground Difference Signal Ground Difference Signal Ofference Signal Difference Signal Ground Difference Signal Ground Difference Signal Ground Difference Signal Ground Ofference Signal Ground No Connection No Connection Ground Ground	1. Interface chips 1.1 LCD : FPD85310 one-chip ASIC including LVDS Receiver 1.2 System : SN75LVDS84 or equivalent 2. Connector 2.1 LCD : FI-SEB20P-HF13 (JAE) or equivalent 2.2 System : FI-SEB20S (JAE) 2.3 Connector pin arrangement 20 19 2 1 < BOTTOM VIEW >

The backlight interface connector is a model BHSR-02VS-1, manufactured by JST. The mating connector part number is SM02B-BHSS-1 or equivalent. The pin configuration for the connector is shown in the table below.

Table 4 BACKLIGHT CONNECTOR PIN CONFIGURATION

Pin	Symbol	Description	Notes
1	HV	Lamp power input	1
2	LV	Ground	2

Notes: 1. The input power terminal is colored white. Ground pin color is black.

2. The lamp ground should be common with GND.

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Table 5 Required Signal Assignment for FlatLink

Pin #	Pin Name	Require Signals	Pin #	Pin Name	Require Signals
1	D4	R4	48	D3	R3
2	Vcc	Vcc	47	D2	R2
3	D5	R5	46	GND	GND
4	D6	G0	45	D1	R1
5	GND	GND	44	D0	R0
6	D7	G1	43	NC	NC
7	D8	G2	42	LVDS GND	LVDS GND
8	Vcc	Vcc	41	Y0M	АОМ
9	D9	G3	40	Y0P	AOP
10	D10	G4	39	Y1M	A1M
11	GND	GND	38	Y1P	A1P
12	D11	G5	37	LVDS Vcc	LVDS Vcc
13	D12	В0	36	LVDS GND	LVDS GND
14	NC	NC	35	Y2M	A2M
15	D13	B1	34	Y2P	A2P
16	D14	B2	33	CLKOUTM	CLKM
17	GND	GND	32	CLKOUTP	CLKP
18	D15	В3	31	LVDS GND	LVDS GND
19	D16	B4	30	PLLGND	PLL GND
20	D17	B5	29	PLLVcc	PLL Vcc
21	Vcc	Vcc	28	PLLGND	PLL GND
22	D18	HSYNC	27	SHDN	SHDN
23	D19	VSYNC	26	CLKIN	Dclk
24	GND	GND	25	D20	DE(Data Enable)

Notes: Refer to LVDS Transmitter Data Sheet for detail descriptions.

3-3. Signal Timing Specification

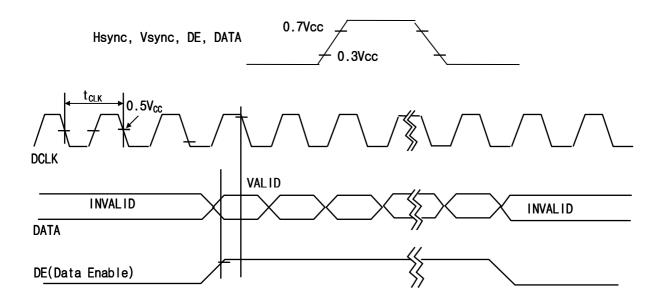
This is the signal timing required at the input of the LVDS Transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

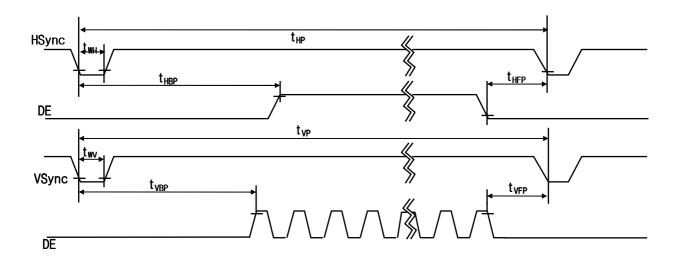
Table 6 Timing Table

	ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Dclk	Period	t _{CLK}	15.1	15.4	16.6		65MHz
Hsync	Period	t _{HP}	1280	1344	1364	t _{CLK}	
	Width-Active	t _{WH}	120	136	146		
Vsync	Period	t _{VP}	801	806	812	t _{HP}	
	Width-Active	t _{WV}	1	6	24		
DE	Horizontal Back Porch	t _{HBP}	65	160	-	t _{CLK}	
(Data	Horizontal Front Porch	t _{HFP}	20	24	45		
Enable)	Vertical Back Porch	t _{VBP}	2	29	-	t _{HP}	
	Vertical Front Porch	t _{VFP}	1	3	-		

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3-4. Signal Timing Wave form





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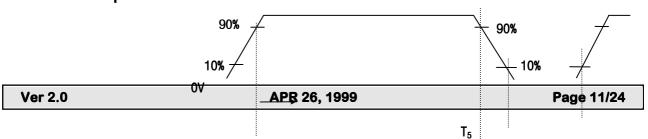
3-5. Color Input Data Reference

The brightness of each primary color(red, green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

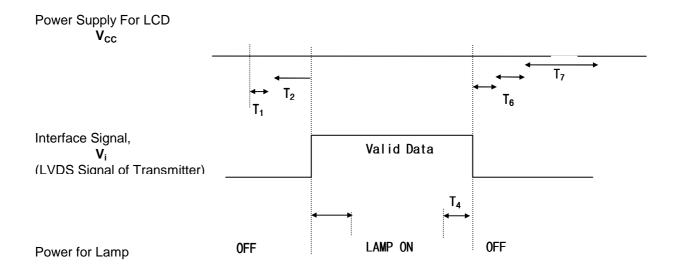
Table 7 COLOR DATA REFERENCE

Table 7 COLON DATA ILLI ENERGE																			
						Input Color Data													
			Re	ed					Gre	en					BI	ue			
		MSI	В			L	SB	MS	В			I	LSB	MS	В			I	_SB
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(00) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Red	Red(02)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(61)		1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)		1				1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(00)Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(01)	0	0	0	0	ő	0	Ö	0	0	0	0	1	0	0	0	0	0	0
	Green(02)	0	Ö	ő	ő	ő	0	Ö	0-	ő	Ö	1	Ö	ő	0	0	0	Ö	0
Green	0.00(02)	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)Bright	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(00) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(02)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue	_ , , , , , ,	:	:	:	:	:	:	:	:	1	:	:	:	:	:	:	:	:	:
	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63) Bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

3-6. Power Sequences



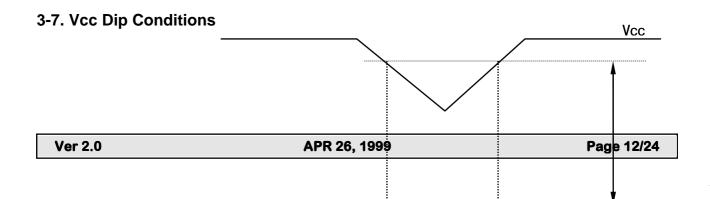


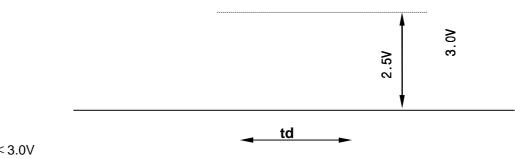


Parameter		Units		
	Min.	Тур.	Max.	
T ₁ T ₂ T ₃ T ₄ T ₅ T ₆ T ₇	0.01 1 1 0 0.01 3		40 — 50 - - 50 - -	8 8 8 8 8 8 8 8 8

Notes: 1. Please avoid floating state of interface signal at invalid period.

- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD V_{CC} to 0V.
- 3. Lamp power must be turn on after power supply for LCD and interface signal are valid.





1)2.5 $V \le Vcc < 3.0V$ td $\le 20 \text{ ms}$ 2)Vcc < 2.5V

Vcc-dip conditions should also follow the Power Up/Down conditions for supply voltage

Notes:This phenomenon is caused by row driver IC initialization after power on (1 vertical period).

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4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25° C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° .

Appendix A -1 presents additional information concerning the measurement equipment and method..

Table 8 OPTICAL CHARACTERISTICS

		TICAL CI	1AKACTE	KISTICS	Units	Notes
Parameter	Symbol	Min.	Values Typ.	Max.	Offics	Notes
		IVIII I.	īyρ.	IVIAA.		
Contrast Ratio	CR	100	250	-		1
Surface Luminance, white	L _{WH}	100	120	-	cd/m ²	2
Luminance Variation	δ WHITE	-	-	1.5		3
Response Time Rise Time Decay Time	Tr Tr _R Tr _D	- -	- -	30 50	msec	4
CIE Color Coordinates Red Green Blue White	X _R YR XG YG XB YB X _W Yw	0.550 0.320 0.265 0.520 0.120 0.105 0.270 0.310	0.580 0.350 0.295 0.550 0.150 0.135 0.300 0.340	0.610 0.380 0.325 0.580 0.180 0.165 0.330 0.370		
Viewing Angle x axis, right (Φ =0 °) x axis, left(Φ =180 °) y axis, up(Φ =90 °) y axis, down (Φ =270 °)	θ x θ x θ y θ y	+40 -40 +10 -30	- - -	- - - -	degree	5
Gamma Curve		-	-	-		6

Notes 1. Contrast Ratio (CR) is defined mathematically as:

Contrast Ratio = Surface Luminance with all white pixels
Surface Luminance with all black pixels

- Surface luminance is the center point across the LCD surface 50cm from the surface with all pixels displaying white.For more information see Appendix A 2.
- 3. The variation in surface Luminance, δ where is determined by measuring L_{ON} at each test position 1 through 5, and then dividing the maximum L_{ON} of 5 points luminance by minimum L_{ON} of 5 points luminance. For more information see Appendix A 2.

 $\delta \, \text{whre} \, = \, \text{Maximum} \, (L_{ON1}, L_{ON2},L_{ON5}) \, \div \, \, \text{Minimum} \, (L_{ON1}, L_{ON2},L_{ON5})$

- 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr_R) and from black to white (Decay Time, Tr_D). For additional information see Appendix A - 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see Appendix A 4
- 6. Gray scale specification is as following.

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Gray Level	Luminance (%) (typ)
LO	0.3
L7	1.0
L15	2.1
L23	4.8
L31	12.0
L39	27.0
L47	50.0
L55	78.0
L63	100

5. Mechanical Characteristics

The chart below provides general mechanical characteristics for the model LP150X1-B2AR LCD. In addition, the figure below is a detailed mechanical drawing of the LCD. Note that dimension are given for reference purposes only.

Outside dimensions:

Horizontal $315.5\pm0.5\,\mathrm{mm}$ Vertical $242.3\pm0.5\,\mathrm{mm}$ Depth $7.0\pm0.3\,\mathrm{mm}$

Dep Bezel area :

> Horizontal $308.5\pm0.5\,\mathrm{mm}$ Vertical $232.5\pm0.5\,\mathrm{mm}$

Active Display area:

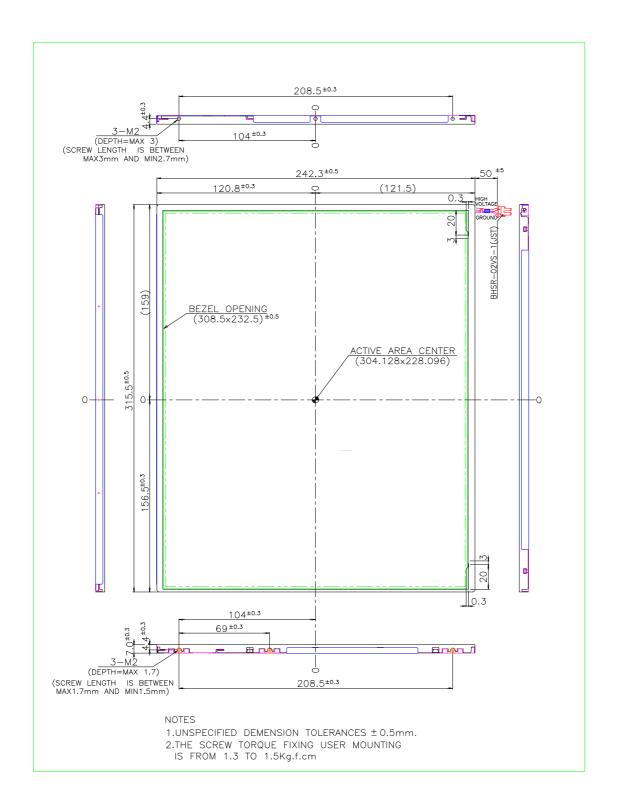
Horizontal 304.128 mm Vertical 228.096 mm

Weight (approximate): 685g (typ), 700g(max)

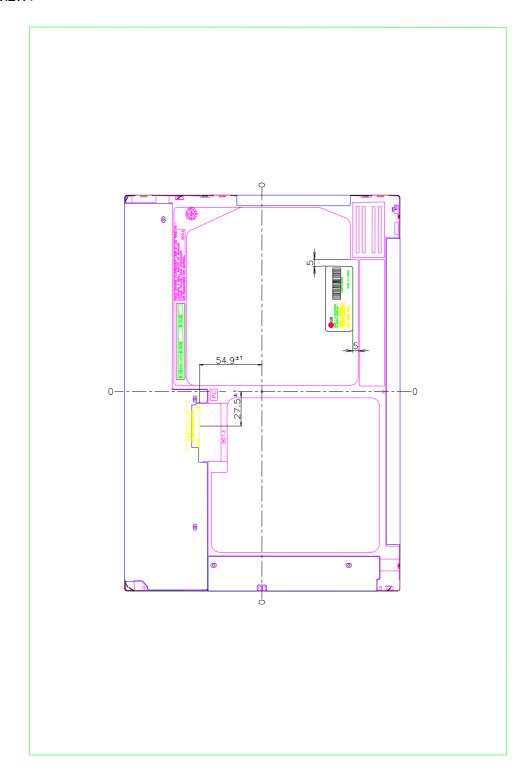
Surface Treatment : Hard coating 3H.

Anti-glare treatment of the front polarizer

< FRONT VIEW >



<REAR VIEW>





6. Reliablity

- Environment test condition

No.	Test ITEM	Conditions
1	High temperature storage test	Ta = 60°C 240h
2	Low temperature storage test	Ta = -20 ℃ 240h
3	High temperature operation test	Ta = 50 ℃ 50%RH 240h
4	Low temperature operation test	Ta = 0°C 240h
5	Vibration test (non-operating)	Sine wave, 10~500~10Hz, 1.5G, 0.37oct/min, 3 axis, 1 hour/axis
6	Shock test (non-operating)	half sine wave, 180G, 2ms, one shock of each six faces (i.e. run 180G 2ms for all six faces.)
7	Altitude operating storage/shipment	0 - 10,000 feet (3048m) 0 - 40,000 feet (12192m)

{Result Evaluation Criteria}

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

- ON/OFF Cycle
 - : The display module will be capable of being operated over 24,000 ON/OFF cycles (Lamp power & Vcc ON/OFF)
- Mean Time Between Failure
 - : The LCD Panel and interface board assembly (excluding the CCFTs) shell have a mean time between failures of 30,000 hours with a confidence level 90%.

7. International Standards

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7-1. Safety

- a) UL 1950 Third Edition, Underwriters Laboratories, Inc. Jan. 28, 1995.
 Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
- b) CAN/CSA C22.2 No. 950-95 Third Edition, Canadian Standards Association, Jan. 28, 1995. Standard for Safety of Information Technology Equipment Including Electrical Business Equipment.
- c) EN 60950: 1992 + A1: 1993 + A2: 1993 + A3: 1995 + A4: 1997 + A11: 1997

 IEC 950: 1991 + A1: 1992 + A2: 1993 + A3: 1995 + A4: 1996

 European Committee for Electrotechnical Standardization (CENELEC)

 EUROPEAN STANDARD for Safety of Information Technology Equipment Including Electrical Business Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9kHz to 40GHz." American National Standards Institute(ANSI),1992.
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interference Characteristics of Information Technology Equipment." International Special Committee on Radio Interference
- c) EN 55022 "Limits and Methods of Measurement of Radio Interference Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization (CENELEC),1988

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8. Packaging

8-1. Designation of Lot Mark

a) Lot Mark

Α	В	С	D	E	F	G	Н	I	J	K	L
---	---	---	---	---	---	---	---	---	---	---	---

A, B : DIVISION CODE C, D, E : MODEL CODE F : YEAR G : MONTH H, I, J, K, L : SERIAL NO.

Note: 1. YEAR

YEAR	89	90	91	92	93	94	95	96	97	98	99
Mark	9	0	1	2	3	4	5	6	7	8	9

2. MONTH

MONTH	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jun.	Aug.	Sep.	Oct.	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	0	N	D

b) Location of Lot Mark

Serial NO. Is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

9.PRECAUTIONS

Please pay attention to the followings when you use this TFT/LCD module.

9.1 MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners.
- (2) You should consider the mounting structure so that uneven force (ex. twisted stress) is not applied to the module.
 - And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface with a transparent protective plate in order to protect the polarizer LC cell. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polalizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil ead. And Please do not rub with dust clothes with chemical treatment. Do not touch the surface df polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaked with petrolium benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluen and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9.2 OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage: $V = \pm 200 \text{mV}$ (Over and under shoot voltage).
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
 And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) A module has high frequency circuit. If you need to shield the electromagnetic noise, please do in yours.
- (7) When a Back-light unit is operating, it sounds. If you need to shield the noise, please do in yours.

9.3 ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits. it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc.. And don't touch I/F pin directly.

9.4 PRECAUTION FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9.5 STORAGE

When storing modules as spares for a long time. The following precautions are necessary.

(1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5 °C and 35 °C at normal humidity.

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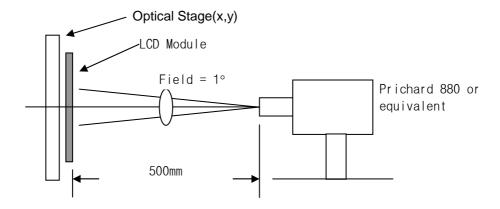
(2) The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

9.6 HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion- blown equipment or in such a condition, etc..
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal- hexane.

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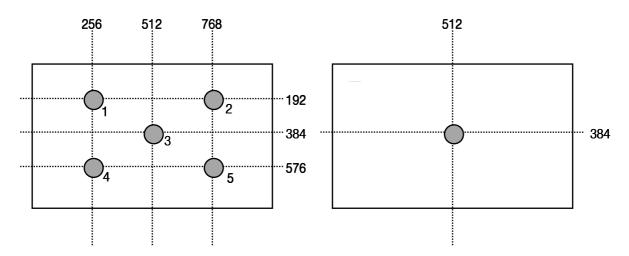
A-1 Optical Characteristic Measurement Equipment and Method



A-2 Luminance

<measuring point for luminance variation>

<measuring point for surface luminance >



A-3 Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

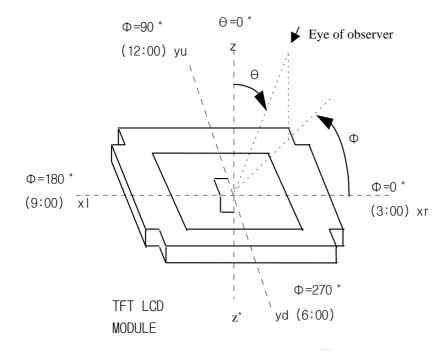
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A-4 Viewing angle

<Definition of viewing angle range>



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