

LMT080DIEFWU-1

LCD Module User Manual

Prepared by:	Checked by:	Approved by:
Weironglin		
won ong		
Date: 2016-11-11	Date:	Date:

Rev.	Descriptions	Release Date
0.1	Preliminary release	2016-01-27
0.2	7 Typing correction	2016-11-11

URL: www.topwaydisplay.com

www.topwaysz.com

Table of Content

1.	General Specification	. 3
2.	Block Diagram	. 3
3.	Terminal Function	. 4
3.1	K1 TFT Input Terminal	4
3.2	K2 (Backlight connector)	5
4.	Absolute Maximum Ratings	. 5
5.	Electrical Characteristics	. 5
5.1	DC Characteristics	5
5.2	Current Consumption	5
5.3	LED Backlight Circuit Characteristics	5
6.	Power Sequence	. 6
6.1	Power on:	6
6.2	Power off:	6
6.3	Timing Characteristics	7
6.4	Recommended Timing Setting Of TCON	7
6.5	Input Clock and Data timing Diagram:	8
6.6	Horizontal input timing Diagram:	8
7.	Optical Characteristics	. 9
Ω	Precautions of using LCD Modules	11

TOPWAY LCD Module User Manual LMT080DIEFWU-1

1. General Specification

Signal Interface : Digital 24-bits RGB
Display Technology : a-Si TFT active matrix
Display Mode : Transmissive / Normal White

Screen Size(Diagonal): 8.0"

Outline Dimension: 183.0 x 141.0 x 5.9 (mm)

(see attached drawing for details)

Active Area : 162.0 x 121.5 (mm)

Number of dots : 800 x 3 (RGB) x 600

Pixel Pitch : 0.0675 x 0.2025 (mm)

Pixel Configuration : RGB Stripe Backlight : LEDs

Surface Treatment : Anti-Glare Treatment

Viewing Direction: 6 o'clock(Gray scale Invesion)

Operating Temperature : $-20 \sim +70^{\circ}$ C Storage Temperature : $-30 \sim +80^{\circ}$ C

2. Block Diagram

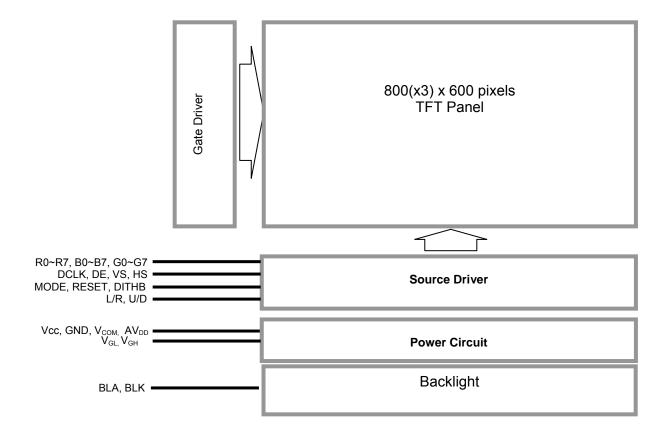


Figure 1

Page: 4 of 11

3. Terminal Function

K1 TFT Input Terminal

Pin No.	Pin Name	I/O	Descriptions			
1						
:	NC	_	No Connection			
4						
5	GND	Power	Power GND (0V)			
6	V _{COM}	Input	Common voltage			
7	Vcc	Power	Power for Digital Circuit			
8	MODE	Input	DE/SYNC mode select (*1)			
9	DE	Input	Data input enable			
10	VS	Input	Vertical Sync Input			
11	HS	Input	Horizontal Sync Input			
12	B7		, ,			
:		Input	8bit Data for Blue			
19	B0	1 '				
20	G7					
		Input	8bit Data for Green			
27	G0	1 '				
28	R7					
:		Input	8bit Data for Red			
35	R0	1				
36	GND	Power	Power GND (0V)			
37	DCLK	Input	Sample clock(*2)			
38	GND	Power	Power GND (0V)			
39	L/R	Input	Left / right selection (*3)			
40	U/D	Input	Up/down selection (*3)			
41	V_{GH}	Power	Gate ON Voltage			
42	V_{GL}	Power	Gate OFF Voltage			
43	AV_DD	Power	Power for Analog Circuit			
44	RESET	Input	Global reset pin (*4)			
45	NC	-	No connection			
46	V _{COM}	Input	Common Voltage			
47	DITHB	Input	Dithering function (*5)			
48	GND	Power	Power GND (0V)			
49	NC	_	No connection			
50	NC		INO CONNECTION			
Note:						

Note:

^{* 3:} Selection of scanning mode

Setting of scan control input		Scanning direction
U/D	L/R	Scarring direction
GND	Vcc	Up to down, left to right
Vcc	GND	Down to up, right to left
GND	GND	Up to down, right to left
Vcc	Vcc	Down to up, left to right

^{*4:} Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

When DITHB=" 0", Enable internal dithering function, For TTL 24 bits parallel RGB image data input.

^{* 1:} When select DE mode, MODE="1". When select SYNC mode, MODE= "0".

^{* 2:} Data shall be latched at the falling edge of DCLK.

^{*5:} Dithering function enable control, normally pull high.

When DITHB=" 1", Disable internal dithering function. For 18bit RGB interface, connect two LSB bits of all the R/G/B data buses to GND.

3.2 K2 (Backlight connector)

Pin No.	Pin Name	Ю	Descriptions	Wire Color
1	BLA	Power	LED driving anode (high voltage)	Red
2	BLK	Power	LED driving cathode (low voltage)	Black

4. Absolute Maximum Ratings

Items	Symbol	Min.	Max.	Unit	Condition
	Vcc	-0.3	5.0	V	
Power veltage	AV_DD	-0.5	13.5	V	
Power voltage	V_{GH}	-0.3	+19.0	V	
	V_{GL}	-12	+0.3	V	
Operating Temperature	T _{OP}	-20	70	°C	No Condensation
Storage Temperature	T _{ST}	-30	80	°C	No Condensation

Note:

5. Electrical Characteristics

5.1 DC Characteristics

Vcc=3.3V,GND=0V,T_a=25°C

Items	Symbol	Min.	Тур.	Max.	Unit	Remark
	Vcc	3.0	3.3	3.6	V	*2
Power voltage	AV_{DD}	10.2	10.4	10.6	V	
Power voltage	V_{GH}	15.3	16.0	16.7	V	
	V_{GL}	-7.7	-7.0	-6.3	V	
Input signal voltage	V_{COM}	2.8	3.8	4.8	V	
Input logic high voltage	V_{IH}	0.7Vcc	-	Vcc	٧	*3
Input logic low voltage	V_{IL}	0	-	0.3Vcc	V] 3
Motor	•		•			•

Note:

5.2 Current Consumption

Vcc=3.3V,GND=0V,T_a=25°C

Items	Symbol	Min.	Тур.	Max.	Unit	Remark
Current for Driver	I_{GH}	ı	0.2	0.5	mA	$V_{GH} = 16.0V$
	I_{GL}	ı	0.2	1.0	mA	$V_{GL} = -7.0V$
	I _{CC}	ı	5.5	10.0	mA	V _{DD} =3.3V
	IAVDD	1	32.0	50.0	mA	AV _{DD} =10.4V

5.3 LED Backlight Circuit Characteristics

Top=25°C

Items	Symbol	MIN.	TYP.	MAX.	Unit	Note
Forward Voltage	Vf _{BLA}	-	9.3	9.9	V	If=330mA
Forward Current	If _{BLA}	-	330.0	-	mA	

Cautions:

www.topwaydisplay.com Document Name: LMT080DIEFWU-1-Manual-Rev0.2.doc www.topwaysz.com Page: 5 of 11

^{*1.} This rating applies to all parts of the module. And should not be exceeded.

^{*2.} The operating temperature only guarantees operation of the circuit. The contrast, response speed, and the other specification related to electro-optical display quality is determined at the room temperature, T_{OP}=25.

^{*3.} Ambient temperature when the backlight is lit (reference value)

^{*4.} Any Stresses exceeding the Absolute Maximum Ratings may cause substantial damage to the device. Functional operation of this device at other conditions beyond those listed in the specification is not implied and prolonged exposure to extreme conditions may affect device reliability.

^{*1.}Be sure to apply VCC and VGL to the LCD first, and then apply VGH.

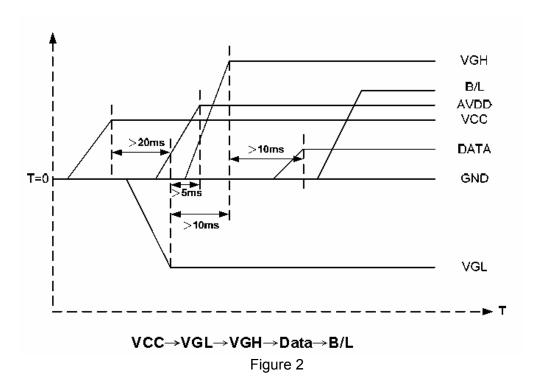
^{*2:} VCC setting should match the signals output voltage (refer to Note 3) of customer's system board.

^{*3:} DCLK,HS,VS,RESET,U/D, L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.

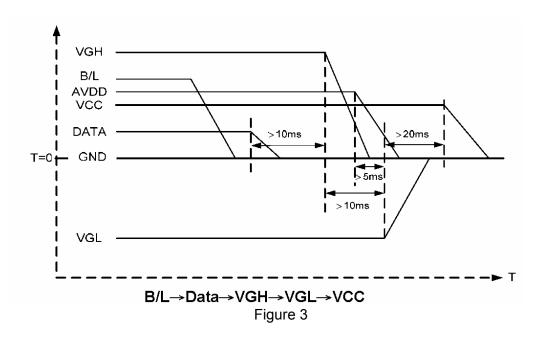
Exceeding the recommended driving current could cause substantial damage to the backlight and shorten its lifetime.

6. Power Sequence

6.1 Power on:



6.2 Power off:



AC Characteristics

6.3 Timing Characteristics

Item	Symbol	MIN.	TYP.	MAX.	Unit	Remark
DCLK Frequency	Fclk	1	40	50	ns	
DCLK Cycle Time	Tcph	20	25	-	ns	
DCLK Pulse duty	Tcw	40	50	60	%	
VSD Setup Time	Tvst	8	-	-	ns	
VSD Hold Time	Tvhd	8	-	-	ns	
HSD Setup Time	Thst	8	-	-	ns	
HSD Hold Time	Thhd	8	-	-	ns	
Data Setup Time	Tdsu	8	-	-	ns	Data to DCLK
Data Hold Time	Tdhd	8	-	-	ns	Data to DCLK
DE Setup Time	Tesu	8	-	-	ns	
DE Hold Time	Tehd	8	-	-	ns	
RSTB pulse width	TRst	10	-	-	ns	
Output stable Time	Tsst	-	-	6	ns	

6.4 Recommended Timing Setting Of TCON

TCON (Embedded In Source IC) Input Timing (DCLK, HSD, VSD, DE)

VCC=3.3V, AVDD=10.4V, AGND=GND=0V, Ta=25°C

ltem	Symbol	MIN.	TYP.	MAX.	Unit	Remark
DCLK	Fclk	-	40	50	MHZ	
DOLK	tclk	20	25	-	ns	
	th	862	1056	1200	tclk	
	thd	1	800	-	tclk	
HSD	thpw	1	-	40	tclk	
	thb	46	46	46	tclk	
	thfp	16	210	354	tclk	
	tv	624	635	700	th	
	tvd	-	600	-	th	
VSD	tvpw	1	-	20	th	
	tvb	23	23	23	th	
	tvfp	1	12	77	th	

Note: DE timing refer to HSD, VSD input timing.

6.5 Input Clock and Data timing Diagram:

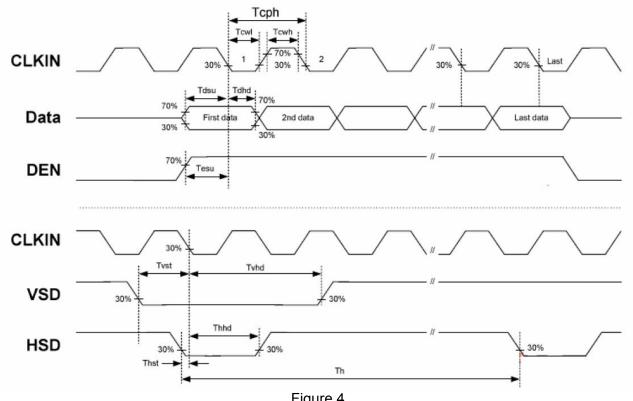


Figure 4

Horizontal input timing Diagram: 6.6



Figure 5

7. Optical Characteristics

Test Conditions:

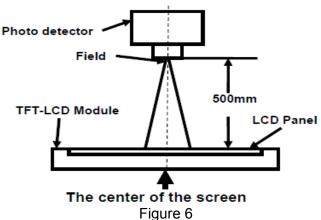
- 1. I_F= 20mA, V_F=9.3V, and the ambient temperature is 25 °C.
- 2. The test systems refer to Note 1 and Note 2.

Vcc=3.3V,GND=0V,Ta=25°C

Item		Symbol	Condition	MIN.	TYP.	MAX.	UNIT	Note.
Viewing angle (CR≥10)		θ_{L}	9 o'clock	60	70	-	degree	Note 2
		θ_{R}	3 o'clock	60	70	-		
		θτ	12 o'clock	40	50	-		
		θ_{B}	6 o'clock	60	70	-		
Response Time		T_f		-	10	20		Note 1
		T _r	Normal θ= Φ =0°	-	15	30		Note 4
Contrast ratio		CR		400	500	-	-	Note 1,3
Color chromaticity Wh	White	Х	0 4 0	0.26	0.31	0.36	-	Note 1
	vviile	Υ		0.28	0.33	0.38	-	Note 5
Luminance		L	-	-	900	-	cd/m ²	Note 1,6
Luminance uniformity		Y _U	-	70	75	-	%	Note 1,7

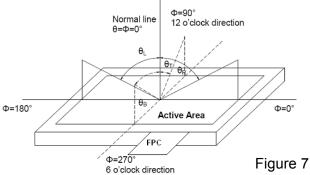
Note 1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.



Note 2: Definition of viewing angle range

Item	Photo detector	Field
Contrast Ratio		
Luminance	BM-5A	1°
Lum Uniformity		
Chromaticity	SR-3A	
Response Time	TRD100	-



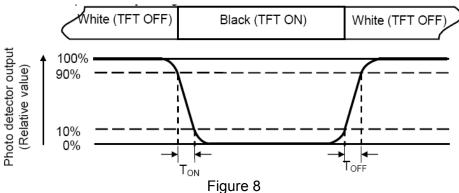
Note 3: Definition of contrast ratio

Contrast ratio (CR) = Luminance measured when LCD is on the "White" state Luminance measured when LCD is on the "Black" state

Note 4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and

"Black" state. Rise time (Ton) is the time between photo detector output intensity changed from 90% to 10%. And fall time (Toff) is the time between photo detector output intensity changed from 10% to 90%.

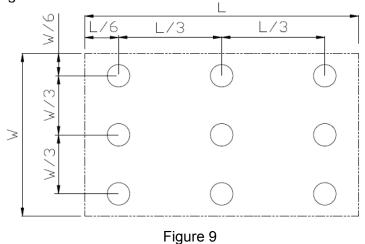


Note 5: Definition of color chromaticity (CIE1931) Color coordinates measured at center point of LCD.

Note 6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = Lmin/Lmax L-----Active area length W---- Active area width



Lmax: The measured Maximum luminance of all measurement position. Lmin: The measured Minimum luminance of all measurement position.

Note 7: Definition of Luminance:

Measure the luminance of white state at center point.

8. Precautions of using LCD Modules

Mounting

- Mounting must use holes arranged in four corners or four sides.
- The mounting structure so provide even force on to LCD module. Uneven force (ex. Twisted stress) should not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- It is suggested to attach a transparent protective plate to the surface in order to protect the polarizer. It should have sufficient strength in order to the resist external force.
- The housing should adopt radiation structure to satisfy the temperature specification.
- Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. Never rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics deteriorate the polarizer.)
- When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer

Operating

- The spike noise causes the mis-operation of circuits. It should be within the $\pm 200 \text{mV}$ level (Over and under shoot voltage)
- Response time depends on the temperature.(In lower temperature, it becomes longer.)
- Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- When fixed patterns are displayed for a long time, remnant image is likely to occur.
- Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference

Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

Storage

When storing modules as spares for a long time, the following precautions are necessary.

- Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.

Protection Film

- When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt tore main on the polarizer. Please carefully peel off the protection film without rubbing it against the polarizer.
- When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

Transportation

The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.