LMPBSS84JZF 60V P-Channel MOSFET

Features

- -60V/-0.13A, $R_{DS(ON)}$<10Ω@V_{GS}=-5V
- Super high density cell design for extremely low RDS (ON)
- Exceptional on-resistance and maximum DC current capability
- SOT-23 package design

Product Description

LMPBSS84JZF, P-Channel enhancement mode MOSFET, uses Advanced Trench Technology to provide excellent $R_{DS(ON)}$, low gate charge.

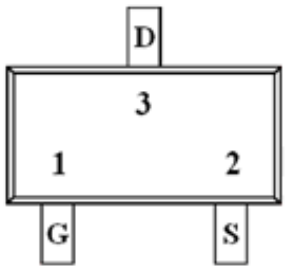
These devices are particularly suited for low voltage power management, such as smart phone and notebook computer and other battery powered circuits, and low in-line power loss are needed in commercial industrial surface mount applications.

Applications

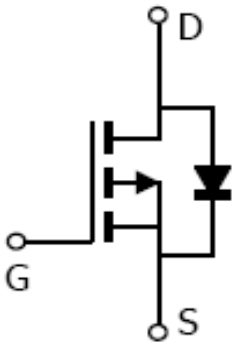
- DC to DC Converter
- Cellular & PCMCIA Card
- Power Management in Portable and Battery etc
- Cordless Telephone

Pin Configuration

LMPBSS84JZF (SOT-23)



PIN	Description
1	Gate
2	Source
3	Drain



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Ordering Information

Ordering Information					
Part Number	P/N	PKG code	Pb Free code	Package	Quantity
LMPBSS84JZF	LMPBSS84	JZ	F	SOT-23	3000

Marking Information

Marking Information		
Part Marking	Part Number	LFC code
PD	P	D

Absolute Maximum Ratings

(T_C=25ºC Unless otherwise noted)

Symbol	Parameter	Typical	Unit
V_{DSS}	Drain-Source Voltage	-60	V
V_{GSS}	Gate-Source Voltage	±20	V
I_D	Continuous Drain Current(T_A=25℃)	-130	mA
I_{DM}	Pulsed Drain Current (tp≦10us)	-520	mA
I_S	Continuous Current	-0.13	A
P_D	Power Dissipation (T_A=25ºC)	225	mW
T_J	Operating Junction Temperature	-55 to 150	ºC
T_{STG}	Storage Temperature Range	-55 to 150	ºC
$R_{\theta JA}$	Maximax Junction to Ambient	556	ºC/ W

Note 1: Pulse Test: PW≦300us, Duty Cycle≦2%.
2: Switching Time is Essentially Independent of Operating Temperature.

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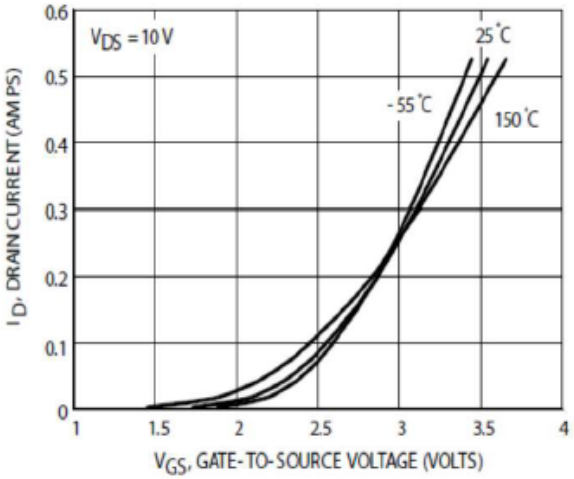
Electrical Characteristics

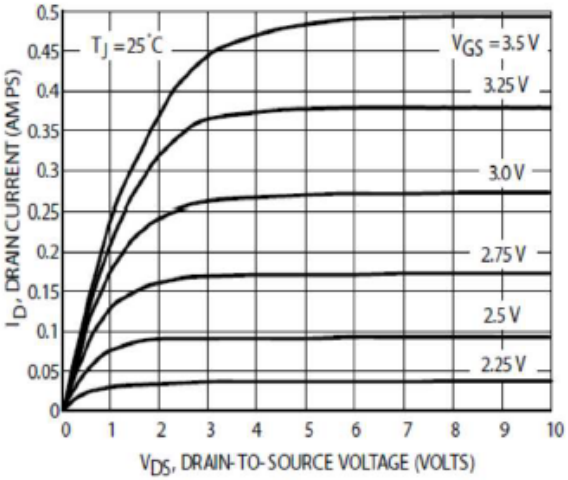
(T_C=25ºC Unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	V_{GS}=0V,I_D=-250uA	-60			V
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS},I_D=-1.0mA	-0.8		-2.0	
I_{GSS}	Gate-Source Leakage Current	V_{DS}=0V,V_{GS}=±20V			±60	uA
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS}= -25V,V_{GS}=0V			-0.1	uA
		V_{DS}= -50V,V_{GS}=0V			-15	
$R_{DS(on)}$	Drain-Source On-Resistance	V_{GS}=-5V,I_D=-100mA			10	Ω
g_{fs}	Forward Transconductance	V_{DS}=-25V,I_D=-100mA, f=1.0KHz	50			mS
V_{SD}	Forward Voltage			-2.5		V
Dynamic						
C_{iss}	Input Capacitance	V_{DS}=-5V, V_{GS}=0V,f=1MHz		30		pF
C_{oss}	Output Capacitance			10		
C_{rss}	Reverse Transfer Capacitance			5.0		
Q_G	Gate Charge			6		nC
$t_{d(on)}$	Turn-On Time	V_{DD}=-15V, R_L=50Ω ,I_D=-2.5A		25		ns
t_r				1.0		
$t_{d(off)}$	Turn-Off Time			16		
t_f				8.0		

Typical Performance Characteristics



Transfer Characteristics



On-Region Characteristics

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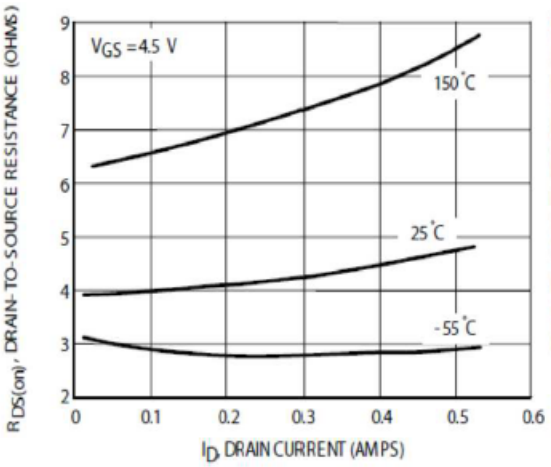
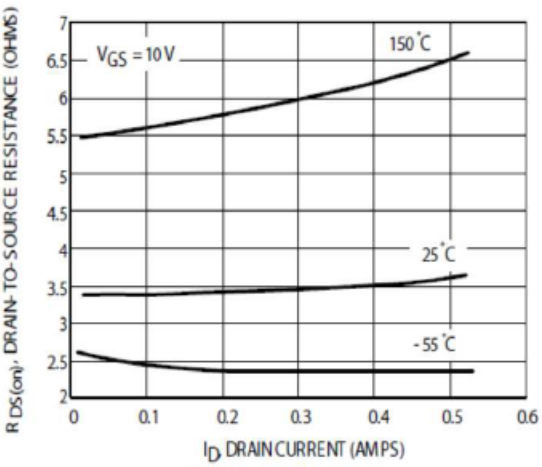
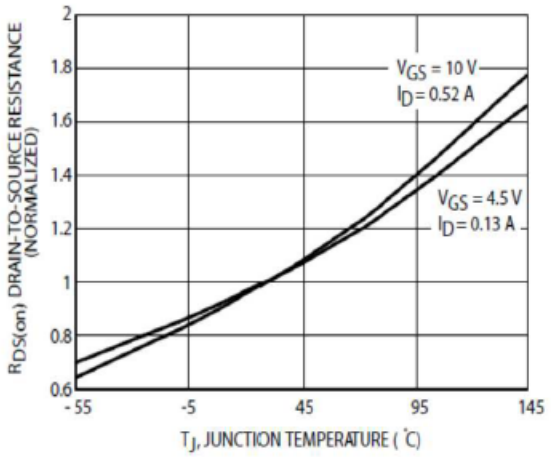
Typical Performance Characteristics(continue)



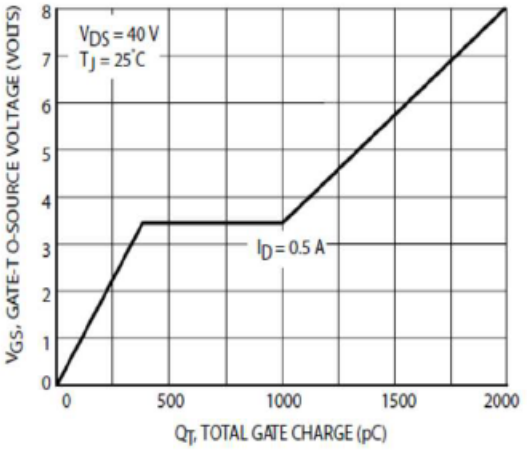
On-Resistance versus Drain Current



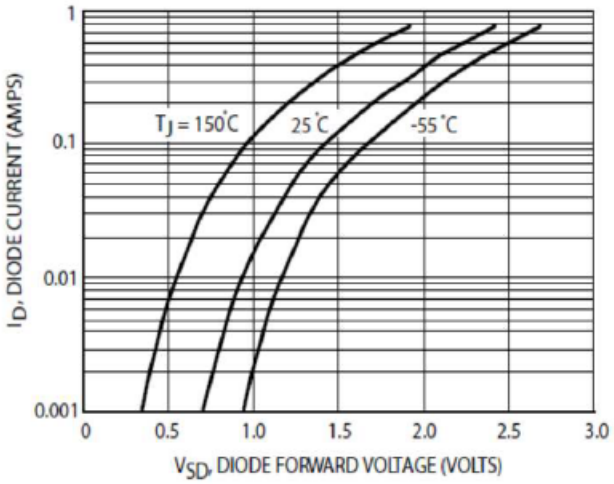
On-Resistance versus Drain Current



On-Resistance Variation with Temperature



Gate Charge



Body Diode Forward Voltage

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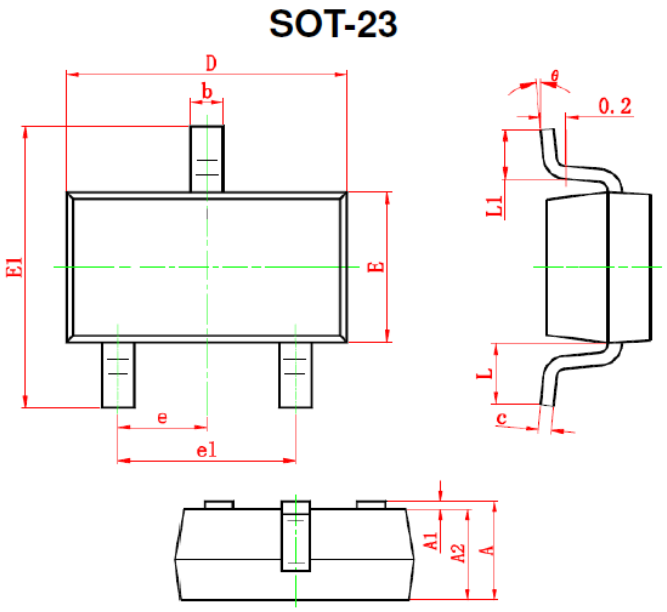
Package Dimension:



Dimensions				
Symbol	Millimeters		Inches	
	Min	Max	Min	Max
A	0.900	1.200	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.100	0.035	0.039
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	6°

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