

LME49720

Dual High Performance, High Fidelity Audio Operational Amplifier

General Description

The LME49720 is part of the ultra-low distortion, low noise, high slew rate operational amplifier series optimized and fully specified for high performance, high fidelity applications. Combining advanced leading-edge process technology with state-of-the-art circuit design, the LME49720 audio operational amplifiers deliver superior audio signal amplification for outstanding audio performance. The LME49720 combines extremely low voltage noise density (2.7nV/ $\sqrt{\text{Hz}}$) with vanishingly low THD+N (0.00003%) to easily satisfy the most demanding audio applications. To ensure that the most challenging loads are driven without compromise, the LME49720 has a high slew rate of $\pm 20\text{V}/\mu\text{s}$ and an output current capability of $\pm 26\text{mA}$. Further, dynamic range is maximized by an output stage that drives $2k\Omega$ loads to within 1V of either power supply voltage and to within 1.4V when driving 600 Ω loads.

The LME49720's outstanding CMRR (120dB), PSRR (120dB), and $\rm V_{OS}$ (0.1mV) give the amplifier excellent operational amplifier DC performance.

The LME49720 has a wide supply range of $\pm 2.5 \text{V}$ to $\pm 17 \text{V}$. Over this supply range the LME49720's input circuitry maintains excellent common-mode and power supply rejection, as well as maintaining its low input bias current. The LME49720 is unity gain stable. This Audio Operational Amplifier achieves outstanding AC performance while driving complex loads with values as high as 100pF.

The LME49720 is available in 8-lead narrow body SOIC, 8-lead Plastic DIP, and 8-lead Metal Can TO-99. Demonstration boards are available for each package.

Key Specifications

■ Power Supply Voltage Range

±2.5V to ±17V

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THD+N ($A_V = 1, V_{OLIT} = 3V_{BMS}, f_{IN} = 1kHz$)

$R_L = 2k\Omega$	0.00003% (typ)
$R_L = 600\Omega$	0.00003% (typ)
■ Input Noise Density	2.7nV/ $\sqrt{\text{Hz}}$ (typ)
■ Slew Rate	±20V/µs (typ)
■ Gain Bandwidth Product	55MHz (typ)
■ Open Loop Gain (R _L = 600Ω)	140dB (typ)
■ Input Bias Current	10nA (typ)
■ Input Offset Voltage	0.1mV (typ)
■ DC Gain Linearity Error	0.000009%

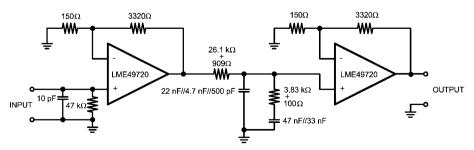
Features

- Easily drives 600Ω loads
- Optimized for superior audio signal fidelity
- Output short circuit protection
- PSRR and CMRR exceed 120dB (typ)
- SOIC, DIP, TO-99 metal can packages

Applications

- Ultra high quality audio amplification
- High fidelity preamplifiers
- High fidelity multimedia
- State of the art phono pre amps
- High performance professional audio
- High fidelity equalization and crossover networks
- High performance line drivers
- High performance line receivers
- High fidelity active filters

Typical Application

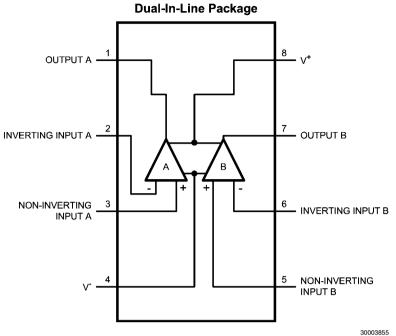


Note: 1% metal film resistors, 5% polypropylene capacitors

Passively Equalized RIAA Phono Preamplifier

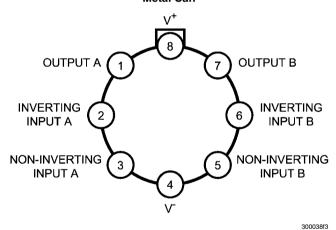
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Connection Diagrams



Order Number LME49720MA See NS Package Number — M08A Order Number LME49720NA See NS Package Number — N08E

Metal Can



Order Number LME49720HA See NS Package Number — H08C

2

200V

100V

150°C

145°C/W

Absolute Maximum Ratings (Notes 1, 2) If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ The proof Position of Position of Positions of Pos

Distributors for availability and specifications. Thermal Resistance Power Supply Voltage θ_{JA} (SO)

Output Short Circuit (Note 3)

Continuous

Temperature Range

Power Dissipation

Internally Limited

T < T < T

Power Dissipation Internally Limited $T_{MIN} \le T_A \le T_{MAX}$ $-40^{\circ}C \le T_A \le 85^{\circ}C$ ESD Susceptibility (Note 4) Supply Voltage Range $\pm 2.5V \le V_S \le \pm 17V$

Electrical Characteristics for the LME49720

			LME49720		l
Symbol	Parameter	Conditions	Typical	Limit	Units
-			(Note 6)	(Note 7)	(Limits)
THD+N	Total Harmonic Distortion + Noise	$A_{V} = 1, V_{OUT} = 3V_{rms}$			0/ ()
		$R_{L} = 2k\Omega$ $R_{L} = 600\Omega$	0.00003 0.00003	0.00009	% (max)
IMD	Intermodulation Distortion	$A_V = 1$, $V_{OUT} = 3V_{RMS}$ Two-tone, 60Hz & 7kHz 4:1	0.00005		%
GBWP	Gain Bandwidth Product		55	45	MHz (min
SR	Slew Rate		±20	±15	V/µs (min)
FPBW	Full Power Bandwidth	V _{OUT} = 1V _{P-P} , -3dB referenced to output magnitude at f = 1kHz	10		MHz
t _s	Settling time	$A_V = -1$, 10V step, $C_L = 100pF$ 0.1% error range	1.2		μs
	Equivalent Input Noise Voltage	f _{BW} = 20Hz to 20kHz	0.34	0.65	μV _{RMS} (max)
e _n	Equivalent Input Noise Density	f = 1kHz f = 10Hz	2.7 6.4	4.7	nV/√Hz (max)
i _n	Current Noise Density	f = 1kHz f = 10Hz	1.6 3.1		p A/ √Hz
V _{OS}	Offset Voltage		±0.1	±0.7	mV (max)
ΔV _{OS} /ΔTemp	Average Input Offset Voltage Drift vs Temperature	-40°C ≤ T _A ≤ 85°C	0.2		μV/°C
PSRR	Average Input Offset Voltage Shift vs Power Supply Voltage	ΔV _S = 20V (Note 8)	120	110	dB (min)
ISO _{CH-CH}	Channel-to-Channel Isolation	$f_{IN} = 1kHz$ $f_{IN} = 20kHz$	118 112		dB
I _B	Input Bias Current	V _{CM} = 0V	10	72	nA (max)
ΔI _{OS} /ΔTemp	Input Bias Current Drift vs Temperature	-40°C ≤ T _A ≤ 85°C	0.1		nA/°C
I _{os}	Input Offset Current	V _{CM} = 0V	11	65	nA (max)
V _{IN-CM}	Common-Mode Input Voltage Range		+14.1 -13.9	(V+) - 2.0 (V-) + 2.0	V (min)
CMRR	Common-Mode Rejection	-10V <vcm<10v< td=""><td>120</td><td>110</td><td>dB (min)</td></vcm<10v<>	120	110	dB (min)
-	Differential Input Impedance		30		kΩ
Z _{IN}	Common Mode Input Impedance	-10V <vcm<10v< td=""><td>1000</td><td></td><td>ΜΩ</td></vcm<10v<>	1000		ΜΩ

Symbol	Parameter	Conditions	LME49720		
			Typical	Limit	Units (Limits)
			(Note 6)	(Note 7)	
		-10V <vout<10v, r<sub="">L = 600Ω</vout<10v,>	140	125	
A _{VOL}	Open Loop Voltage Gain	$-10V$ <vout<10v, r<sub="">L = $2k\Omega$</vout<10v,>	140		dB (min)
		$-10V$ <vout<10v, r<sub="">L = 10kΩ</vout<10v,>	140		
V _{OUTMAX}	Maximum Output Voltage Swing	$R_L = 600\Omega$	±13.6	±12.5	V (min)
		$R_L = 2k\Omega$	±14.0		
		$R_L = 10k\Omega$	±14.1		
I _{OUT}	Output Current	$R_L = 600\Omega, V_S = \pm 17V$	±26	±23	mA (min)
I _{OUT-CC}	Instantaneous Short Circuit Current		+53 -42		mA
R _{OUT}	Output Impedance	f _{IN} = 10kHz			Ω
		Closed-Loop	0.01		
		Open-Loop	13		
C _{LOAD}	Capacitive Load Drive Overshoot	100pF	16		%
I _s	Total Quiescent Current	I _{OUT} = 0mA	10	12	mA (max)

Note 1: Absolute Maximum Ratings indicate limits beyond which damage to the device may occur.

Note 2: Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

Note 3: Amplifier output connected to GND, any number of amplifiers within a package.

Note 4: Human body model, 100pF discharged through a 1.5k Ω resistor.

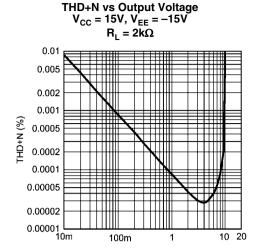
Note 5: Machine Model ESD test is covered by specification EIAJ IC-121-1981. A 200pF cap is charged to the specified voltage and then discharged directly into the IC with no external series resistor (resistance of discharge path must be under 50Ω).

Note 6: Typical specifications are specified at +25°C and represent the most likely parametric norm.

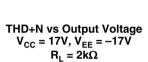
Note 7: Tested limits are guaranteed to National's AOQL (Average Outgoing Quality Level).

Note 8: PSRR is measured as follows: V_{OS} is measured at two supply voltages, $\pm 5V$ and $\pm 15V$. PSRR = | $20log(\Delta V_{OS}/\Delta V_S)$ |.

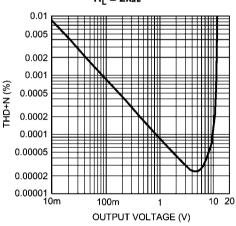
Typical Performance Characteristics



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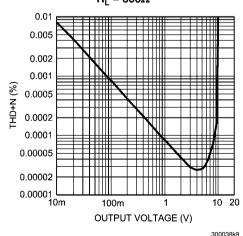


OUTPUT VOLTAGE (V)

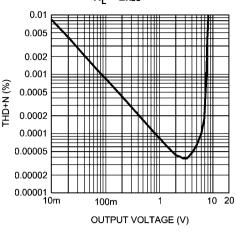


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THD+N vs Output Voltage $V_{CC} = 15V, \, V_{EE} = -15V \\ R_L = 600\Omega$

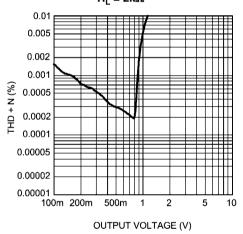


THD+N vs Output Voltage V_{CC} = 12V, V_{EE} = -12V R_L = 2k Ω



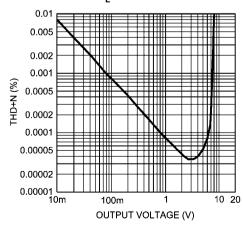
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THD+N vs Output Voltage V_{CC} = 2.5V, V_{EE} = -2.5V R_L = $2k\Omega$



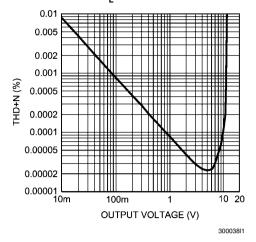
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THD+N vs Output Voltage V_{CC} = 12V, V_{EE} = -12V R_L = 600 Ω

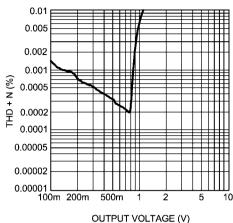


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THD+N vs Output Voltage V_{CC} = 17V, V_{EE} = -17V R_{L} = 600 Ω

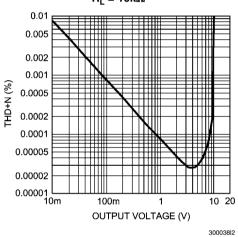


THD+N vs Output Voltage $\rm V_{CC}$ = 2.5V, $\rm V_{EE}$ = -2.5V $\rm R_{L}$ = 600Ω

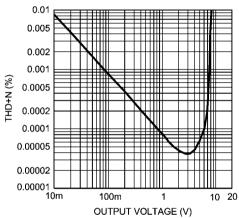


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THD+N vs Output Voltage V_{CC} = 15V, V_{EE} = -15V R_L = 10k Ω

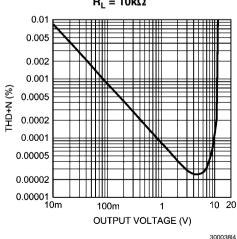


THD+N vs Output Voltage $V_{CC} = 12V, \, V_{EE} = -12V \\ R_L = 10k\Omega$

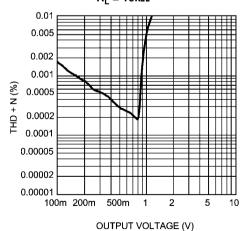


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THD+N vs Output Voltage V_{CC} = 17V, V_{EE} = -17V R_L = 10k Ω

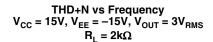


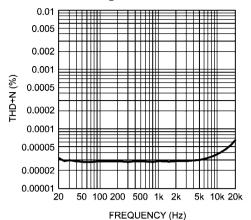
THD+N vs Output Voltage V_{CC} = 2.5V, V_{EE} = -2.5V R_{I} = 10k Ω



JI VOLIAGE (V)

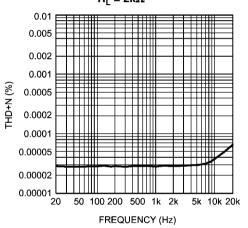
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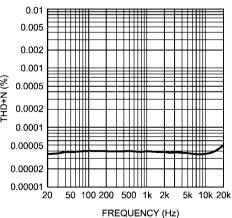
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THD+N vs Frequency V_{CC} = 17V, V_{EE} = -17V, V_{OUT} = $3V_{RMS}$ R_L = $2k\Omega$



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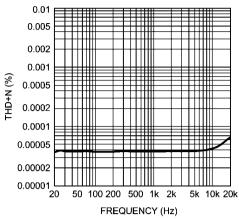
THD+N vs Frequency
$$\begin{aligned} \mathbf{V}_{\text{CC}} &= 12 \text{V, } \mathbf{V}_{\text{EE}} = -12 \text{V, } \mathbf{V}_{\text{OUT}} = 3 \mathbf{V}_{\text{RMS}} \\ \mathbf{R}_{\text{L}} &= 600 \Omega \end{aligned}$$



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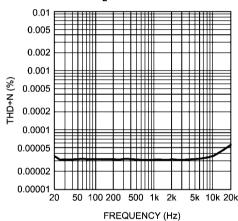
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THD+N vs Frequency
$$\begin{aligned} V_{CC} &= 12V, \, V_{EE} = -12V, \, V_{OUT} = 3V_{RMS} \\ R_L &= 2k\Omega \end{aligned}$$



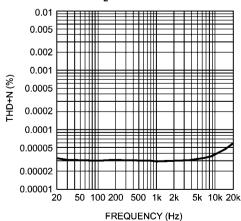
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THD+N vs Frequency
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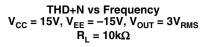


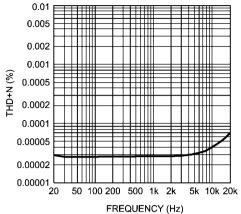
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THD+N vs Frequency
$$\begin{aligned} \text{V}_{\text{CC}} &= \text{17V}, \text{V}_{\text{EE}} = -\text{17V}, \text{V}_{\text{OUT}} = \text{3V}_{\text{RMS}} \\ \text{R}_{\text{L}} &= 600\Omega \end{aligned}$$

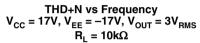


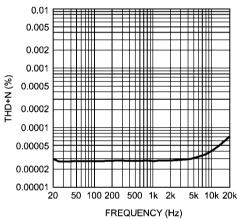
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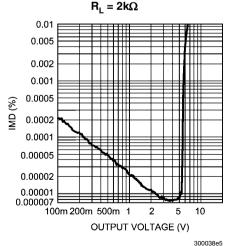
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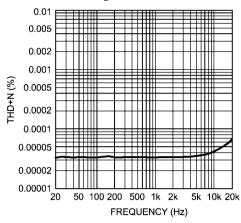


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IMD vs Output Voltage $V_{CC} = 12V$, $V_{EE} = -12V$

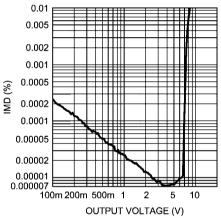


THD+N vs Frequency $\begin{aligned} V_{CC} &= 12V, \, V_{EE} = -12V, \, V_{OUT} = 3V_{RMS} \\ R_L &= 10k\Omega \end{aligned}$

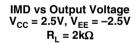


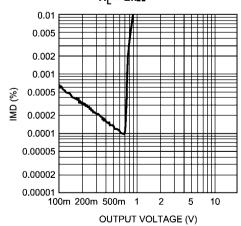
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IMD vs Output Voltage V_{CC} = 15V, V_{EE} = -15V R_L = $2k\Omega$

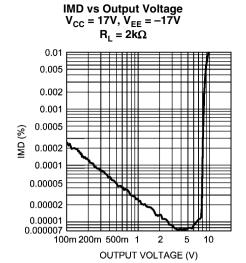


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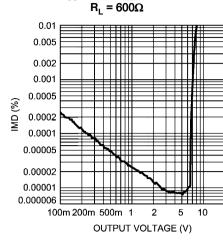




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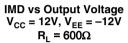


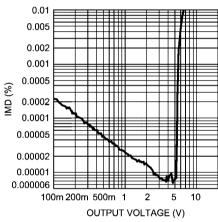
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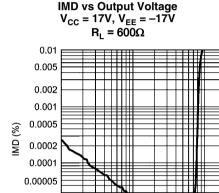
IMD vs Output Voltage $V_{CC} = 15V$, $V_{EE} = -15V$

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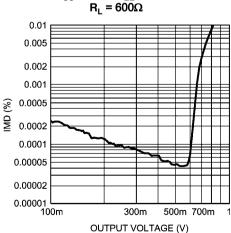
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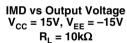
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IMD vs Output Voltage
$$V_{CC} = 2.5V$$
, $V_{EE} = -2.5V$



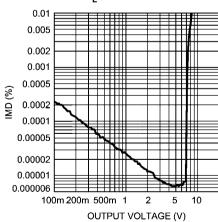
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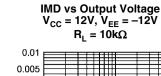


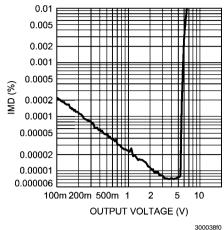
OUTPUT VOLTAGE (V)

100m 200m 500m 1

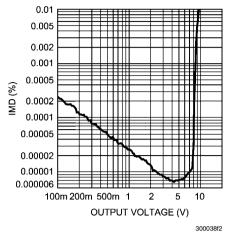


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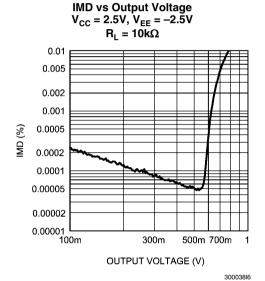


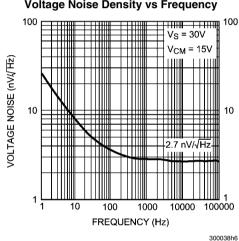


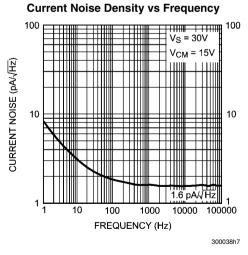


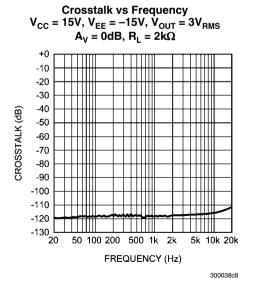
IMD vs Output Voltage $V_{CC} = 17V$, $V_{EE} = -17V$

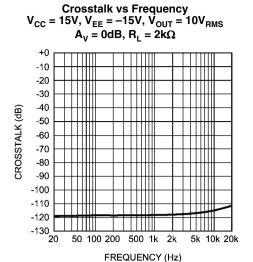
 $R_L = 10k\Omega$



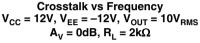


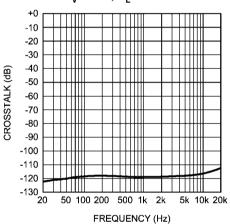




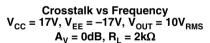


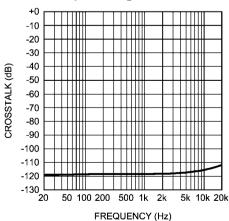
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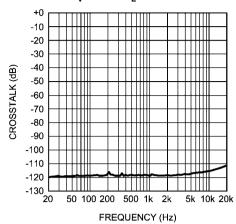
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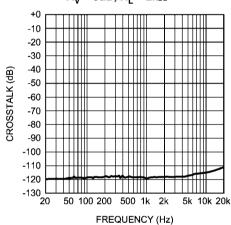
300038d1

 $\begin{array}{c} \text{Crosstalk vs Frequency} \\ \text{V}_{\text{CC}} = 12\text{V}, \text{V}_{\text{EE}} = -12\text{V}, \text{V}_{\text{OUT}} = 3\text{V}_{\text{RMS}} \\ \text{A}_{\text{V}} = 0\text{dB}, \text{R}_{\text{I}} = 2\text{k}\Omega \end{array}$



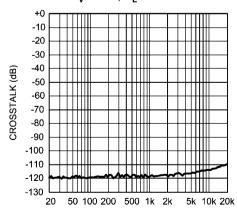
300038c6

 $\begin{aligned} & \text{Crosstalk vs Frequency} \\ V_{\text{CC}} &= 17V, V_{\text{EE}} = -17V, V_{\text{OUT}} = 3V_{\text{RMS}} \\ A_{_{V}} &= 0\text{dB}, R_{_{L}} = 2k\Omega \end{aligned}$



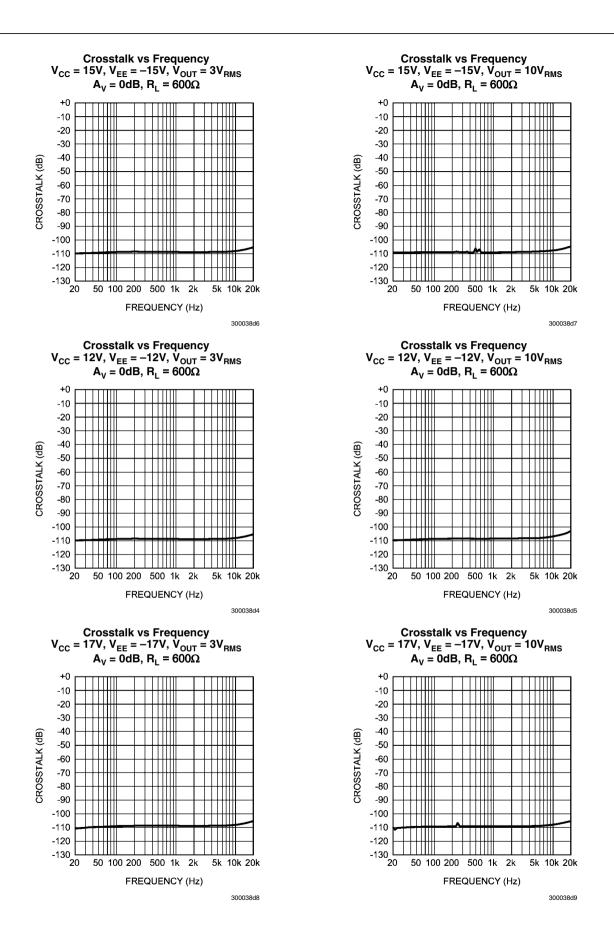
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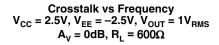
Crosstalk vs Frequency V_{CC} = 2.5V, V_{EE} = -2.5V, V_{OUT} = 1 V_{RMS} A_V = 0dB, R_1 = 2k Ω

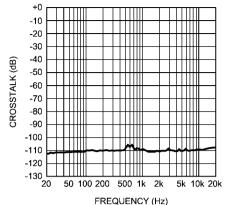


FREQUENCY (Hz)

300038n8

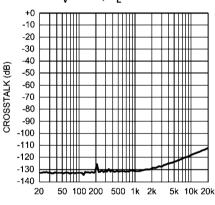






300038d2

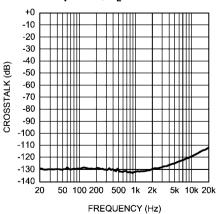
Crosstalk vs Frequency V_{CC} = 15V, V_{EE} = -15V, V_{OUT} = 10V $_{RMS}$ $A_{_{V}}$ = 0dB, $R_{_{L}}$ = 10k Ω



FREQUENCY (Hz)

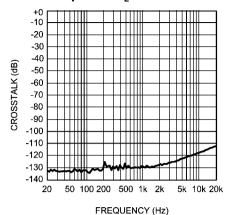
300038n7

$\begin{array}{l} \text{Crosstalk vs Frequency} \\ \text{V}_{\text{CC}} = 12\text{V}, \, \text{V}_{\text{EE}} = -12\text{V}, \, \text{V}_{\text{OUT}} = 10\text{V}_{\text{RMS}} \\ \text{A}_{\text{V}} = 0\text{dB}, \, \text{R}_{\text{L}} = 10\text{k}\Omega \end{array}$



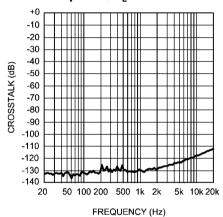
300038n6

 $\begin{array}{c} \text{Crosstalk vs Frequency} \\ \text{V}_{\text{CC}} = 15\text{V}, \, \text{V}_{\text{EE}} = -15\text{V}, \, \text{V}_{\text{OUT}} = 3\text{V}_{\text{RMS}} \\ \text{A}_{\text{V}} = 0\text{dB}, \, \text{R}_{\text{L}} = 10\text{k}\Omega \end{array}$



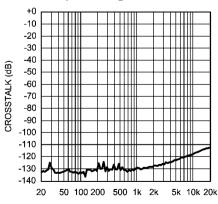
30003800

$\begin{aligned} & \text{Crosstalk vs Frequency} \\ V_{\text{CC}} &= 12V, \, V_{\text{EE}} = -12V, \, V_{\text{OUT}} = 3V_{\text{RMS}} \\ & A_{_{V}} = 0 dB, \, R_{_{L}} = 10 k\Omega \end{aligned}$



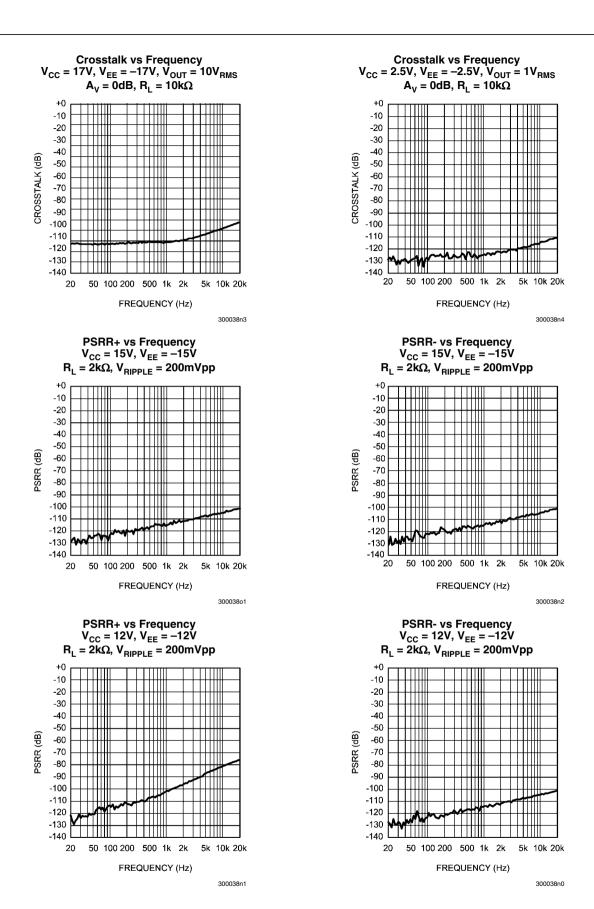
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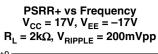
$\begin{aligned} & \text{Crosstalk vs Frequency} \\ & \text{V}_{\text{CC}} = 17\text{V}, \, \text{V}_{\text{EE}} = -17\text{V}, \, \text{V}_{\text{OUT}} = 3\text{V}_{\text{RMS}} \\ & \text{A}_{\text{V}} = 0\text{dB}, \, \text{R}_{\text{L}} = 10\text{k}\Omega \end{aligned}$

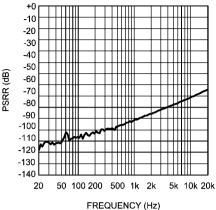


FREQUENCY (Hz)

300038n5

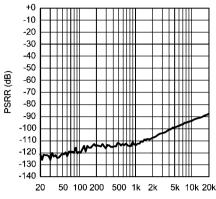






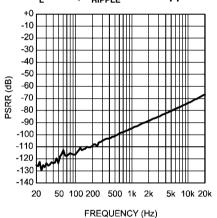
300038m9

PSRR- vs Frequency V_{CC} = 17V, V_{EE} = -17V R_L = 2k Ω , V_{RIPPLE} = 200mVpp



300038o3

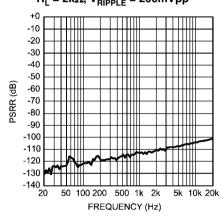
$\begin{aligned} & \text{PSRR+ vs Frequency} \\ & \text{V}_{\text{CC}} = 2.5\text{V}, \, \text{V}_{\text{EE}} = -2.5\text{V} \\ & \text{R}_{\text{L}} = 2\text{k}\Omega, \, \text{V}_{\text{RIPPLE}} = 200\text{mVpp} \end{aligned}$



300038m8

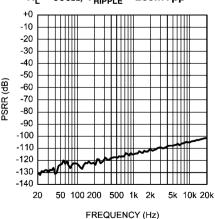
PSRR- vs Frequency V_{CC} = 2.5V, V_{EE} = -2.5V R_L = 2k Ω , V_{RIPPLE} = 200mVpp

FREQUENCY (Hz)



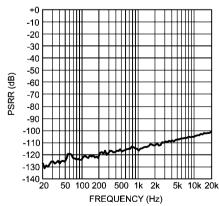
30003806

$\begin{aligned} & \text{PSRR+ vs Frequency} \\ & \text{V}_{\text{CC}} = 15\text{V}, \, \text{V}_{\text{EE}} = -15\text{V} \\ & \text{R}_{\text{L}} = 600\Omega, \, \text{V}_{\text{RIPPLE}} = 200\text{mVpp} \end{aligned}$

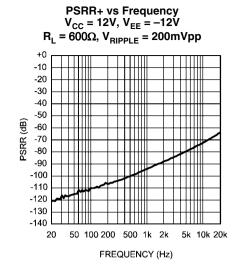


300038o2

$\begin{aligned} & \text{PSRR- vs Frequency} \\ & \text{V}_{\text{CC}} = 15\text{V}, \, \text{V}_{\text{EE}} = -15\text{V} \\ & \text{R}_{\text{L}} = 600\Omega, \, \text{V}_{\text{RIPPLE}} = 200\text{mVpp} \end{aligned}$

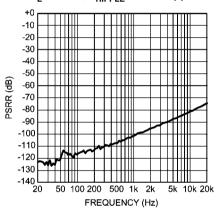


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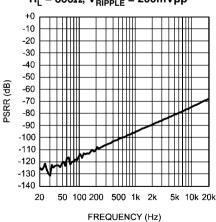
300038m7

PSRR+ vs Frequency V_{CC} = 17V, V_{EE} = -17V R_L = 600 Ω , V_{RIPPLE} = 200mVpp



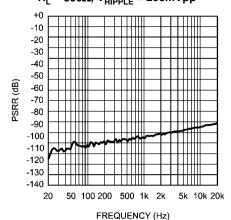
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 $\begin{aligned} & \text{PSRR+ vs Frequency} \\ & \text{V}_{\text{CC}} = 2.5\text{V}, \text{V}_{\text{EE}} = -2.5\text{V} \\ & \text{R}_{\text{L}} = 600\Omega, \text{V}_{\text{RIPPLE}} = 200\text{mVpp} \end{aligned}$



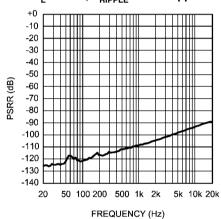
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 $\begin{array}{c} \text{PSRR- vs Frequency} \\ \text{V}_{\text{CC}} = 12\text{V}, \, \text{V}_{\text{EE}} = -12\text{V} \\ \text{R}_{\text{L}} = 600\Omega, \, \text{V}_{\text{RIPPLE}} = 200\text{mVpp} \end{array}$



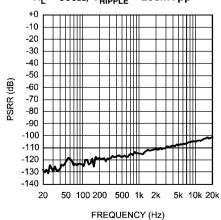
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 $\begin{aligned} & \text{PSRR- vs Frequency} \\ & \text{V}_{\text{CC}} = 17\text{V}, \, \text{V}_{\text{EE}} = -17\text{V} \\ & \text{R}_{\text{L}} = 600\Omega, \, \text{V}_{\text{RIPPLE}} = 200\text{mVpp} \end{aligned}$

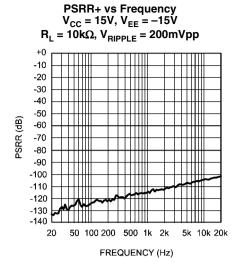


300038m6

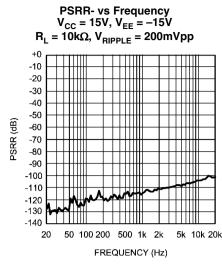
 $\begin{aligned} & \text{PSRR- vs Frequency} \\ & \text{V}_{\text{CC}} = 2.5\text{V}, \text{V}_{\text{EE}} = -2.5\text{V} \\ & \text{R}_{\text{L}} = 600\Omega, \text{V}_{\text{RIPPLE}} = 200\text{mVpp} \end{aligned}$



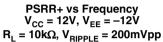
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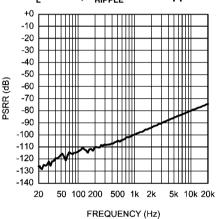


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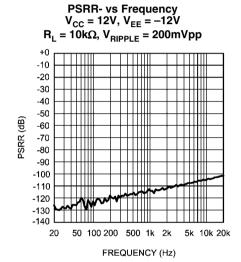


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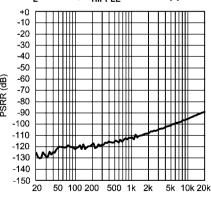


(ПZ) 300038m1



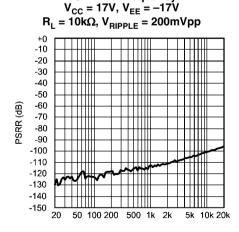
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 $\begin{aligned} & \text{PSRR+ vs Frequency} \\ & \text{V}_{\text{CC}} = 17\text{V}, \, \text{V}_{\text{EE}} = -17\text{V} \\ & \text{R}_{\text{L}} = 10\text{k}\Omega, \, \text{V}_{\text{RIPPLE}} = 200\text{mVpp} \end{aligned}$



FREQUENCY (Hz)

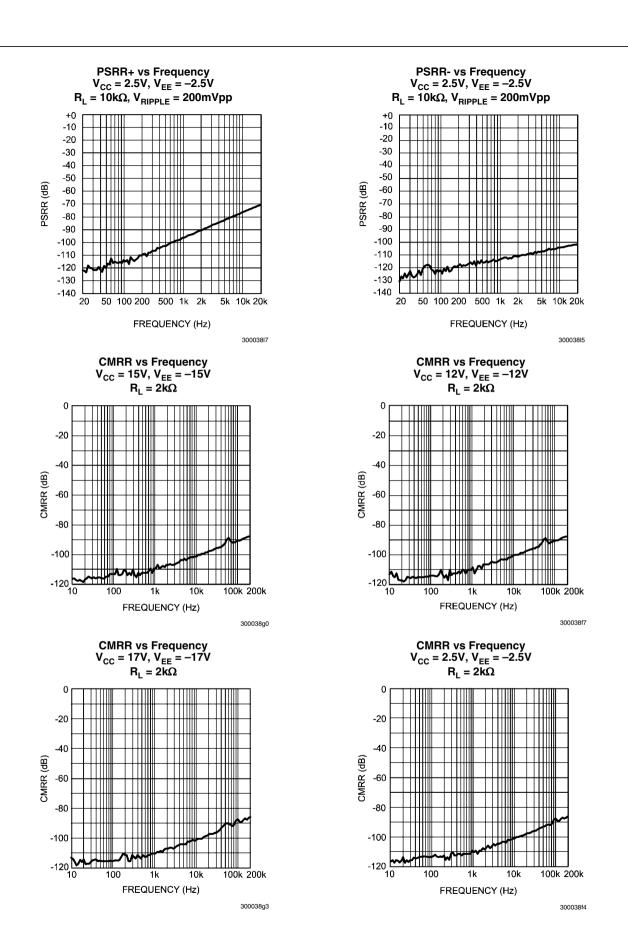
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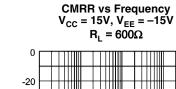


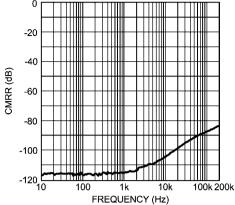
PSRR- vs Frequency

FREQUENCY (Hz)

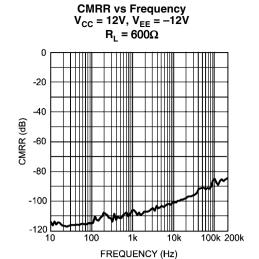
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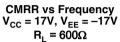


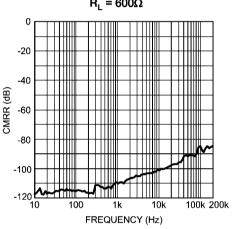


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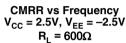


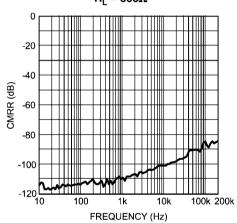
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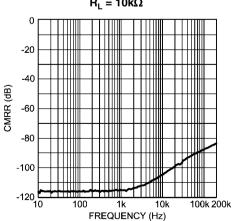
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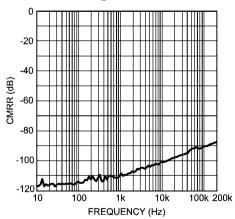
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CMRR vs Frequency V_{CC} = 15V, V_{EE} = -15V R_L = 10k Ω

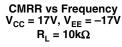


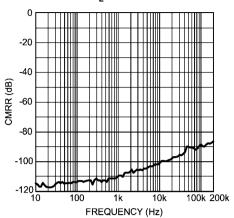
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CMRR vs Frequency V_{CC} = 12V, V_{EE} = -12V R_L = 10k Ω



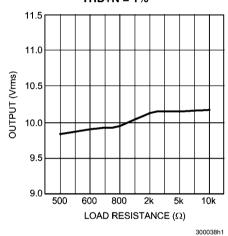
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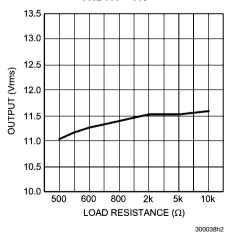


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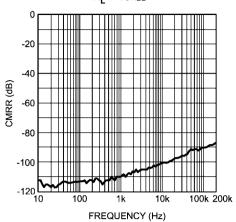
Output Voltage vs Load Resistance $V_{DD} = 15V, \, V_{EE} = -15V$ THD+N = 1%



Output Voltage vs Load Resistance $V_{DD} = 17V, V_{EE} = -17V$ THD+N = 1%

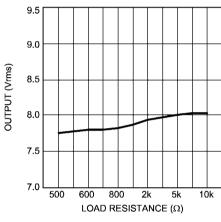


CMRR vs Frequency V_{CC} = 2.5V, V_{EE} = -2.5V R_L = 10k Ω



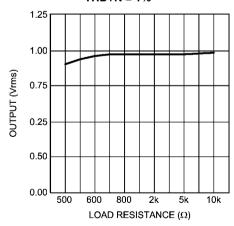
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Output Voltage vs Load Resistance V_{DD} = 12V, V_{EE} = -12V THD+N = 1%



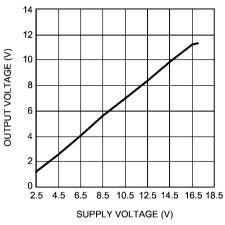
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Output Voltage vs Load Resistance V_{DD} = 2.5V, V_{EE} = -2.5V THD+N = 1%



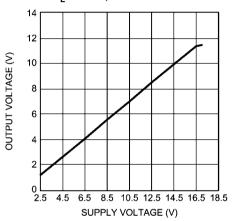
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Output Voltage vs Supply Voltage $R_1 = 2k\Omega$, THD+N = 1%



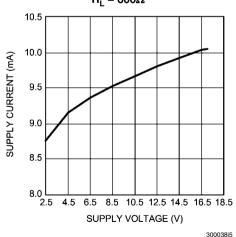
300038j9

Output Voltage vs Supply Voltage $R_L = 10k\Omega, THD + N = 1\%$

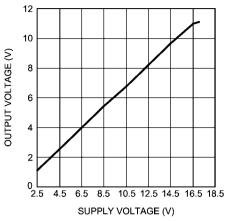


300038k0

Supply Current vs Supply Voltage $R_L = 600\Omega$

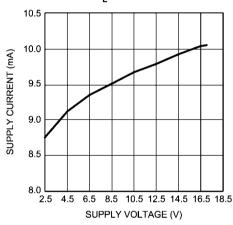


Output Voltage vs Supply Voltage $R_1 = 600\Omega$, THD+N = 1%



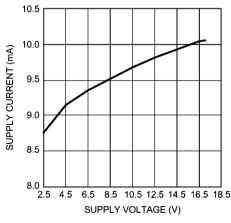
300038j8

Supply Current vs Supply Voltage $R_L = 2k\Omega \label{eq:RL}$



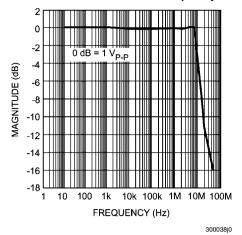
300038i6

Supply Current vs Supply Voltage $R_L = 10k\Omega$

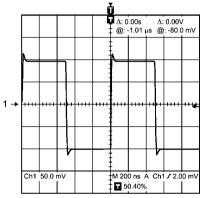


300038j7

Full Power Bandwidth vs Frequency



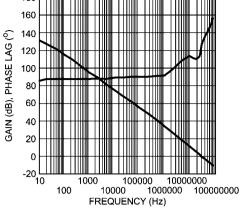
Small-Signal Transient Response $A_V = 1$, $C_L = 10pF$



300038i7

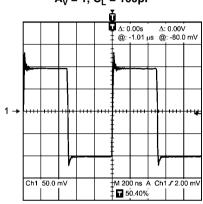
180

Gain Phase vs Frequency



300038j1

Small-Signal Transient Response $A_V = 1$, $C_L = 100pF$



300038i8

Application Information

DISTORTION MEASUREMENTS

The vanishingly low residual distortion produced by LME49720 is below the capabilities of all commercially available equipment. This makes distortion measurements just slightly more difficult than simply connecting a distortion meter to the amplifier's inputs and outputs. The solution, however, is quite simple: an additional resistor. Adding this resistor extends the resolution of the distortion measurement equipment.

The LME49720's low residual distortion is an input referred internal error. As shown in Figure 1, adding the 10Ω resistor connected between the amplifier's inverting and non-inverting

inputs changes the amplifier's noise gain. The result is that the error signal (distortion) is amplified by a factor of 101. Although the amplifier's closed-loop gain is unaltered, the feedback available to correct distortion errors is reduced by 101, which means that measurement resolution increases by 101. To ensure minimum effects on distortion measurements, keep the value of R1 low as shown in Figure 1.

This technique is verified by duplicating the measurements with high closed loop gain and/or making the measurements at high frequencies. Doing so produces distortion components that are within the measurement equipment's capabilities. This datasheet's THD+N and IMD values were generated using the above described circuit connected to an Audio Precision System Two Cascade.

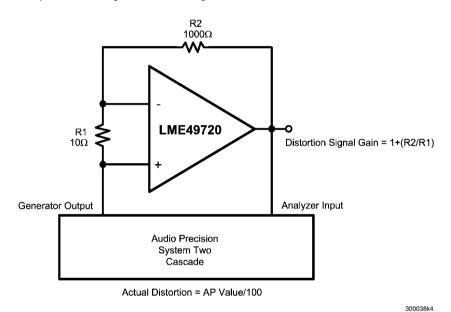
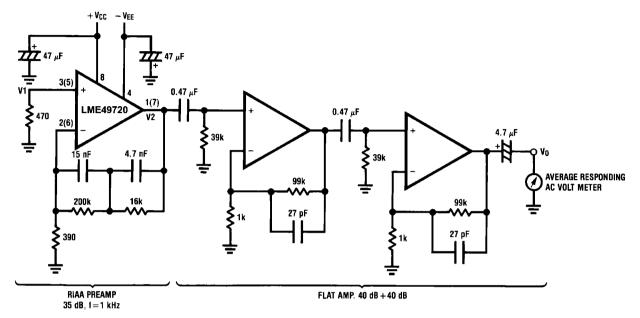


FIGURE 1. THD+N and IMD Distortion Test Circuit

The LME49720 is a high speed op amp with excellent phase margin and stability. Capacitive loads up to 100pF will cause little change in the phase characteristics of the amplifiers and are therefore allowable.

Capacitive loads greater than 100pF must be isolated from the output. The most straightforward way to do this is to put a resistor in series with the output. This resistor will also prevent excess power dissipation if the output is accidentally shorted

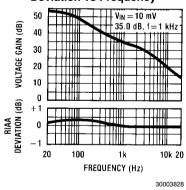


30003827

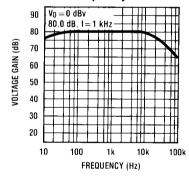
Complete shielding is required to prevent induced pick up from external sources. Always check with oscilloscope for power line noise.

Noise Measurement Circuit Total Gain: 115 dB @f = 1 kHz Input Referred Noise Voltage: $e_n = V0/560,000$ (V)

RIAA Preamp Voltage Gain, RIAA Deviation vs Frequency

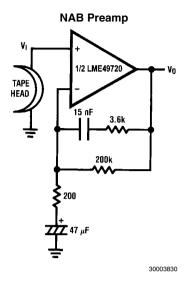


Flat Amp Voltage Gain vs Frequency

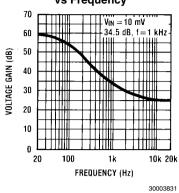


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TYPICAL APPLICATIONS

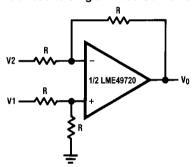


NAB Preamp Voltage Gain vs Frequency

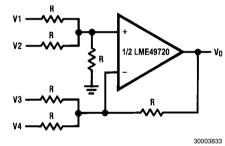


 $A_V = 34.5$ F = 1 kHz $E_n = 0.38 \mu\text{V}$ A Weighted

Balanced to Single Ended Converter



Adder/Subtracter

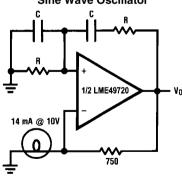


 $V_0 = V1 + V2 - V3 - V4$

V_O = V1–V2

Sine Wave Oscillator

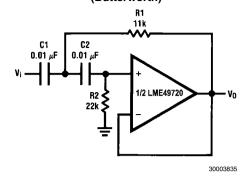
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$$f_0 = \frac{1}{2\pi BC}$$

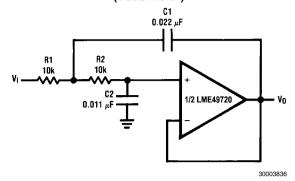
Second Order High Pass Filter (Butterworth)



$$R1 = \frac{\sqrt{2}}{2w_0C}$$

Illustration is $f_0 = 1 \text{ kHz}$

Second Order Low Pass Filter (Butterworth)

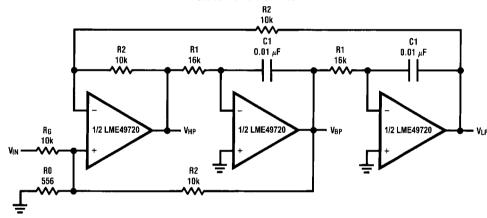


$$C1 = \frac{\sqrt{2}}{\omega_0 R}$$

$$C2 = \frac{C1}{2}$$

Illustration is $f_0 = 1 \text{ kHz}$

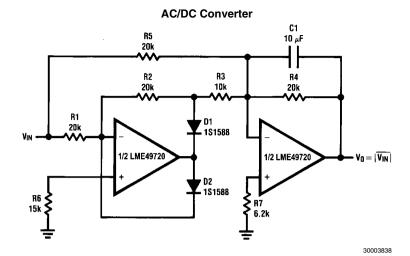
State Variable Filter



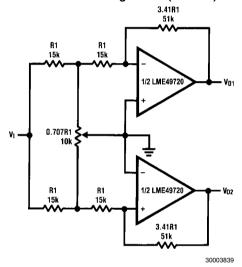
30003837

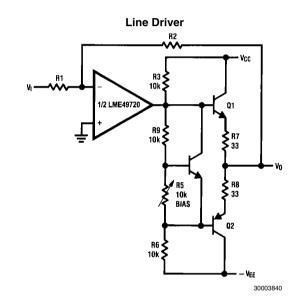
$$f_0 = \frac{1}{2\pi C 1 R 1}, Q = \frac{1}{2} \left(1 + \frac{R2}{R0} + \frac{R2}{RG} \right), A_{BP} = QA_{LP} = QA_{LH} = \frac{R2}{RG}$$

Illustration is $f_0 = 1 \text{ kHz}$, Q = 10, $A_{BP} = 1$

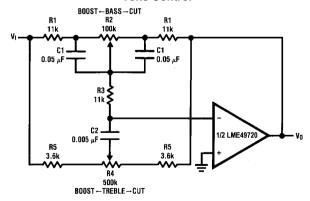


2 Channel Panning Circuit (Pan Pot)





Tone Control



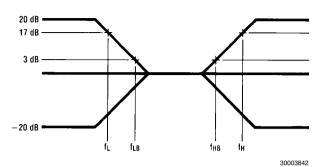
300038p0

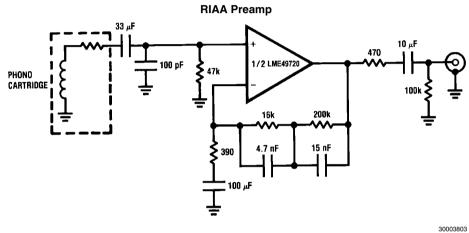
$$\begin{split} f_L &= \frac{1}{2\pi R2C1}, f_{LB} = \frac{1}{2\pi R1C1} \\ f_H &= \frac{1}{2\pi R5C2}, f_{HB} = \frac{1}{2\pi (R1 + R5 + 2R3)C2} \end{split}$$

Illustration is:

$$f_L = 32 \text{ Hz}, f_{LB} = 320 \text{ Hz}$$

 $f_H = 11 \text{ kHz}, f_{HB} = 1.1 \text{ kHz}$





 $A_{v} = 35 \text{ dB}$

 $E_n^{\nu} = 0.33 \, \mu V$

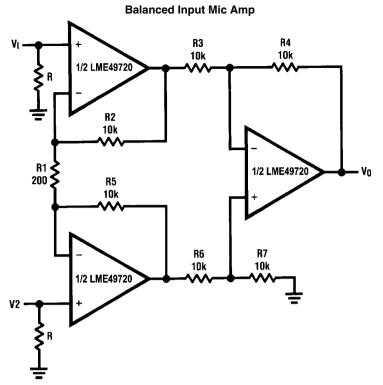
S/N = 90 dB

f = 1 kHz

A Weighted

A Weighted, $V_{IN} = 10 \text{ mV}$

@f = 1 kHz



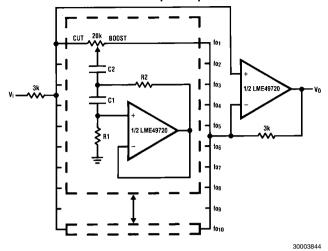
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If R2 = R5, R3 = R6, R4 = R7

$$V0 = \left(1 + \frac{2R2}{R1}\right) \frac{R4}{R3} (V2 - V1)$$

Illustration is: V0 = 101(V2 - V1)

10 Band Graphic Equalizer



fo (Hz)	C ₁	C ₂	R ₁	R ₂
32	0.12µF	4.7µF	75kΩ	500Ω
64	0.056µF	3.3µF	68kΩ	510Ω
125	0.033µF	1.5µF	62kΩ	510Ω
250	0.015µF	0.82µF	68kΩ	470Ω
500	8200pF	0.39µF	62kΩ	470Ω
1k	3900pF	0.22µF	68kΩ	470Ω
2k	2000pF	0.1µF	68kΩ	470Ω
4k	1100pF	0.056µF	62kΩ	470Ω
8k	510pF	0.022µF	68kΩ	510Ω
16k	330pF	0.012µF	51kΩ	510Ω

Note 9: At volume of change = $\pm 12 \text{ dB}$

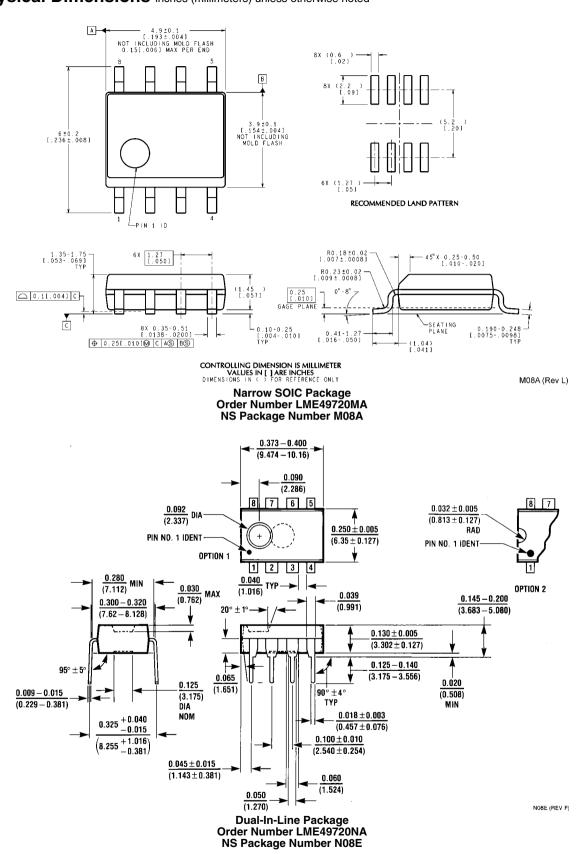
Q = 1.7

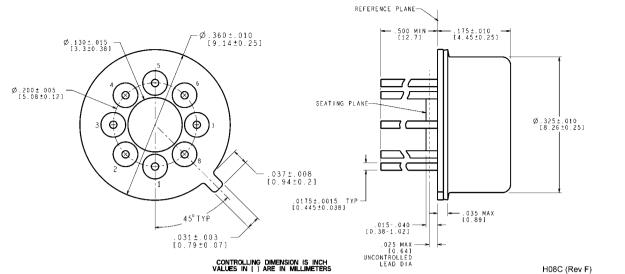
Reference: "AUDIO/RADIO HANDBOOK", National Semiconductor, 1980, Page 2-61

Revision History

Rev	Date	Description
1.0	03/30/07	Initial release.

Physical Dimensions inches (millimeters) unless otherwise noted





TO-99 Metal Can Package Order Number LME49720HA NS Package Number H08C

Notes

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