

# LM119, LM219, LM319

# High-speed dual comparators

Datasheet - production data

## **Features**

- Two independent comparators
- Supply voltage: +5 V to ±15 V
- Typically 80 ns response time at ±15 V
- Minimum fan-out of two each side
- Maximum input current of 1 µA over the operating temperature range
- Inputs and outputs can be isolated from system
  ground
- High common-mode slew rate

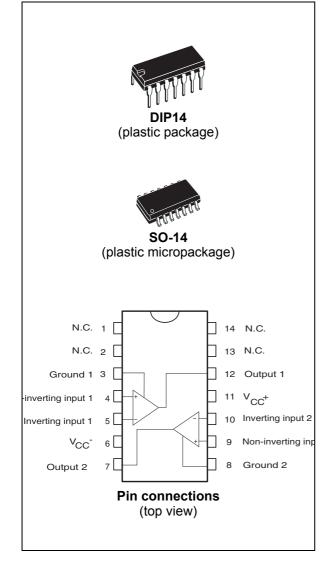
## Description

These products are precision high-speed dual comparators designed to operate over a wide range of supply voltages down to a single 5 V logic supply and ground. They feature low input currents and high gains.

The open collector of the output stage makes them compatible with transistor-transistor logic (TTL) as well as capable of driving lamps and relays at currents up to 25 mA.

Although designed primarily for applications requiring operation from digital logic supplies, these comparators are fully specified for power supplies up to  $\pm 15$  V.

They feature faster response times than the LM111 at the expense of higher current consumption. However, the high speed, wide operating voltage range and low package count make the LM119, LM219, and LM319 much more versatile.



This is information on a product in full production.

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# 1 Schematic diagram

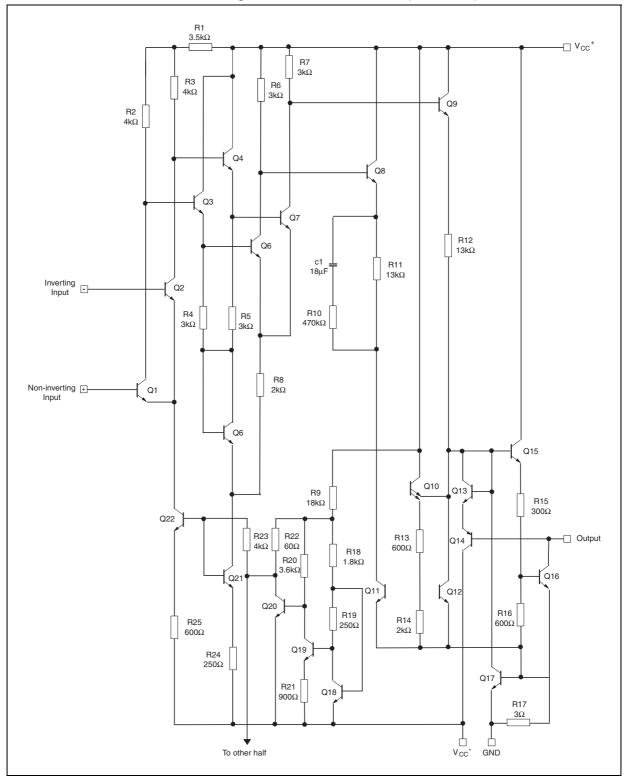


Figure 1. Circuit schematics (1/2 LM119)



# 2 Absolute maximum ratings and operating conditions

Symbol	Parameter	Value	Unit
V <sub>o</sub> - V <sub>CC</sub> <sup>-</sup>	Output to negative supply voltage	36	
V <sub>CC</sub> <sup>-</sup>	Negative supply voltage	-25	
V <sub>CC</sub> <sup>+</sup>	Positive supply voltage	18	V
V <sub>id</sub>	Differential input voltage	±5	
Vi	Input voltage <sup>(1)</sup>	±15	
	Output short-circuit to ground	Infinite	
Тj	Maximum junction temperature	150	ى°
T <sub>stg</sub>	storage temperature range	-65 to +150	
R <sub>thja</sub>	Thermal resistance junction to ambient <sup>(2)(3)</sup> DIP14 SO-14	80 105	°C/W
R <sub>thjc</sub>	Thermal resistance junction to case <sup>(2)(3)</sup> DIP14 SO-14	33 31	C/W
ESD	HBM: human body model <sup>(4)</sup> MM: machine model <sup>(5)</sup> CDM: charged device model <sup>(6)</sup>	400 100 1500	V

Table 1. Absolute maximum ratings (AM
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1. For supply voltages lower than ±15 V the absolute maximum input voltage is equal to the supply voltage.

2. Short-circuits can cause excessive heating. Destructive dissipation can result from simultaneous shortcircuits on all amplifiers.

3. R<sub>th</sub> are typical values.

4. Human body model: 100 pF discharged through a 1.5 k $\Omega$  resistor between two pins of the device, done for all couples of pin combinations with other pins floating.

 Machine model: a 200 pF cap is charged to the specified voltage, then discharged directly between two pins of the device with no external series resistor (internal resistor < 5 Ω), done for all couples of pin combinations with other pins floating.

6. Charged device model: all pins and the package are charged together to the specified voltage and then discharged directly to the ground through only one pin. This is done for all pins.

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Symbol	Parameter	Value	Unit
V <sub>CC</sub>	Supply voltage	5 to ±15	V
T <sub>oper</sub>	Operating free-air temperature range LM119 LM219 LM319	-55 to + 125 -45 to + 105 0 to + 70	°C



# 3 Electrical characteristics

0 milest	Demonster	LI	M119, L	M219		LM31	9	11
Symbol	Parameter		Тур.	Max.	Min	Тур.	Max.	Unit
V <sub>io</sub>	Input offset voltage $(R_s \le 5 \text{ k}\Omega)^{(1)(2)}$ $T_{min} \le T_{amb} \le T_{max}$		0.7	4 7		2	8 10	mV
I <sub>io</sub>	Input offset current <sup>(1)</sup> $T_{min} \le T_{amb} \le T_{max}$		30	75 100		80	200 300	-
I <sub>ib</sub>	Input bias current <sup>(1)</sup> $T_{min} \le T_{amb} \le T_{max}$		150	500 1000		250	1000 1200	– nA
A <sub>vd</sub>	Large signal voltage gain	10	40		8	40		V/mV
I <sub>CC</sub> +	Positive supply current $V_{CC} = \pm 15 V$ $V_{CC}^+ = +5 V$ , $V_{CC}^- = 0 V$		8 4.3	11.5		8 4.3	12.5	mA
I <sub>CC</sub> -	Negative supply current		3	4.5		3	5	
V <sub>icm</sub>	Input common mode voltage range $V_{CC} = \pm 15 V$ $V_{CC}^+ = +5 V, V_{CC}^- = 0 V$	±12 1	±13	3	±12 1	±13	3	
V <sub>OL</sub>	Low level output voltage $I_o = 25 \text{ mA}$ $V_i \leq -5 \text{ mV}$ $V_i \leq -10 \text{ mV}$ $T_{min} \leq T_{amb} \leq T_{max}$ $V_{CC}^+ \geq +4.5 \text{ V}, V_{CC}^- = 0 \text{ V}, I_{o(sink)} < 3.2 \text{ mA}$ $V_i \leq -6 \text{ mV}$ $V_i \leq -10 \text{ mV}$		0.75	1.5		0.75	1.5	v
I <sub>OH</sub>	$ \begin{array}{l} \mbox{High level output current } (V_o = +35 \ \mbox{V}) \\ V_i \geq 5 \ \mbox{mV} \\ V_i \geq 10 \ \mbox{mV} \\ T_{min} \leq T_{amb} \ \leq T_{max}, \ \mbox{V}_i \geq 5 \ \mbox{mV} \end{array} $		0.2 1	2 10		0.2	10	μΑ
t <sub>res</sub>	Response time <sup>(3)</sup>		80			80		ns

## Table 3. V<sub>CC</sub> = ±15 V, T<sub>amb</sub> = +25 °C (unless otherwise specified)

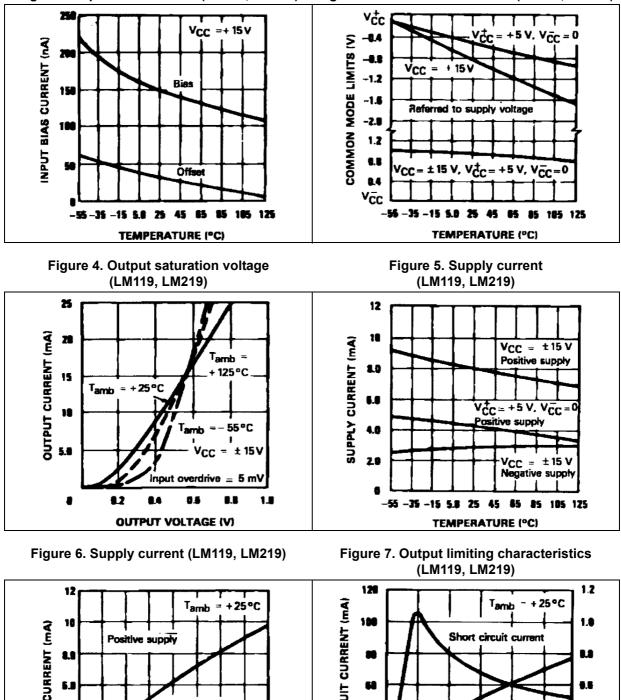
 These specifications apply for V<sub>CC</sub> = ±15 V, unless otherwise stated. The offset voltage, offset current and bias current specifications apply for any supply voltage from a single +5 V up to ±15 V supplies. The offset voltages and offset current given are the maximum values required to drive the output down to 1V or up to +14 V with a 1 mA load current. Thus, these parameters define an error band and take into account the worst case effects of voltage gain and input impedance.

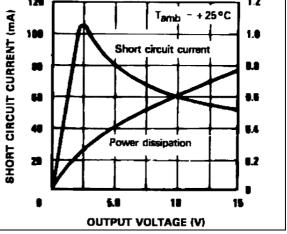
2. At output switch point, V<sub>o</sub>  $\approx$  1.4 V, no load, with V<sub>CC</sub> from 5 V to ±15 V and over the full input common-mode range.

3. The response time specified is for a 100 mV input step with 5 mV overdrive.



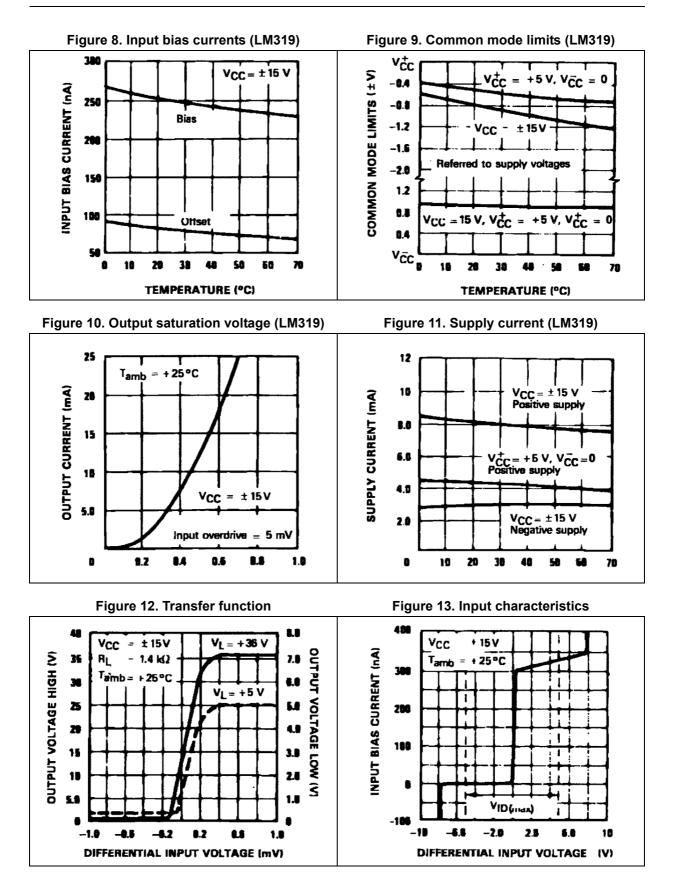
Figure 2. Input bias currents (LM119, LM219) Figure 3. Common mode limits (LM119, LM219)





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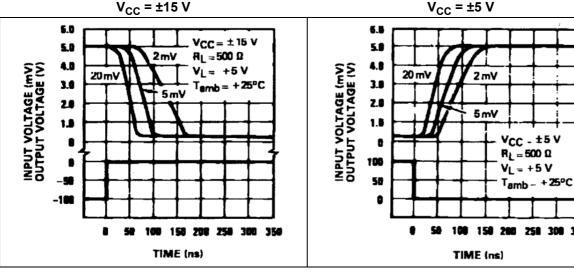


Figure 14. Response time on falling edge,

Figure 16. Response time on falling edge,

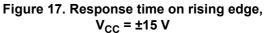
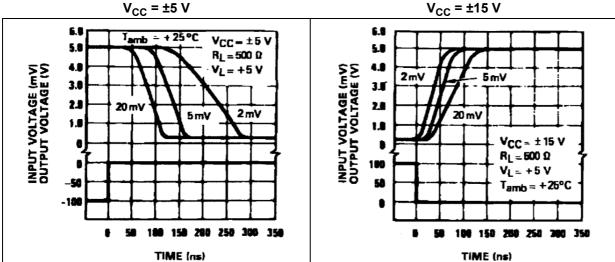


Figure 15. Response time on rising edge,





# 4 Typical application diagrams

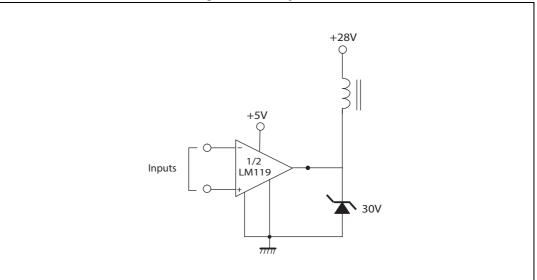
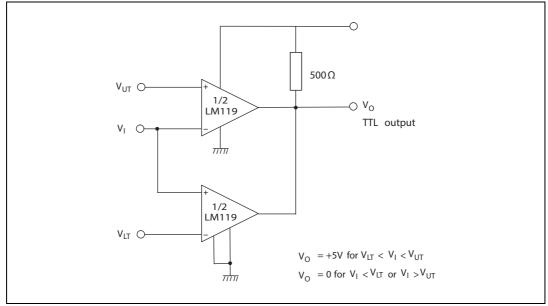


Figure 18. Relay driver

## Figure 19. Window detector



# 5 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK<sup>®</sup> is an ST trademark.

## 5.1 DIP14 package information

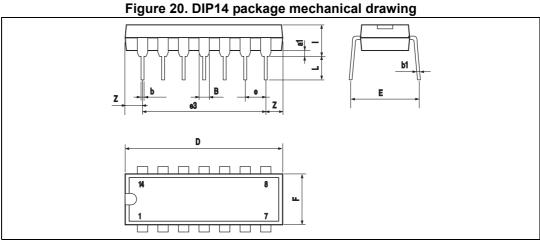
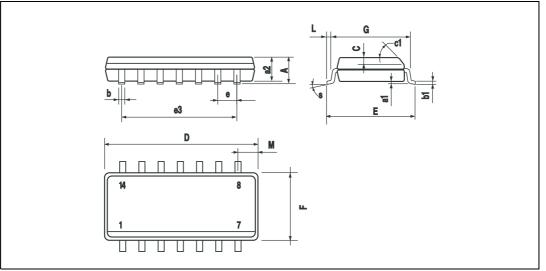


	Table 4. DIP14 package mechanical data						
			Dime	nsions			
Ref.		Millimeters			Inches		
	Min.	Тур.	Max.	Min.	Тур.	Max.	
a1	0.51			0.020			
В	1.39		1.65	0.055		0.065	
b		0.5			0.020		
b1		0.25			0.010		
D			20			0.787	
Е		8.5			0.335		
е		2.54			0.100		
e3		15.24			0.600		
F			7.1			0.280	
I			5.1			0.201	
L		3.3			0.130		
Z	1.27		2.54	0.050		0.100	



# 5.2 SO-14 package information



## Table 5. SO-14 package mechanical drawing

## Table 6. SO-14 package mechanical data

			Dime	nsions		
Ref.		Millimeters			Inches	
	Min.	Тур.	Max.	Min.	Тур.	Max.
А			1.75			0.068
a1	0.1		0.2	0.003		0.007
a2			1.65			0.064
b	0.35		0.46	0.013		0.018
b1	0.19		0.25	0.007		0.010
С		0.5			0.019	
c1			45°	(typ.)		•
D	8.55		8.75	0.336		0.344
E	5.8		6.2	0.228		0.244
е		1.27			0.050	
e3		7.62			0.300	
F	3.8		4.0	0.149		0.157
G	4.6		5.3	0.181		0.208
L	0.5		1.27	0.019		0.050
М			0.68			0.026
S			8° (1	max.)		



## **Ordering information** 6

Order code	Temperature range	Package	Packaging	Marking
LM119N		DIP14	Tube	LM119N
LM119D LM119DT	-55 °C to +125 °C	SO-14	Tube or Tape and reel	119
LM219N		DIP14	Tube	LM219N
LM219D LM219DT	-45 °C to +105 °C	SO-14	Tube or Tape and reel	219
LM319N		DIP14	Tube	LM319N
LM319D LM319DT	0 °C to +70 °C	SO-14	Tube or Tape and reel	319

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### **Revision history** 7

## Figure 22. Document revision history

Date	Revision	Changes
5-Jul-2002	1	Initial release.
28-Jan-2007	2	Added ESD, R <sub>thja</sub> parameters in <i>Table 1: Absolute maximum ratings</i> ( <i>AMR</i> ). Expanded orderable parts table, see <i>Table 21: Order codes</i> . Updated document format.
26-Mar-2013	3	Minimum operating temperature changed from -40 °C to -45 °C. Updated titles of <i>Figure 14</i> , <i>Figure 15</i> , <i>Figure 16</i> , and <i>Figure 17</i> .

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