# SPECIFICATION FOR APPROVAL

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(	(	Fina	I Specif	fication	

Title		18.5" HD TFT LCD			
BUYER	DELL	SUPPLIER	LG Display Co., Ltd.		
MODEL		*MODEL	LM185WH2		
		SUFFIX	TLE1		

<sup>\*</sup>When you obtain standard approval, please use the above model name without suffix

SIGNATURE	DATE
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Please return 1 copy for yo With your signature and co	our confirmatio

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# **Contents**

No		ITEM		
		COVER	1	
		CONTENTS	2	
		RECORD OF REVISIONS	3	
1		GENERAL DESCRIPTION	4	
2		ABSOLUTE MAXIMUM RATINGS	5	
3		ELECTRICAL SPECIFICATIONS	6	
	1)	ELECTRICAL CHARACTERISTICS	6	
	2)	INTERFACE CONNECTIONS	9	
	3)	LVDS characteristics	12	
	4)	SIGNAL TIMING SPECIFICATIONS	15	
	5)	SIGNAL TIMING WAVEFORMS	16	
	6)	COLOR INPUT DATA REFERNECE	17	
	7)	POWER SEQUENCE	18	
	8)	POWER DIP CONDITION	19	
4		OPTICAL SFECIFICATIONS	20	
5		MECHANICAL CHARACTERISTICS	25	
6		RELIABILITY	27	
7		INTERNATIONAL STANDARDS	28	
	1)	SAFETY	28	
	2)	EMC	28	
8		PACKING	29	
	1)	DESIGNATION OF LOT MARK	29	
	2)	PACKING FORM	29	
9		PRECAUTIONS	30	
	1)	MOUNTING PRECAUTIONS	30	
	2)	OPERATING PRECAUTIONS	30	
	3)	ELECTROSTATIC DISCHARGE CONTROL	31	
	4)	PRECAUTIONS FOR STRONG LIGHT EXPOSURE	31	
	5)	STROAGE	31	
	6)	HANDLING PRECAUTIONS FOR PROTECTION FILM	31	



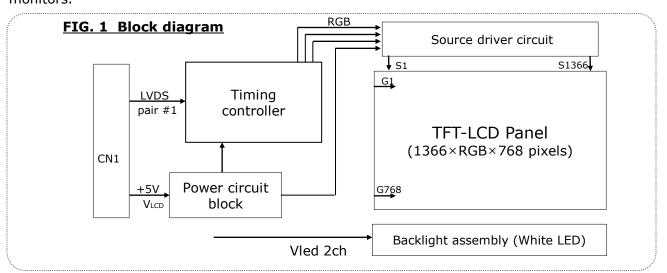
# **Record of revisions**

<b>Revision No</b>	Date	Page	Description
Ver 0.0	FEB.21.2012	-	Preliminary Specifications
Ver 0.1	APR. 18.2012	4 19 22 29	Updated LCM's Weight ( 1350g ) Updated LDM's optical data ( color coordinates) Change TCO5.0 Test Conditions Updated Packing information
Ver 0.2	May. 07. 2012	26	Updated LCM's drawing
Ver 0.2	Jul. 17. 2012	- 10 19 26 30	Final specification Add comment ( Wire locking type) Update color coordinate (Rx, Gy, By) Update LCM's drawing Change the Operating precautions → 3: (In Higher temperature, it becomes lower.)



### 1. General description

LM185WH2-TLE1 is a Color Active Matrix Liquid Crystal Display with a Light Emitting Diode(LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. It has a 18.5 inch diagonally measured active display area with HD resolution (768 vertical by 1366 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16,7M colors with Advanced-FRC(Frame Rate Control). It has been designed to apply the interface method that enables low power, high speed, low EMI. FPD Link or compatible must be used as a LVDS(Low Voltage Differential Signaling) chip. It is intended to support applications where thin thickness, wide viewing angle, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LM185WH2-TLE1 characteristics provide an excellent flat panel display for office automation products such as monitors.



#### **General features**

General leatures	
Active screen size	18.51 inches (470.1mm) diagonal
Outline Dimension	430.4(H) x 254.6(V) x 10.2(D) mm(Typ.)
Pixel Pitch	0.10*RGB(H)mm x 0.30(V)mm
Pixel Format	1366 horizontal By 768 vertical Pixels. RGB stripe arrangement
Interface	LVDS 1Port
Color depth	16.7M colors
Luminance, white	250 cd/m² ( Center 1Point, typ)
Viewing Angle (CR>10)	R/L 170(Typ.), U/D 160(Typ.)
Power Consumption	Total 8.60W(Typ.), (3.60 W@V <sub>LCD</sub> , 5.0W@IBL = 75 mA)
Weight	1350g(Typ)
Display operating mode	Transmissive mode, Normally White
Surface treatments	Hard coating (3H), Anti-glare treatment of the front polarizer



## 2. Absolute maximum ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

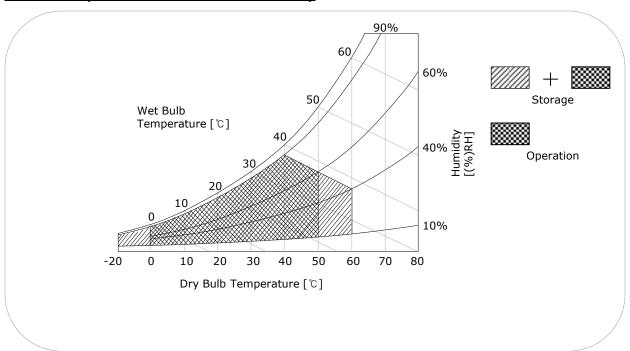
**Table 1. Absolute maximum ratings** 

Parameter	Symbol	Symbol		Units	Notes	
Tarameter	Symbol	Min	Max	Offics	Notes	
Power Supply Input Voltage	$V_{LCD}$	-0.3	+6.0	Vdc	At 25℃	
Operating Temperature	T <sub>OP</sub>	0	50	°C		
Storage Temperature	T <sub>ST</sub>	-20	60	°C	1,2,3	
Operating Ambient Humidity	H <sub>OP</sub>	10	90	%RH		
LCM Surface Temperature (Operation)	$T_{Surface}$	0	65	°C	1,4	

Note: 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C Max, and no condensation of water.

- 2. Maximum Storage Humidity is up to 40  $^{\circ}\text{C}\text{, }90\%$  RH only for 4 corner light leakage Mura.
- 3. Storage condition is guaranteed under packing condition.
- 4. LCM Surface Temperature should be Min.  $0^{\circ}$  and Max.  $65^{\circ}$  under the VLCD=5.0V, fV=60Hz,  $25^{\circ}$  ambient Temperature no humidity control and LED string current is typical value.

FIG. 2 Temperature and relative humidity





## 3. Electrical specifications

### 3-1. Electrical characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the LED/Backlight, is typically generated by a LED Driver. The LED Driver is an external unit to the LCDs.

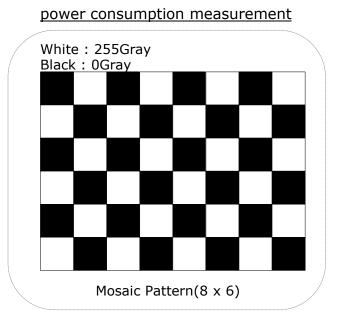
Table 2. Electrical characteristics

Parameter	Cymbol	Values			Lloit	Notes	
Parameter	Symbol	Min	Тур	Max	Unit	notes	
MODULE:							
Power Supply Input Voltage	V <sub>LCD</sub>	4.5	5.0	5.5	Vdc		
Permissive Power Input Ripple	V <sub>LCD</sub>	_	-	0.3	V	3	
Dower Cupply Input Current	I <sub>LCD-MOSAIC</sub>	-	720	940	mA	1	
Power Supply Input Current	I <sub>LCD-BLACK</sub>	_	900	1170	mA	2	
Power Consumption	P <sub>LCD</sub>	-	3.60	4.70	Watt	1	
Inrush current	I <sub>RUSH</sub>	-	-	3.0	А	4	

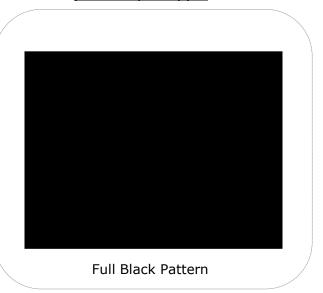
#### Note:

- 1. The specified current and power consumption are under the VLCD=5.0V,  $25 \pm 2^{\circ}\text{C}$ ,  $f_V$ =60Hz condition whereas mosaic pattern(8 x 6) is displayed and  $f_V$  is the frame frequency.
- 2. The current is specified at the maximum current pattern.
- 3. Permissive power ripple should be measured under VCC=5.0V,  $25^{\circ}C$ ,  $f_V$  (frame frequency)=75Hz condition and At that time, we recommend the bandwidth configuration of oscilloscope is to be under 20MHz.
- 4. The duration of rush current is about 5ms and rising time of power Input is 500us  $\pm$  20%.

#### FIG.3 pattern for Electrical characteristics



#### power input ripple





### **Table 3. LED array ELECTRICAL CHARACTERISTICS**

Parameter	Symbol	Condition		Unit	Note		
Parameter	Зуппол		Min.	Тур.	Max.		S
LED String Current	Is		-	75	80	mA	1,2,5
LED String Voltage	Vs		30.8	33	35.2	V	1,5
Power Consumption	PBar		-	5.0	5.3	Watt	1,2,4
LED Life Time	LED_LT		30,000	-		Hrs	3

Notes) The LED Bar consists of 22LED packages, 2 strings (parallel) x 11 packages (serial)

#### LED driver design guide

: The design of the LED driver must have specifications for the LED in LCD Assembly. The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED driver.

So all the parameters of an LED driver should be carefully designed and output current should be Constant current control.

Please control feedback current of each string individually to compensate the current variation among the strings of LEDs.

When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the LED driver (no lighting, flicker, etc) never occurs.

When you confirm it, the LCD module should be operated in the same condition as installed in your instrument.

- 1. Specified values are for a single LED bar.
- 2. The specified current is defined as the input current for a single LED string with 100% duty cycle.
- 3. The LED life time is defined as the time when brightness of LED packages become 50% or less than the initial value under the conditions at  $Ta = 25\pm2^{\circ}C$  and LED string current is typical value.
- 4. The power consumption shown above does not include loss of external driver. The typical power consumption is calculated as  $P_{Bar} = Vs(Typ.) \times Is(Typ.) \times No.$  of strings. The maximum power consumption is calculated as  $P_{Bar} = Vs(Max.) \times Is(Typ.) \times No.$  of strings.
- 5. LED operating conditions are must not exceed Max. ratings.



## 3-2. Interface connections

LCD Connector(CN1): GT103-30S-HF15-E2500(LSM) or IS100-L300-C23 (UJU)

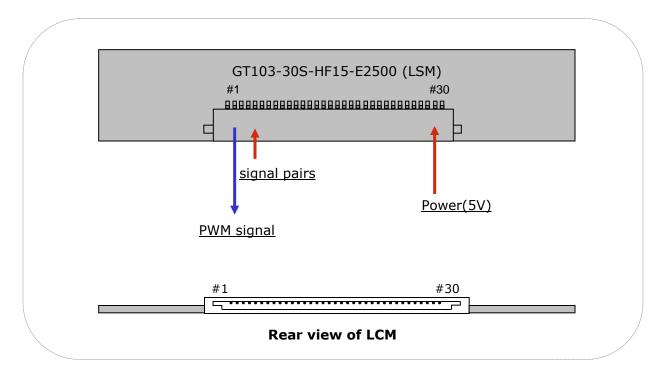
Mating connector: FI-X30H and FI-X30HL (JAE) or Equivalent

Table 4. Module connector(CN1) pin configuration

Din No	Cymahal	Description
Pin No	Symbol	Description
1	NC	No Connection (For LCD internal use only.)
2	PWM_OUT	Reference signal for inverter control
3	NC	No Connection (For LCD internal use only.)
4	GND	Ground
5	RX0-	Minus signal of channel 0 (LVDS)
6	RX0+	Plus signal of channel 0 (LVDS)
7	GND	Ground
8	RX1-	Minus signal of channel 1 (LVDS)
9	RX1+	Plus signal of channel 1 (LVDS)
10	GND	Ground
11	RX2-	Minus signal of channel 2 (LVDS)
12	RX2+	Plus signal of channel 2 (LVDS)
13	GND	Ground
14	RXCLK-	Minus signal of clock channel (LVDS)
15	RXCLK+	Plus signal of clock channel (LVDS)
16	GND	Ground
17	RX3-	Minus signal of channel 3 (LVDS)
18	RX3+	Plus signal of channel 3 (LVDS)
19	GND	Ground
20	NC	No Connection (For LCD internal use only.)
21	NC	No Connection (For LCD internal use only.)
22	NC	No Connection (For LCD internal use only.)
23	GND	Ground
24	GND	Ground
25	GND	Ground
26	VLCD	Power Supply (5.0V)
27	VLCD	Power Supply (5.0V)
28	VLCD	Power Supply (5.0V)
29	VLCD	Power Supply (5.0V)
30	VLCD	Power Supply (5.0V)



### FIG. 4 Connector diagram



#### Note:

- 1. NC: No Connection.
- 2. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.
- 3. All  $V_{ICD}$  (power input) pins should be connected together.
- 4. Input Level of LVDS signal is based on the IEA 664 Standard.
- 5. PWM\_OUT is a reference signal for inverter control.

  This PWM signal is synchronized with vertical frequency.

  Its frequency is 3 times of vertical frequency, and its duty ratio is 50%.

  If the system don't use this pin, do not connect.

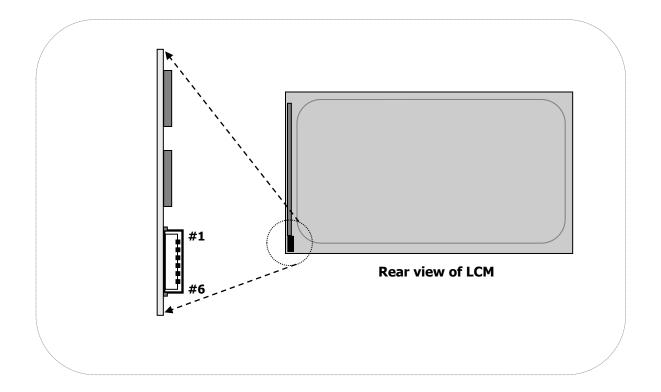


### Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION

The LED interface connector is a model SM06B-SHJH(HF), Wire locking Type manufactured by JST. The mating connector is a SHJP-06V-S(HF) or or SHJP-06V-A-K(HF) and Equivalent.

The pin configuration for the connector is shown in the table below.

Pin	Symbol	Description	Notes
1	FB1	Channel1 Current Feedback	
2	NC	No connection	
3	VLED	LED Power Supply	
4	VLED	LED Power Supply	
5	NC	No connection	
6	FB2	Channel2 Current Feedback	

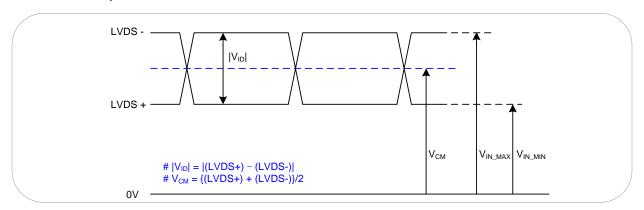


[ Figure 5 ] Backlight connector view



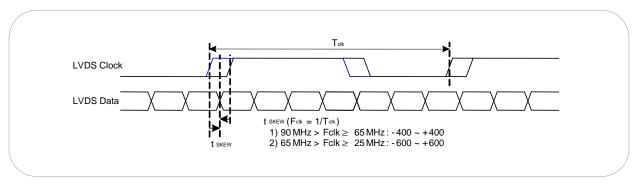
## 3-3. LVDS characteristics

## 3-3-1. DC Specification



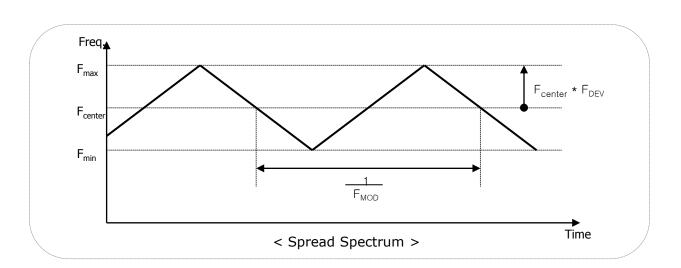
Description	Symbol	Min	Max	Unit	Notes
LVDS Differential Voltage	V <sub>ID</sub>	200	600	mV	-
LVDS Common mode Voltage	V <sub>CM</sub>	0.6	1.8	V	-
LVDS Input Voltage Range	V <sub>IN</sub>	0.3	2.1	V	-

# 3-3-2. AC Specification



Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t <sub>SKEW</sub>	- 400	+ 400	ps	90MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t <sub>SKEW</sub>	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
Maximum deviation of input clock frequency during SSC	F <sub>DEV</sub>	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F <sub>MOD</sub>	-	200	KHz	-





### 3-3-3. LVDS Data format

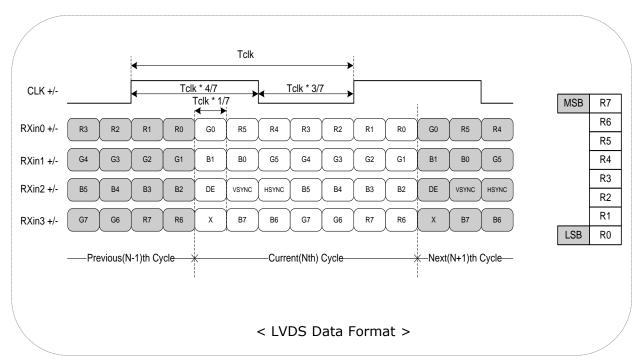




Table 6. Required signal assignment for Flat Link(NS:DS90CF383) transmitter

Pin #	Pin Name	Require Signal	Pin #	Pin Name	Require Signal
1	VCC	Power Supply for TTL Input	29	GND	Ground pin for TTL
2	D5	TTL Input (R7)	30	D26	TTL Input (DE)
3	D6	TTL Input (R5)	31	T <sub>X</sub> CLKIN	TTL Level clock Input
4	D7	TTL Input (G0)	32	PWR DWN	Power Down Input
5	GND	Ground pin for TTL	33	PLL GND	Ground pin for PLL
6	D8	TTL Input (G1)	34	PLL VCC	Power Supply for PLL
7	D9	TTL Input (G2)	35	PLL GND	Ground pin for PLL
8	D10	TTL Input (G6)	36	LVDS GND	Ground pin for LVDS
9	VCC	Power Supply for TTL Input	37	TxOUT3+	Positive LVDS differential data output 3
10	D11	TTL Input (G7)	38	TxOUT3 -	Negative LVDS differential data output 3
11	D12	TTL Input (G3)	39	T <sub>X</sub> CLKOUT+	Positive LVDS differential clock output
12	D13	TTL Input (G4)	40	T <sub>X</sub> CLKOUT –	Negative LVDS differential clock output
13	GND	Ground pin for TTL	41	T <sub>X</sub> OUT2+	Positive LVDS differential data output 2
14	D14	TTL Input (G5)	42	T <sub>X</sub> OUT2 –	Negative LVDS differential data output 2
15	D15	TTL Input (B0)	43	LVDS GND	Ground pin for LVDS
16	D16	TTL Input (B6)	44	LVDS VCC	Power Supply for LVDS
17	VCC	Power Supply for TTL Input	45	T <sub>X</sub> OUT1+	Positive LVDS differential data output 1
18	D17	TTL Input (B7)	46	T <sub>X</sub> OUT1 –	Negative LVDS differential data output 1
19	D18	TTL Input (B1)	47	T <sub>X</sub> OUT0+	Positive LVDS differential data output 0
20	D19	TTL Input (B2)	48	T <sub>X</sub> OUT0 –	Negative LVDS differential data output 0
21	GND	Ground pin for TTL Input	49	LVDS GND	Ground pin for LVDS
22	D20	TTL Input (B3)	50	D27	TTL Input (R6)
23	D21	TTL Input (B4)	51	D0	TTL Input (R0)
24	D22	TTL Input (B5)	52	D1	TTL Input (R1)
25	D23	TTL Input (RSVD)	53	GND	Ground pin for TTL
26	VCC	Power Supply for TTL Input	54	D2	TTL Input (R2)
27	D24	TTL Input (HSYNC)	55	D3	TTL Input (R3)
28	D25	TTL Input (VSYNC)	56	D4	TTL Input (R4)

Notes: Refer to LVDS Transmitter Data Sheet for detail descriptions.



## 3-4. Signal timing specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

## **Table 7. Timing table**

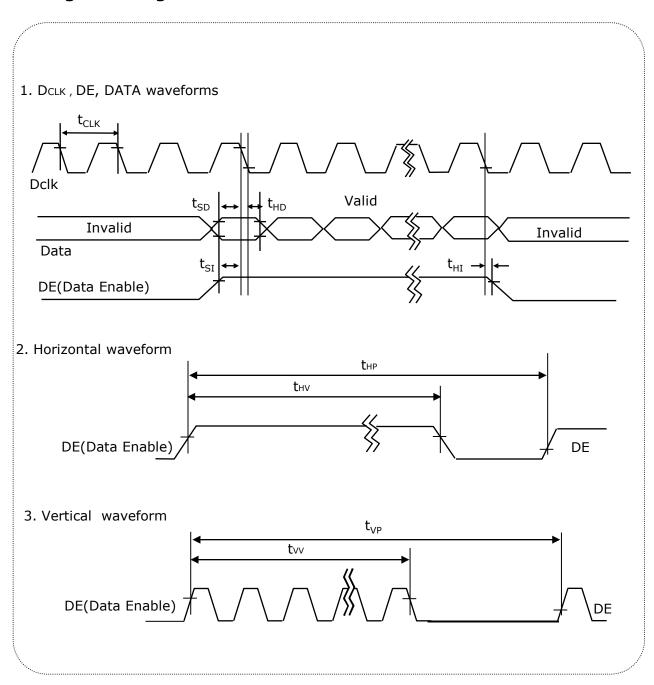
Par	ameter	Symbol	Min.	Тур.	Max.	Unit	Notes
D	Period	t <sub>CLK</sub>	11.1	13.0	16.2	ns	
D <sub>CLK</sub>	Frequency	f <sub>CLK</sub>	61.6	77.0	90.0	MHz	
	Horizontal Valid	t <sub>HV</sub>	1366	1366	1366	_	
Horizontal	H Period Total	t <sub>HP</sub>	1430	1608	2044	t <sub>CLK</sub>	
	Hsync Frequency	f <sub>H</sub>	38.3	47.9	62.0	kHz	
	Vertical Valid	t <sub>vv</sub>	768	768	768	_	
Vertical	V Period Total	t <sub>VP</sub>	776	798	1108	t <sub>HP</sub>	
	Vsync Frequency	f <sub>V</sub>	48	60	76	Hz	
DE	DE Setup Time	t <sub>SI</sub>	4	-	-		For D
(Data Enable)	DE Hold Time	t <sub>HI</sub>	4	-	-	ns	For D <sub>CLK</sub>
Data	Data Setup Time	t <sub>SD</sub>	4	-	-		For D
Data	Data Hold Time	t <sub>HD</sub>	4	-	-	ns	For D <sub>CLK</sub>

#### Note:

- 1. DE Only mode operation. The input of Hsync & Vsync signal does not have an effect on LCD normal operation.
- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
- 3. Horizontal period should be even.



## 3-5. Signal timing waveforms





## 3-6. Color input data reference

The brightness of each primary color (red,green and blue) is based on the 8bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 8. Color data reference

	bie 8. Color data																								
											Inp	out	Co	lor	Da	ata									
	Color				Re	ed							Gre	een							Bl	ue			
	Color							LS	SB	М	SB					LS	SB	Μ	SB					LS	B
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	В4	В3	B2	В1	В0
Basic Color	Black Red (255) Green (255) Blue (255) Cyan Magenta Yellow White	0 1 0 0 1 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1	0 1 0 0 1 1	0 1 0 0 1 1	0 0 1 0 1 0 1	0 1 0 1 0 1	0 0 1 0 1 0 1	0 0 1 0 1 0 1	0 0 1 0 1 0 1	0 0 1 0 1 0 1	0 0 1 0 1 0 1	0 0 1 0 1 0 1	0 0 1 1 1 0 1	0 0 1 1 1 0 1	0 0 1 1 0 1	0 0 1 1 0 1	0 0 1 1 0 1	0 0 1 1 0 1	0 0 1 1 0 1	0 0 1 1 1 0 1
Red	Red(000) Dark Red(001) Red(002) Red(253) Red(254) Red(255) Bright	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 1 - 0 1 1	0 1 0 - 1 0 1	000 000	0 0 0 - 0 0 0	000 000	000 000	000 000	000 000	000 000	000 000	0 0 0 - 0 0 0	0 0 0 - 0 0	000000	000 000	000 000	000 000	0 0 0 0 0 0	0 0 - - 0 0
Green	Green(000) Dark Green(001) Green(002)  Green(253) Green(254) Green(255)Bright	000 000	000 000	000000	000000	000 000	0 0 0 0 0 0	0 0 0 - 0 0 0	000000	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 1 - 0 1	0 1 0 - 1 0 1	0 0 0 - 0 0 0	0 0 0 - 0 0	000000	000 000	000 000	000 000	0 0 0 0 0 0	0 0 0 - 0 0
Blue	Blue(000) Dark Blue(001) Blue(002) Blue(253) Blue(254) Blue(255) Bright	000 000	000 000	0 0 0 0 0 0	000 000	000 000	000 000	0 0 0 - 0 0 0	000 000	000 000	0 0 0 0 0 0	000 000	000 000	000 000	000 000	000 000	000 000	0 0 - - 1 1	0 0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 1 - 0 1 1	0 1 0 - 1 0 1



## 3-7. Power sequence

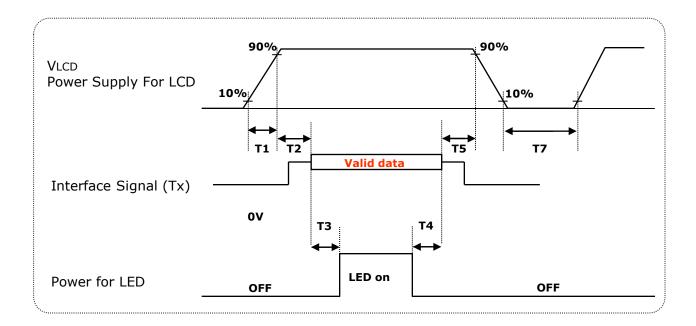


Table 9. Power sequence

Darameter		Unite		
Parameter	Min	Тур	Max	Units
T1	0.5	-	10	ms
T2	0.01	-	50	ms
Т3	500	-	-	ms
T4	200	-	-	ms
T5	0.01	-	50	ms
T7	1	-	-	S

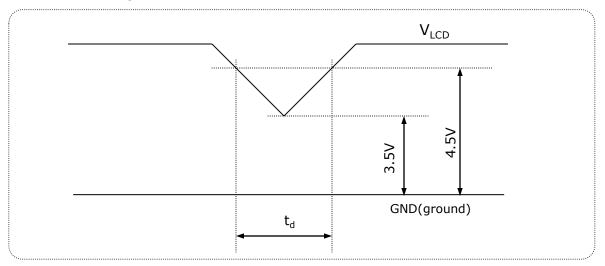
#### Notes:

- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD  $V_{\text{LCD}}$  to 0V.
- 3. LED power must be turn on after power supply for LCD an interface signal are valid.



# 3-8. $V_{LCD}$ Power dip condition

## FIG. 6 Power dip condition



1) Dip condition

$$3.5V \le V_{LCD} \le 4.5V$$
 ,  $t_d \le 20ms$ 

2) 
$$V_{LCD}$$
< 3.5V

 $V_{\text{LCD}}\text{-dip}$  conditions should also follow the Power On/Off conditions for supply voltage.



## 4. Optical specification

Optical characteristics are determined after the unit has been 'ON' for 30 minutes in a dark environment at  $25^{\circ}$ C.

Table 10. Optical characteristics

Ta= 25°C,  $V_{LCD}$ =5.0V, fV=60Hz  $f_{CLK}$ = 77.0MHz,  $I_{BL}$ =75mA

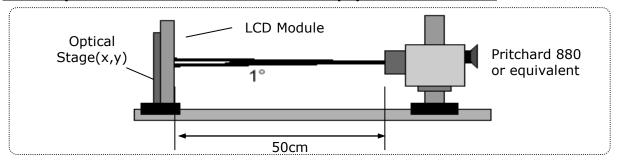
TUBIC 101 C	peic	ai Cilai actei	.50.05		1a= 25°C,	$V_{LCD}=5.0V$ , f	V=60Hz f <sub>CLK</sub> =	= 77.0MHz,	IBL=75mA
Dox	ame	tor	Symb	nol.		Values		Units	Notes
Pai	anne	iter	Syllic	וטנ	Min	Тур	Max	Units	Notes
Contrast Ratio			CR		600	1000	-		1 (PR-880)
Surface Lumin	ance	e, white	L <sub>W</sub>	1	200	250	-	cd/m <sup>2</sup>	2 (PR-880)
Luminance Va	iatio	on	δ <sub>WHITE</sub> 9P		75			%	3 (PR-880)
Docnonco Timo		Rise Time	Tr <sub>R</sub>	l	-	1.1	2.6	ms	4
Response Time	3	Decay Time	Tr <sub>D</sub>	)	-	3.9	7.4	ms	(RD80S)
		RED	Rx			0.643			
		KED	Ry			0.333			
		CDEEN	Gx			0.318			
Color Coordina	tes	GREEN	Gy		Тур	0.623	Тур		(PR-650)
[CIE1931]		BLUE	Bx		-0.03	0.152	+0.03		(FIX-030)
		DLUL	Ву			0.066			
		WHITE	Wx	[		0.313			
		WILL C	Wy	'		0.329			
Viewing Angle	(CR	>5)							
x axi	s, ri	ght(φ=0°)	θr		75	88		Degree	
x axi	s, le	ft (φ=180°)	θΙ		75	88			
y axi	s, u	ρ (φ=90°)	θu		70	85			
у ах	is, d	lown (φ=270°)	θd		70	85			5
Viewing Angle	(CR	>10)							(PR-880)
x axi	s, ri	ght(φ=0°)	θr		70	85		Degree	
x axi	s, le	ft (φ=180°)	θΙ		70	85			
y axi	s, u	ρ (φ=90°)	θu		60	75			
y axi	s, d	own (φ=270°)	θd		70	85			
Crosstalk							1.5	%	(PR880)
Luminance uni Angular depen	nance uniformity - lar dependence (TCO 5.0)		LR		-	-	1.73		6 (PR880)
Color grayscal	e lin	earity	Δu′\	<i>'</i>		0.018			8 (PR-650)



The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to 0 °.

FIG. 7 presents additional information concerning the measurement equipment and method.

#### FIG. 7 Optical characteristic measurement equipment and method



#### Notes:

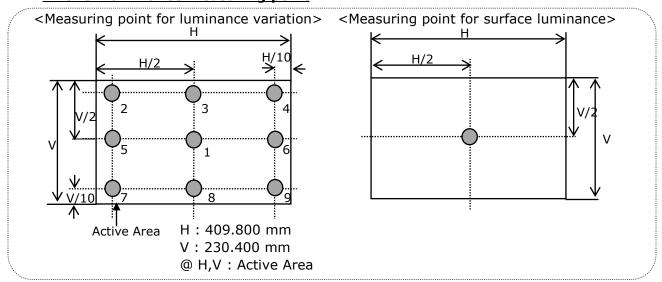
Contrast ratio(CR) is defined mathematically as :It is measured at center point(1)

- Surface luminance is the luminance value at center 1 point(1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 8.
- 3. The variation in surface luminance ,  $\delta$   $_{\text{WHITE}}$  is defined as

$$\delta_{\text{WHITE}} = \frac{\text{Maximum (P1,P2 .....P9)}}{\text{Minimum (P1,P2 .....P9)}}$$

For more information see Figure 8.

#### FIG. 8 Luminance measuring point





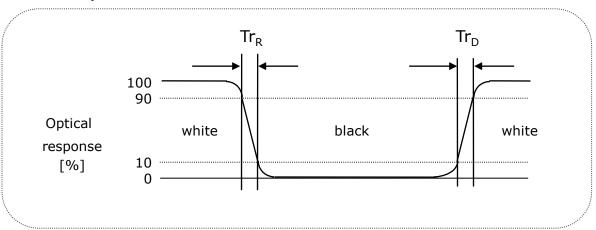
#### Notes:

4. Response time is the time required for the display to transition from black to white (Decay Time,  $Tr_D$ ) and from white to black (Rise Time,  $Tr_R$ )

The sampling rate is 2,500 sample/sec. For additional information see FIG. 9.

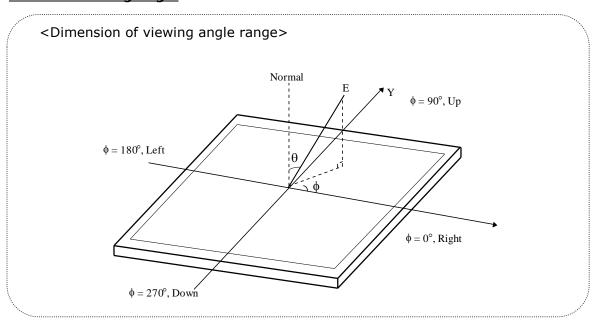
The response time is defined as the following figure and shall be measured by switching the input signal for each gray to gray.

FIG. 9 Response time



5. Viewing angle is the angle at which the contrast ratio is greater than 10 or 5. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG. 10.

#### FIG. 10 Viewing angle





#### Notes:

6. Luminance Uniformity - angular - dependence (LR& TB)

TCO 5.0 Luminance uniformity – angular dependence, is the capacity of the VDU to present the same Luminance level independently of the viewing direction. The angular-dependent luminance uniformity is calculated as the ratio of maximum luminance to minimum luminance in the specified measurement areas.

- Test pattern  $\,$ : Full white  $4^{\circ} \times 4^{\circ}$  square size, back ground shall be set to 80%

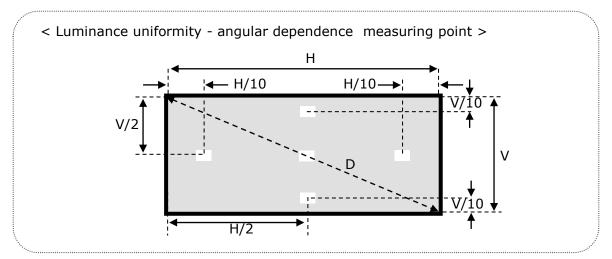
image loading, RGB 204, 204, 204

Test luminance : ≥150cd/m²Test point : 5-point

- Test distance : D \* 1.5 = 70.52cm

- Test method :  $L_R = ((L_{max.+30deg.} / L_{min. +30deg.}) + (L_{max. -30deg.} / L_{min. -30deg.})) / 2$  $T_B = ((L_{max.+15deg.} / L_{min. +15deg.})$ 

### FIG. 11 Luminance Uniformity angular dependence



#### 7. Gray scale specification

Table 11. Gray scale

Gray level	Luminance [%] (Typ)
L0	0.10
L31	0.97
L63	4.43
L95	11.06
L127	21.13
L159	34.88
L191	54.53
L223	77.00
L255	100



#### Notes:

8. Color grayscale linearity,  $\Delta u'v'$  is defined as

$$\sqrt{(u'_A - u'_B)^2 + (v'_A - v'_B)^2}$$

Where indices A and B are the two gray levels found to have the largest color differences between them.

i.e. get the largest  $\Delta u'$  and  $\Delta v'$  of each 6pairs of u' and v' and calculate  $\Delta u'v'$ .

#### -Test pattern:

100% full white pattern with a test pattern as shown FIG.12 Squares of 40mm by 40mm in size, filled with 255, 225, 195, 165, 135 and 105 grayscale steps should be arranged in the center of the screen.

#### -Test method:

### First gray step:

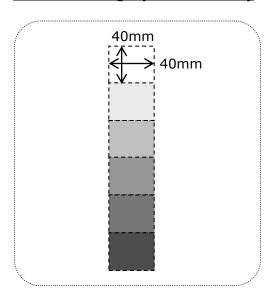
Move a square of 255 gray level should be moved into the center of the screen and measure luminance and u' and v' coordinates.

### Next gray step:

Move a 255 gray square into the center and measure both luminance and u' and v' coordinates.

The same procedure shall then be repeated for gray steps 195, 165, 135 and 105.

#### FIG. 12 Color grayscale linearity



Ver. 1.0 JUL. 17. 2012 23 / 31



## 5. Mechanical characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

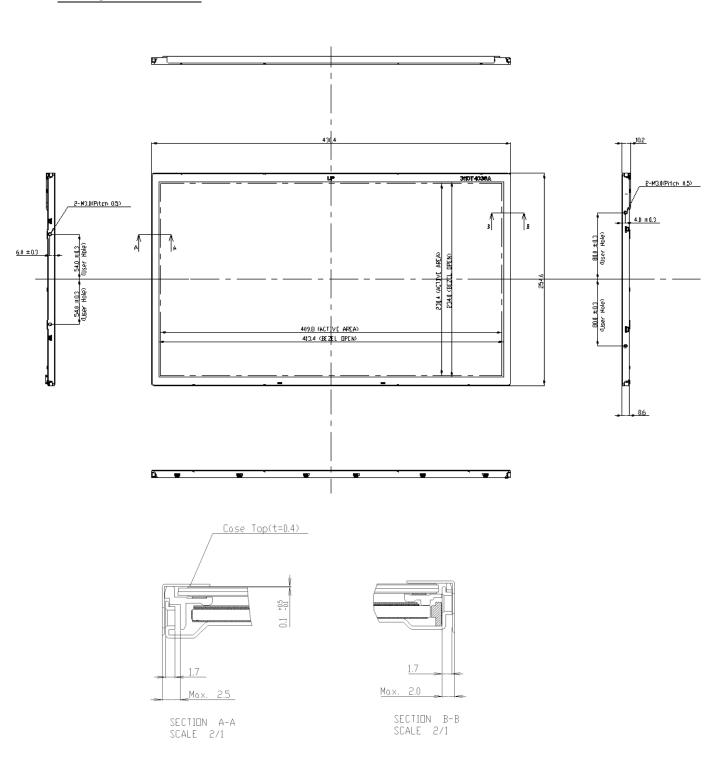
**Table 12. Mechanical characteristics** 

	Horizontal	430.4 mm		
Outline dimension	Vertical	254.6 mm		
	Depth	10.20 mm		
Bezel area	Horizontal	413.4 mm		
Dezei ai ea	Vertical	234.0 mm		
Active display area	Horizontal	409.800 mm		
Active display area	Vertical	230.400 mm		
Weight	1350g(Typ), 1420g(Max)			
Surface treatment	Hard coating(3H) Anti-glare treatment of the front	polarizer		

Notes: Please refer to a mechanic drawing in terms of tolerance at the next page.

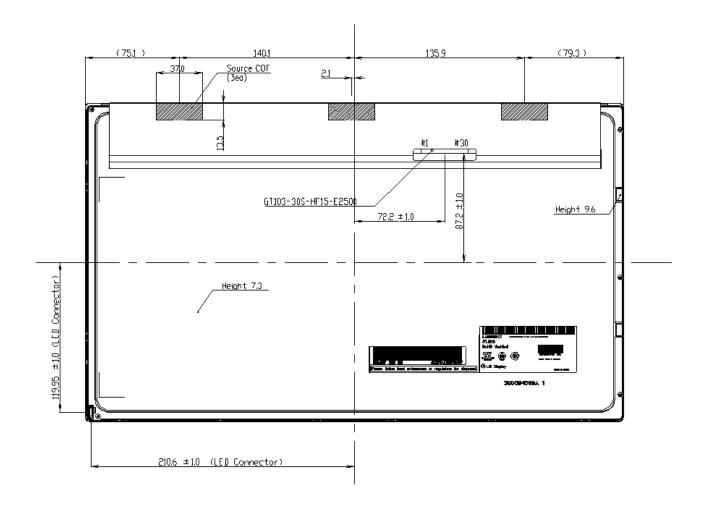


## <FRONT VIEW>





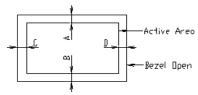
#### <REAR VIEW>



#### Notes

- 1. I/F Connector Specification: GT103-30S-HF15-E2500(LSM)
  2. LED Connector specification
   JST, SM06B-SHJH(HF), 6PIN
  3. Torque of user hole: 2.5-3.5 kgf-cm
  4. Tilt and partial disposition talerance of display area as following

  - (1) Y-Direction:  $|A-B| \le 1.0$ (2) X-Direction:  $|C-D| \le 1.0$



- 5. Unspecified tolerances to be  $\pm$  0.5mm 6. The COF area is weak & sensitive, So, please don't press the COF area.



## 6. Reliability

**Table 13. Environment test conditions** 

No	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240hrs
2	Low temperature storage test	Ta= -20°C 240hrs
3	High temperature operation test	Ta= 50°C 50%RH 240hrs
4	Low temperature operation test	Ta= 0°C 240hrs
5	Vibration test (non-operating)	Wave form: random Vibration level: 1.0GRMS Bandwidth: 10-300Hz Duration: X,Y,Z, 30 min One time each direction
6	Shock test (non-operating)	Shock level: 120G Waveform: half sine wave, 2msec Direction: $\pm X$ , $\pm Y$ , $\pm Z$ One time each direction
7	Altitude operating storage / shipment	0 - 10,000 feet(3,048m) 0 - 40,000 feet(12,192m)

<sup>{</sup> Result evaluation criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



#### 7. International standards

### 7-1. Safety

- a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc., Standard for Safety of Information Technology Equipment.
- b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association, Standard for Safety of Information Technology Equipment.
- c) EN 60950-1:2001, First Edition, European Committee for Electro-technical Standardization(CENELEC) European Standard for Safety of Information Technology Equipment.
- d) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

#### 7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI),1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electro-technical Standardization.(CENELEC), 1998 (Including A1: 2000)



## 8. Packing

## 8-1. Designation of lot mark

a) Lot mark

А	В	С	D	Е	F	G	Н	I	J	K	L	М	
---	---	---	---	---	---	---	---	---	---	---	---	---	--

A,B,C : Size (Inch) D : Year

E: Month  $F \sim M$ : Serial No.

#### Note:

#### 1. Year

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	К

#### 2. Month

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of lot mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

## 8-2. Packing form

a) Package quantity in one box: 12 pcs

b) Box size: 350 mm X 300 mm X 470 mm



#### 9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

### 9-1. Mounting Precautions

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the Module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

## 9-2. Operating precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :  $V=\pm200$ mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In higher temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can not be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw (if not, it causes metal foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.



### 9-3. Electrostatic discharge control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

## 9-4. Precautions for strong light exposure

Strong light exposure causes degradation of polarizer and color filter.

### 9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

  It is recommended that they be stored in the container in which they were shipped.

## 9-6. Handling precautions for protection film

- (1) The protection film is attached to the bezel with a small masking tape.

  When the protection film is peeled off, static electricity is generated between
  the film and polarizer. This should be peeled off slowly and carefully by people who are
  electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.