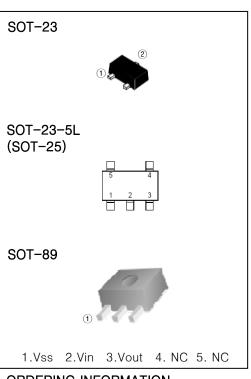
LM1108F/SF

FEATURES

- Extremely Low Supply Current(50µA,Typ.)
- Very Low Dropout Voltage
- 300mA Output Current
- High Output Voltage Accuracy +/- 1.4 %
- Standardor Custom Output Voltages
- Over Currentand Over Temperature Protection
- Small input/output differential : 0.3V for full load

APPLICATIONS

- Battery Operated Systems
- Portable Computers
- Portable Cameras and Video Recorders
- Medical Instruments
- Instrumentation
- Cellular/GSM/PHS Phones
- Linear Post-Regulators for SMPS
- Pagers



ORDERING INFORMATION

| Device | Marking | Package |
|------------|----------|---------|
| LM1108 SF | HXX | SOT-23 |
| LM1108 SF5 | HXX | SOT-25 |
| LM1108 F | 1108 X.X | SOT-89 |

(Note : "XX" is Output Voltage for SOT-23 Pkg, 1.5V=H15, 1.8V=H18....3.3V=H33)

DESCRIPTION

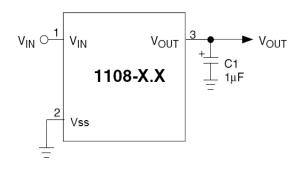
The LM1108 is a fixed output, high accuracy (typically $\pm 0.5\%$) C MOS low drop-out regulator.

Total supply current is typically 50µA at full load (20 to 60 times lower than in bipolar regulators). LM1108 key features include ultra low noise operation, very low dropout voltage (typically 240mV at full load), and fast response to step changes in load.

The LM1108 in corporates both over temperature and over current protection.

The LM1108 is stable with an output capacitor of only1µF and has a maximum out put current of 300mA. It is available in a SOT-23 & SOT-89 package

TYPICAL APPLICATION CIRCUIT



ABSOLUTE MAXIMUM RATING (Note 1)

| Supply Voltage (Vin) | +6.5V |
|--------------------------------------|---------------------|
| Output Current (lout) | 300 mA |
| Output Voltage (Vout) | Vss-0.3 to Vin+0.3V |
| Total Power Dissipation (Pd) SOT-89 | 500 mW |
| SOT-23 | 230 mW |
| Operating Ambient Temperature (Topr) | -30 ~ +80 °C |
| Lead Temperature (soldering, 5 sec) | 260°C |
| Storage Temperature (Tstg) | -40 ~ +125 °C |
| | |

ELECTRICAL CHARACTERISTICS

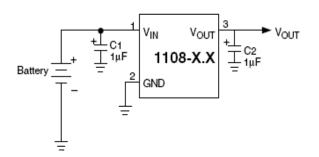
| Parameter | Symbol | Condition | | Limit | | Units | |
|---|------------|--|-----|-------|------|-------|--|
| Parameter Symbol | | Condition | Min | Тур | Max | | |
| Output Voltage Accuracy | Vout | lo=1mA | | | | V | |
| Output voltage Accuracy | | lo=0~300 mA | | | | V | |
| Line Reguration | ∆Vout/∆Vir | lo=1mA, | | | | %/V | |
| Load Reguration(Note.1) | ∆Vout/∆lo | Vin=6V, 0.1mA <lo<300 ma,<="" td=""><td></td><td>0.005</td><td>0.04</td><td>%/mA</td></lo<300> | | 0.005 | 0.04 | %/mA | |
| Maximum Output Current | lo | Vin=5V, Vout >0.96Vrating | 300 | 500 | | mA | |
| Current Limit | | | 400 | | | mA | |
| Ground Current | lgnd | lo=0~300 mA | | 15 | 30 | μA | |
| | | | | | | | |
| Dropout Voltage for | | lo=100 mA | | 100 | 180 | | |
| Vout>2.5V | | lo=300 mA | | 300 | 550 | | |
| 2.0V <vout<2.5v< td=""><td rowspan="4">Varop</td><td>lo=100 mA</td><td></td><td>150</td><td>300</td><td colspan="2">v</td></vout<2.5v<> | Varop | lo=100 mA | | 150 | 300 | v | |
| 2.00<0001<2.50 | | lo=300 mA | | 450 | 800 | mV | |
| Vout<2.0V | | lo=100 mA | | 200 | 400 | | |
| vout<2.0V | | lo=300 mA | | 600 | 1100 | | |

Note.1 : Load Regulation is measured using pulse techniques with duty cycle<5%

Detail Description

- The LM1108 is aprecision, fixed output LDO.

Un like bipolar regulators, the LM1108's supply current does not increase with load current. In addition,VOUT remains stable and with in regulation over theen tire 0mA to IOUT MAX operating load currentrange, (an important consideration in RTC and CMOS RAM battery back-up applications). Figure3-1shows a typical application circuit.



1. Output Capacitor

1uF(min) capacitor from VOUT to ground is required.

The output capacitor hould have an effective series resistance greater than 0.1 Ω and less than .0 Ω . 1uF capacitor should be connected from VIN to GND if there is more than 10 inche sofwire between the regulator and the AC filter capacitor ,or if a battery is used as the power source. Aluminum electrolytic or tantalum capacitor types can be used. (Since many aluminum electrolytic capacitors freeze at approximately -30°C, solid tantalums are recommended for applications operating below -25° C.)

When operating from sources other than batteries, supply-noise rejection and transient response can be improved by increasing the value of the input and output capacitors and employing passive filtering techniques.

2. THERMAL CONSIDERATIONS

2.1 Thermal Shutdown

Integrated thermal protection circuitry shuts the regulator off when die temperature exceeds150°C. The regulator remains off until the die temperature drops to approxi mately 140°C.

2.2 Power Dissipation

The amount of power the regulator dissipate is primarily a function of input and output voltage, and output current. The following equation is used to calculate worst case actual power dissipation:

EQUATION 2-1:

The maximum allowable power dissipation (Equation 2-2) is a function of the maximum ambient temperature(TAMAX), the maximum allowable die temperature(TJMAX) and the thermal resistance from junction-to-air(θ JA).

TABLE 2-1 :

P_D ≈ (V_{INMAX} – V_{OUTMIN})I_{LOADMAX}

Where:

P_D = Worst case actual power dissipation

 V_{INMAX} = Maximum voltage on V_{IN}

 V_{OUTMIN} = Minimum regulator output voltage

ILOADMAX = Maximum output (load) current

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EQUATION 2-2:

$$P_{DMAX} = (\underline{T_{JMAX} - T_{AMAX}}) \\ \theta_{JA}$$
 Where all terms are previously defined.

Table 2-1 shows various values of θ JA for the LM1108 versus board copper area.

TABLE 2-1 : THERMAL RESISTANCE GUIDELINES FOR LM1108

| Copper Area (Topside)* | Copper Area (Backside) | Board Area | Thermal Resistance (θ _{JA}) |
|------------------------------|------------------------------|---------------|---|
| 2500 sq mm | 2500 sq mm | 2500 sq mm | 45°C/W |
| 1000 sq mm | 2500 sq mm | 2500 sq mm | 45°C/W |
| 225 sq mm | 2500 sq mm | 2500 sq mm | 53°C/W |
| 100 sq mm | 2500 sq mm | 2500 sq mm | 59°C/W |
| 1000 sq mm | 1000 sq mm | 1000 sq mm | 52°C/W |
| 1000 sq mm | 0 sq mm | 1000 sq mm | 55°C/W |

NOTE: *Tab of device attached to topside copper

Equation 2-1can be used in conjunction with Equation 2-2 to ensure regulator thermal operation is within limits. Forexample:

Given:

| /en: | Find: 1. Actual power dissipation |
|---------------------------------|--|
| V _{INMAX} = 3.3V + 10% | 2. Maximum allowable dissipation |
| $V_{OUTMIN} = 2.7V - 0.5\%$ | Actual power dissipation: |
| I _{LOADMAX} = 275mA | P _D ≈ (V _{INMAX} – V _{OUTMIN})I _{LOADMAX} |
| T _{JMAX} = 125°C | = [(3.3 x 1.1) – (2.7 x .995)]275 x 10 ⁻³ |
| T _{AMAX} = 95°C | = 260mW |
| $\theta_{JA} = 59^{\circ}C/W$ | Maximum allowable power dissipation: |
| | |

$$P_{DMAX} = \frac{(T_{JMAX} - T_{AMAX})}{\theta_{JA}}$$
$$= \frac{(125 - 95)}{59}$$
$$= 508 \text{mW}$$

In this example, the LM1108 dissipates a maximum of 260mW; below the allow able limit of 508 mW. In a similar manner, Equation 2-1 and Equation 2-2 can be used to calculate maximum current and/or input voltage limits. For example, the maximum allowable VIN, is found by sustituting the maximum allowable power dissipation of 508 mW into Equation 2-1, from which VINMAX = 4.6V.

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