PREPARED BY : DATE		SPEC No. LD-K23147
APPROVED BY : DATE	**SHARP**	FILE No. 20 pages
		ISSUE: Feb, 7, 2011
	LIQUID CRYSTAL DISPLAY GROUP	APPLICABLE GROUP
	SHARP CORPORATION	LIQUID CRYSTAL DISPLAY GROUP
	SPECIFICATION	

DEVICE SPECIFICATION FOR

TFT-LCD module

MODEL No. LK600D3LB08

CUSTOMERS APPROVAL

DATE _____

BY _____

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SHARP CORPORATION

RECORDS OF REVISION

LK600D3LB08

SPEC No.	DATE	REVISED		SUMMARY	NOTE
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1. Application

This specification literature applies to the color 60.0" TFT-LCD module LK600D3LB08.

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2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit, LED drive circuit and back light system etc.   Graphics and texts can be displayed on a 1920×RGB×1080 dots panel with one billion colors by using V-By-One® HS to interface, +12V of DC supply voltages.

This module includes the LED backlight system. (Typ. +122.8V of DC supply voltage)

And in order to improve the response time of LCD, this module applies the Over Shoot driving (O/S driving) technology for the control circuit .In the O/S driving technology, signals are being applied to the Liquid Crystal according to a pre-fixed process as an image signal of the present frame when a difference is found between image signal of the previous frame and that of the current frame after comparing them.

With this technology, image signals can be set so that liquid crystal response completes within one frame. As a result, motion blur reduces and clearer display performance can be realized.

This LCD module also adopts 240Hz Frame Rate driving method.

With combination of these technologies, motion blur can be reduced and clearer display performance can be realized.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	152.496 (Diagonal)	cm
	60.0 (Diagonal)	inch
Active area	1329.12 (H) x 747.63 (V)	mm
Pixel Format	1920 (H) x 1080 (V) (1pixel = R + G + B dot)	pixel
Pixel pitch	0.69225 (H) x 0.69225 (V)	mm
Pixel configuration	R, G, B vertical stripe	
Display mode	Normally black	
Unit Outline Dimensions (*1)	1367 (H) x788.6 (V) x 21.4 (D (S-PWB Area))	mm
Mass	19.8±1	kg
Surface treatment	Low-Haze Anti glare Hard coating: 2H and more	

(*1) Outline dimensions are shown in Fig.1 (excluding protruding portion)

4. Input Terminals

4.1. TFT panel driving

CN1 (Interface signals and +12V DC power supply)

Using connector	: 20519-051E(IPEX)or equivalent connector
Mating connector	: FFC with 51 poles,0.5mm terminal pitch
	and 0.33±0.03mm thickness(exposure conductor)
Mating LVDS transmitter	: THC63LVD1023 or equivalent device

Pin No.	Symbol	Function	Remark
1	Vcc	+12V Power Supply	
2	Vcc	+12V Power Supply	
3	Vcc	+12V Power Supply	
4	Vcc	+12V Power Supply	
5	Vcc	+12V Power Supply	
6	Vcc	+12V Power Supply	
7	Vcc	+12V Power Supply	
8	Vcc	+12V Power Supply	
9	NC	NC	
10	GND		
11	GND		
12	HTPDN	Hot plug detect	Output(Open Drain)
13	LOCKN	Lock detect	Output(Open Drain)
14	GND		
15	Rx0n	V-by-One HS Data Lane0	
16	Rx0p	V-by-One HS Data Lane0	
17	GND		
18	GND		
19	Rx1n	V-by-One HS Data Lane1	
20	Rx1p	V-by-One HS Data Lane1	
21	GND		
22	GND		
23	Rx2n	V-by-One HS Data Lane2	
24	Rx2p	V-by-One HS Data Lane2	
25	GND		
26	GND		
27	Rx3n	V-by-One HS Data Lane3	
28	Rx3p	V-by-One HS Data Lane3	
29	GND		
30	GND		
31	Rx4n	V-by-One HS Data Lane4	
32	Rx4p	V-by-One HS Data Lane4	
33	GND		
34	GND		
35	Rx5n	V-by-One HS Data Lane5	
36	Rx5p	V-by-One HS Data Lane5	
37	GND		
38	GND		
39	Rx6n	V-by-One HS Data Lane6	
40	Rx6p	V-by-One HS Data Lane6	
41	GND		
42	GND		
43	Rx7n	V-by-One HS Data Lane7	
44	Rx7p	V-by-One HS Data Lane7	
45	Reserved	Connect to GND	

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| 46	FRAME	Frame frequency setting (0:200Hz,1:240Hz)	Pull down GND [Note 2]
47	2D/3D	Select 2D/3D mode (0: 2D-mode,1: 3D-mode)	Pull down GND [Note 2]
48	Reserved	It is required to set non-connection(OPEN)	Pull down GND [Note 5]
49	Reserved	It is required to set non-connection(OPEN)	Pull up 3.3V [Note 3]
50	Reserved	It is required to set non-connection(OPEN)	Pull up 3.3V [Note 4]
51	Reserved	It is required to set non-connection(OPEN)	Pull up 3.3V [Note 4]

[Note1]

	Data	30bpp RGB
Byte0	D[0]	R2
	D[1]	R3
	D[2]	R4
	D[3]	R5
	D[4]	R6
	D[5]	R7
	D[6]	R8
	D[7]	R9(MSB)
Byte1	D[8]	G2
	D[9]	G3
	D[10]	G4
	D[11]	G5
	D[12]	G6
	D[13]	G7
	D[14]	G8
	D[15]	G9(MSB)
Byte2	D[16]	B2
	D[17]	B3
	D[18]	B4
	D[19]	B5
	D[20]	B6
	D[21]	B7
	D[22]	B8
	D[23]	B9(MSB)
Byte3	D[24]	
	D[25]	
	D[26]	B0(LSB)
	D[27]	B1
	D[28]	G0(LSB)
	D[29]	G1
	D[30]	R0(LSB)
	D[31]	R1

[Note 2]The equivalent circuit figure of the terminal
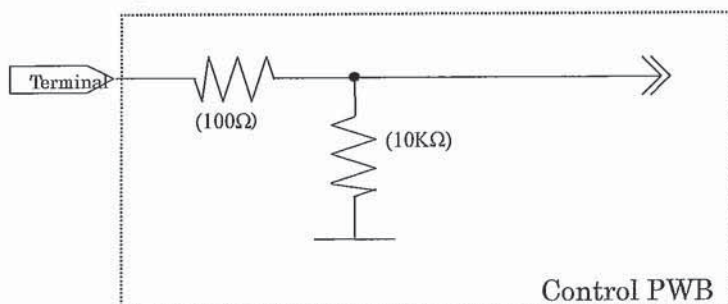
[Note 3]The equivalent circuit figure of the terminal
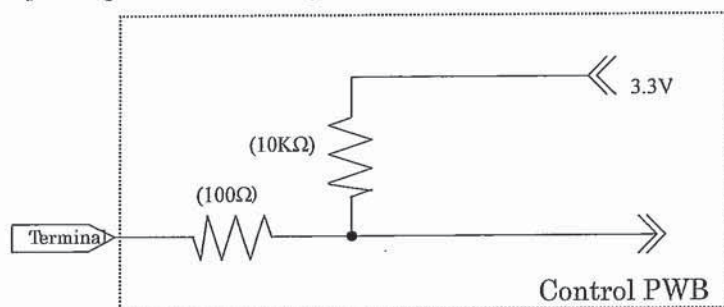
$(10K\Omega)$

(100Ω)

Terminal

3.3V

Control PWB

[Note 4]The equivalent circuit figure of the terminal
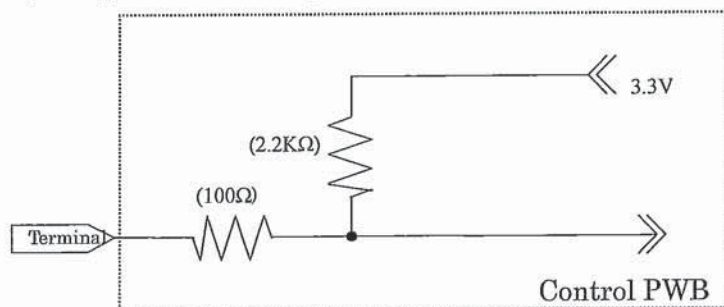
$(2.2K\Omega)$

(100Ω)

Terminal

3.3V

Control PWB

[Note 5]The equivalent circuit figure of the terminal

Terminal

$(10K\Omega)$

$(10K\Omega)$

Control PWB

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4.2. Interface block diagram

BACK LIGHT(LED package)

POWER SUPPLY
122.8V DC

GND

CN101

LED PWB

GATE DRIVER

LCD PANEL
1920×3(RGB)×1080

GATE DRIVER

SOURCE DRIVER

Power Supply
Circuit

Control
Signals

CONTROL PWB

CN1

INPUT SIGNALS
Rx0n, Rx0p
Rx1n, Rx1p
Rx2n, Rx2p
Rx3n, Rx3p
Rx4n, Rx4p
Rx5n, Rx5p
Rx6n, Rx6p
Rx7n, Rx7p
FRAME
2D/3D

POWER SUPPLY
+12V DC

OUTPUT SIGNALS
HTPDN
LOCKN
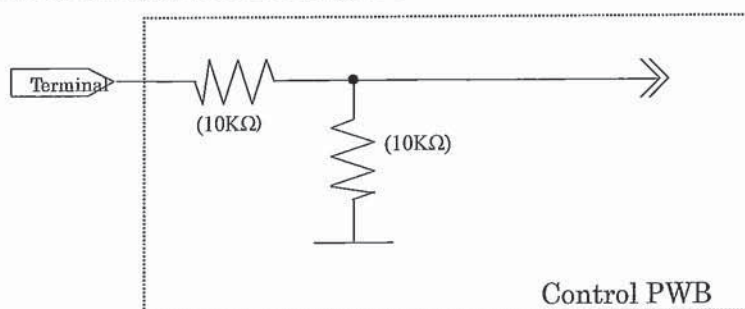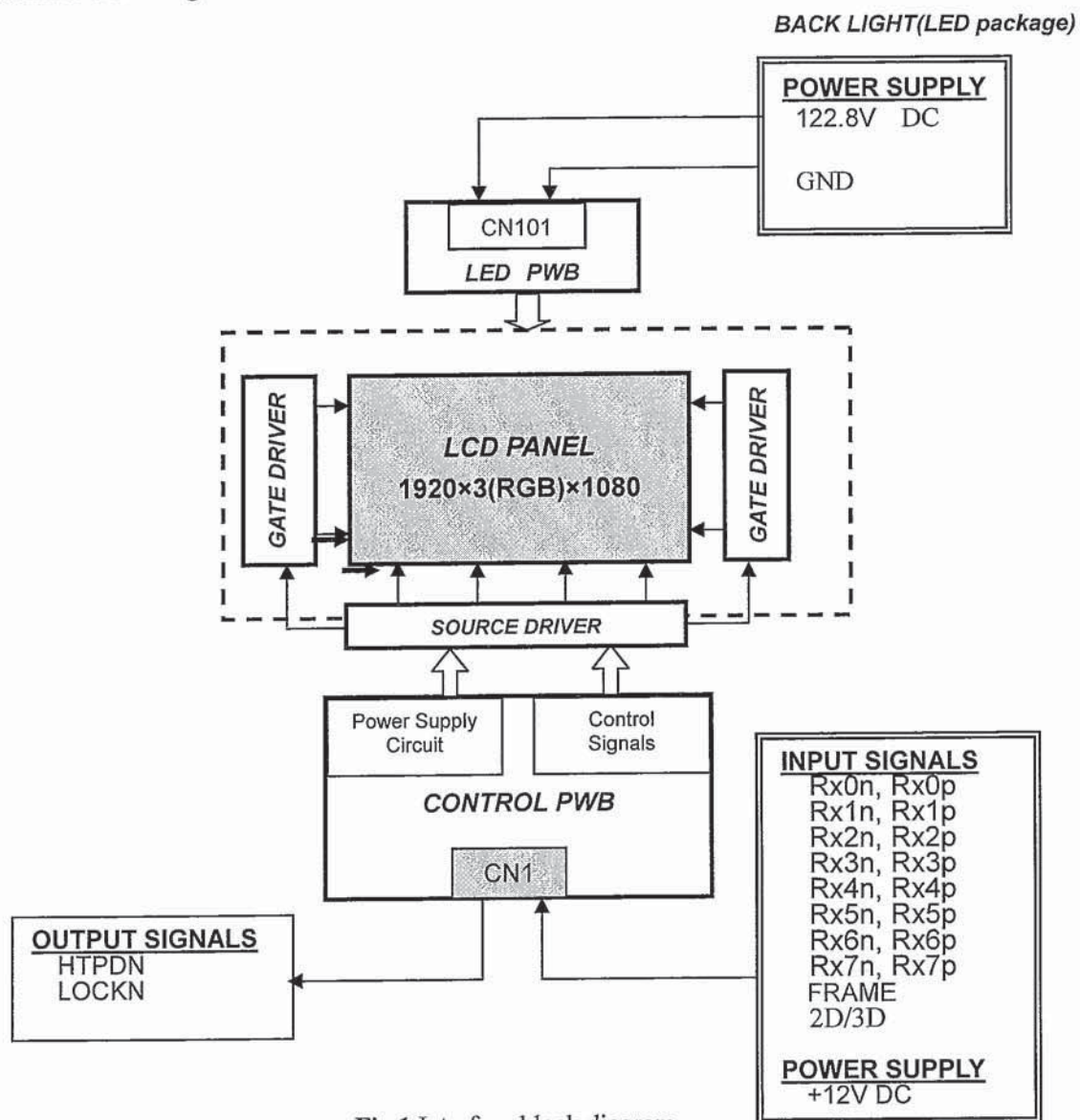
Fig.1 Interface block diagram

Backlight driving

4.3. Backlight driving

CN101 (DC power supply)

Using connector: A2006T1P-2L(JWT)

Mating connector: A2010WR0-15P-SW-3.2-W1 (JWT)

Pin No.	Symbol	Function	Remark
1	PIN_1	LED Anode terminal (LED-PWB1), red cable	
2	Reserved		
3	PIN_3	LED Cathode terminal (LED-PWB1), grey cable	
4	Reserved		
5	PIN_5	LED Anode terminal (LED-PWB2), blue cable	
6	Reserved		Ta=25℃
7	PIN_7	LED Cathode terminal (LED-PWB2), grey cable	
8	Reserved		
9	Pin_9	LED Anode terminal (LED-PWB3), black cable	
10	Reserved		
11	Pin_11	LED Cathode terminal (LED-PWB3), grey cable	
12	Reserved		
13	Pin_13	LED Anode terminal (LED-PWB4), white cable	
14	Reserved		
15	PIN_15	LED Cathode terminal (LED-PWB4) , grey cable	

CN101 side cable lengths (from panel backplate opening to including connector head)   L= 500±30 [mm](typ.)

[Note1] LED bar position

```
┌──────────┐          ┌──────────┐
│ LED bar 1 │          │ LED bar 2 │
└──────────┘          └──────────┘
        LCD Module
        (Back side)
┌──────────┐          ┌──────────┐
│ LED bar 3 │          │ LED bar 4 │
└──────────┘          └──────────┘
```
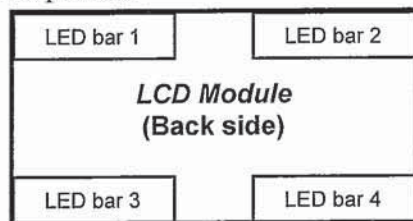
4.4. The back light system characteristics

The characteristics of the LED are shown in the following table. The value mentioned below is at the case of One LED.

Item	Symbol	Min.	Typ.	Max.	Unit	Remarks
Life time	T_{LED}	-	40,000	-	Hour	25℃

5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage (for Control)	V_I	Ta=25°C	-0.3 ~ 3.6	V	[Note 1]
12V supply voltage (for Control)	VCC	Ta=25°C	0 ~ +14	V	
122.8V supply voltage (for LED PWB)	V_{LED}	Ta=25 °C	116.0 ~ 136.0	V	
1040mA supply current (for LED PWB)	I_{LED}	Ta=25 °C	0 ~ 1088	mA	
Storage temperature	Tstg	-	-25 ~ +60	°C	[Note 2]
Operation temperature (Ambient)	Topa	-	0 ~ +50	°C	

[Note 1] FRAME,2D/3D

[Note 2] Humidity 95%RH Max.(Ta≦40°C)

Maximum wet-bulb temperature at 39 °C or less.(Ta>40°C)   No condensation.

6. Electrical Characteristics

6.1. Control circuit driving
Ta=25 °C

Parameter		Symbol	Min.	Typ.	Max.	Uniit	Remark
+12V supply voltage	Supply voltage	Vcc	11.4	12	12.6	V	[Note 1]
	Current dissipation	Icc	-	1.2	2.7	A	[Note 2]
	Inrush current	I_{RUSH}	-	3.47	-	A	t1=500us [Note 6]
Permissible input ripple voltage		V_{RP}	-	-	100	mVp-p	Vcc = +12.0V
Input Low voltage		V_{IL}	0	-	1.0	V	[Note 3]
Input High voltage		V_{IH}	2.3	-	3.3	V	
Input leak current (Low)		I_{IL1}	-	-	100	µA	$V_I = 0V$ [Note 4]
Input leak current (High)		I_{IH1}	-	-	100	µA	$V_I = 3.3V$ [Note 4]
Terminal resistor		R_T	80	100	120	Ω	Differential input
Differential input high threshld		VRTH			50	mV	
Differential input Low threshld		VRTL	-50			mV	
Differential input Allowable Intra-pair Skew		tRISK_INTA	0.3			UI	[Note 5]
Differential input Allowable Inter-pair Skew		tRISK_INTER	5			UI	[Note 5]

[Note 1]

Input voltage sequences

$50 \mu s < t1 < 20ms$

$20ms < t2 < 50ms$

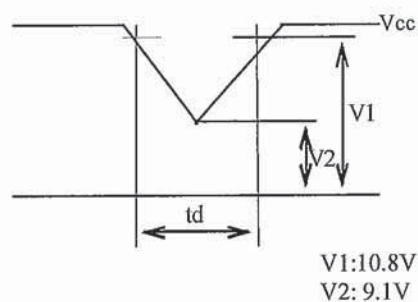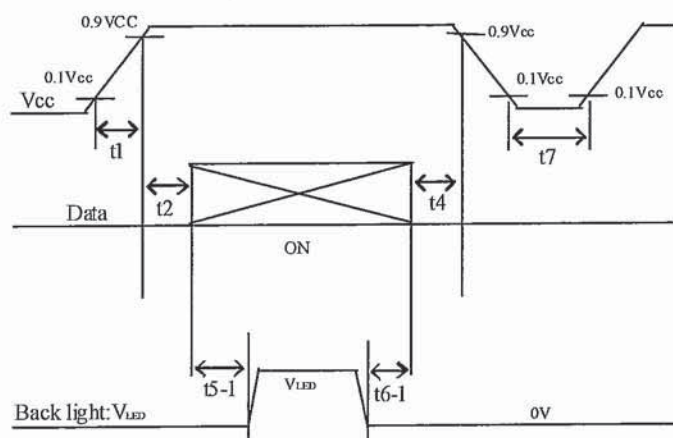$20ms < t3 < 50ms$

$0 < t4 < 1s$

$t5-1 > 1s$

$t6 -1 > 0$

$t7 > 1s$

Dip conditions for supply voltage

a) $9.1V \leqq Vcc < 10.8V$

td < 10ms

b) Vcc < 9.1V

Dip conditions for supply voltage is based on input voltage sequence.

V1:10.8V
V2: 9.1V

Data: Rx0n Rx0p Rx1n Rx1p Rx2n Rx2p Rx3n Rx3p
Rx4n Rx4p Rx5n Rx5p Rx6n Rx6p Rx7n Rx7p

※   *V_{CM} voltage pursues the sequence mentioned above

[Note] About the relation between data input and back light lighting, please base on the above-mentioned input

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sequence. When back light is switched on before panel operation or after a panel operation stop, it may not display normally. But this phenomenon is not based on change of an incoming signal, and does not give damage to a liquid crystal display.

[Note 2] Typical current situation: 1024 gray-bar patterns.   (Vcc = +12.0V)
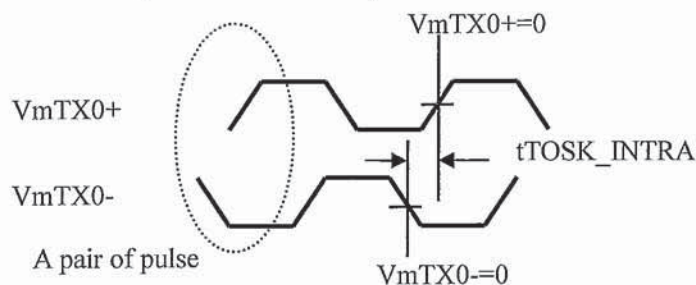
The explanation of RGB gray scale is seen in section 8.
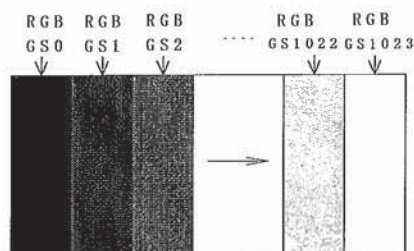
$Vcc＝+12.0V$

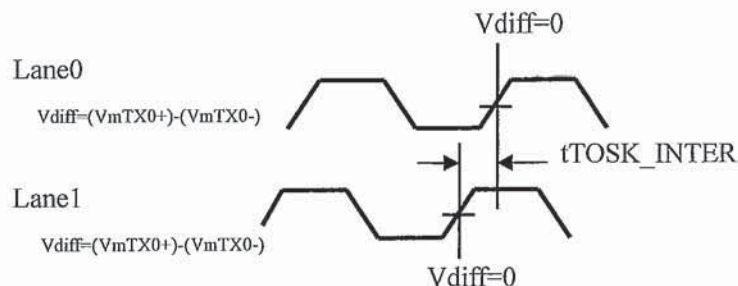$CK＝74.25MHz$
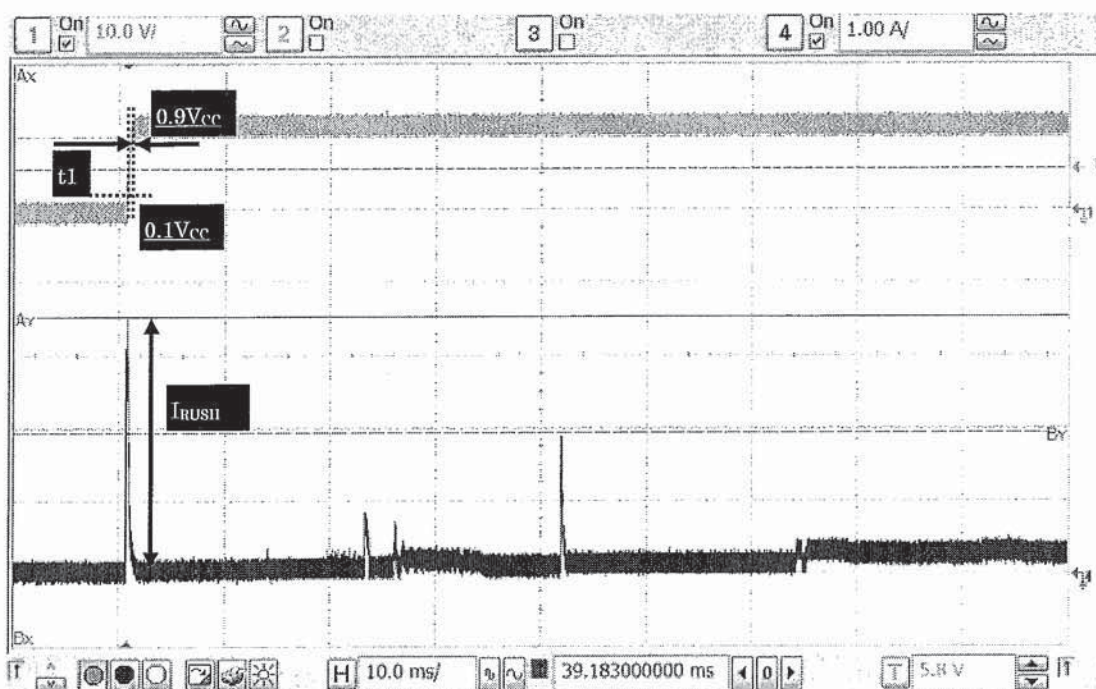
$Th＝7.41\mu s$

[Note 3] FRAME、2D/3D

[Note 4] FRAME、2D/3D

[Note 5-1] Differential input Allowable Intra-pair Skew

[Note 5-2] Differential input Allowable Inter-pair Skew

[Note 6] Vcc12V inrush current waveform

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6.2. LED driving for back light

The back light system is edge light type with LEDs .

Ta=25℃

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Operating Current	I_{LED}	-	1040	-	mA	130mAx2x4 [Note1]
Operating Voltage	V_{LED}	-	122.8	-	V	@1040mA/module [None2]

[None1]   PIN1, PIN3,PIN5,PIN7,PIN9,PIN11,PIN13,PIN15   (CN101)

LED current (I_{LED}) is the value of LED PWB

*Please decrease LED heat enough when the LED current is increased more than TYP value.

Please keep 85℃ or less the temperature of the terminal of LED.

[Note2] VLED is a potential difference between the anode and the cathode of each LED PWB.

Ta = 25℃, Measurement after 100ms has passed since power supply was turned on.

*The products are sensitive to the static electricity and care shall be fully taken when handling the products. Particularly in case that an over-voltage which exceeds the Absolute Maximum Rating of the products shall be applied, the overflowed energy may cause damages to, or possibly result in destruction of the products. Please take absolutely secured countermeasures against static electricity and surge when handling the products.

7.   Timing characteristics of input signals

7.1.   Timing characteristics

Timing diagrams of input signal are shown in Fig.2.

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
Clock	Frequency	1/Tc	67	74.25	80	MHz	
Data enable signal	Horizontal period	TH	270	275	300	clock	
			3.64	3.7	4.00	μs	
	Horizontal period (High)	THd	240	240	240	clock	
	Vertical period	TV	1118	1125	1400	line	
				240		Hz	
	Vertical period (High)	TVd	1080	1080	1080	line	

[Note]-When vertical period is very long, flicker and etc. may occur.

-Please turn off the module after it shows the black screen.

-Please make sure that length of vertical period should become of an integral multiple of horizontal length of period. Otherwise, the screen may not display properly.

-As for your final setting of driving timing, we will conduct operation check test at our side, please inform your final setting.
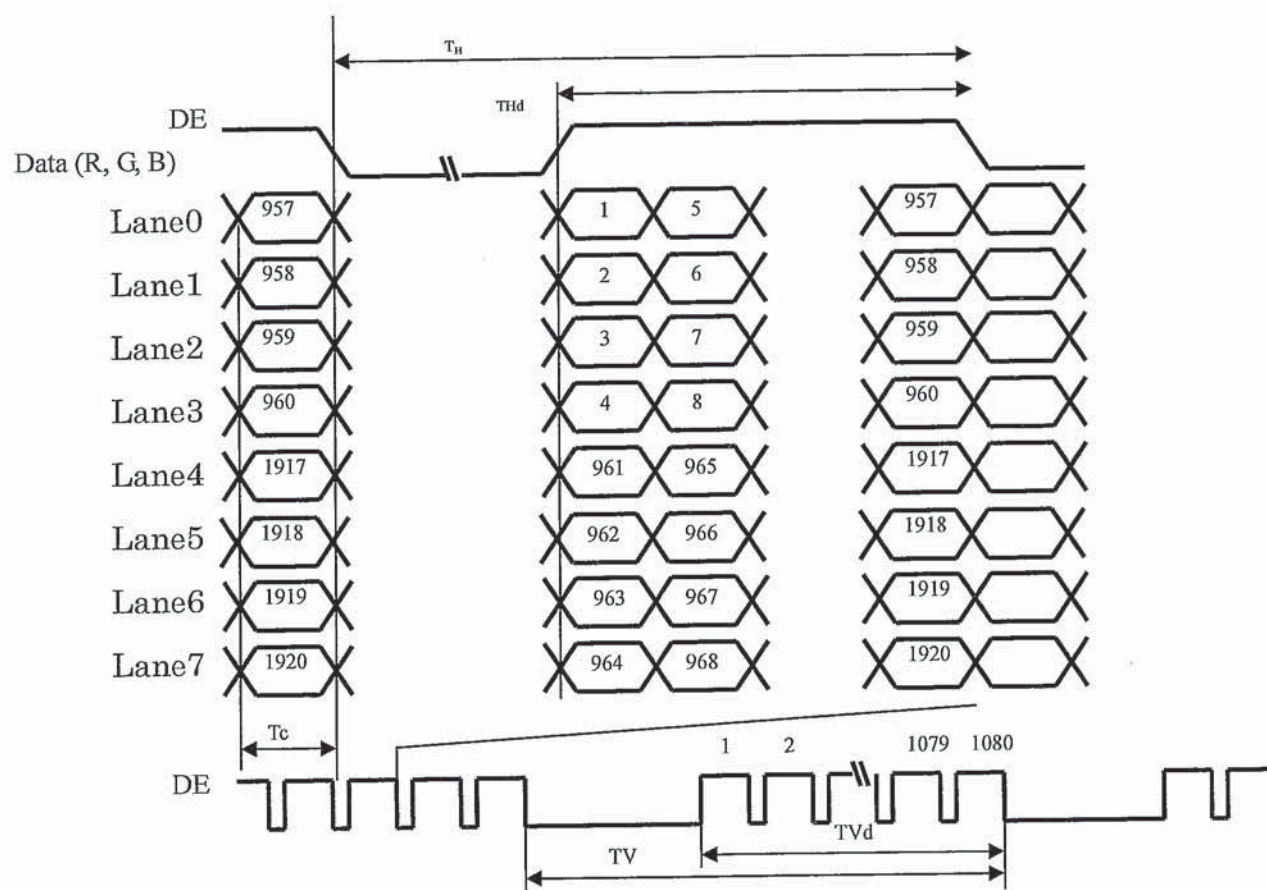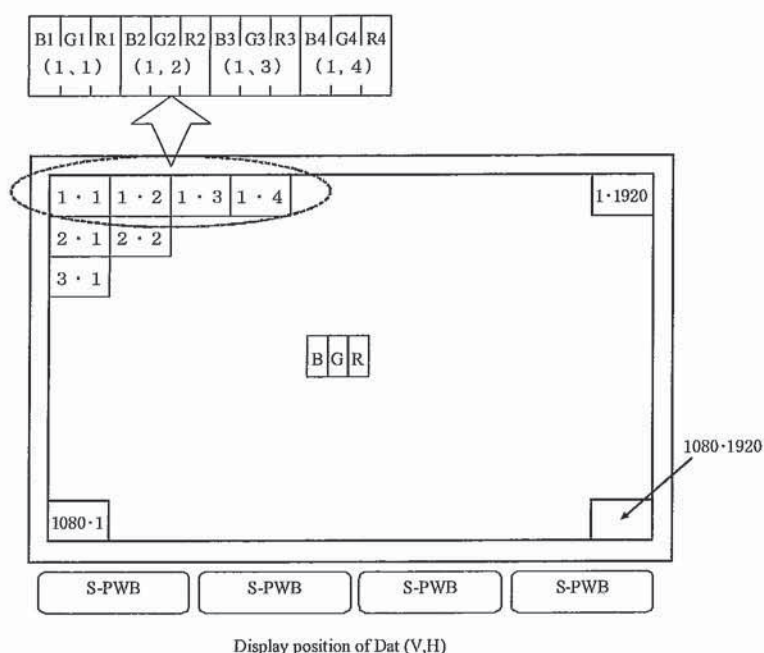
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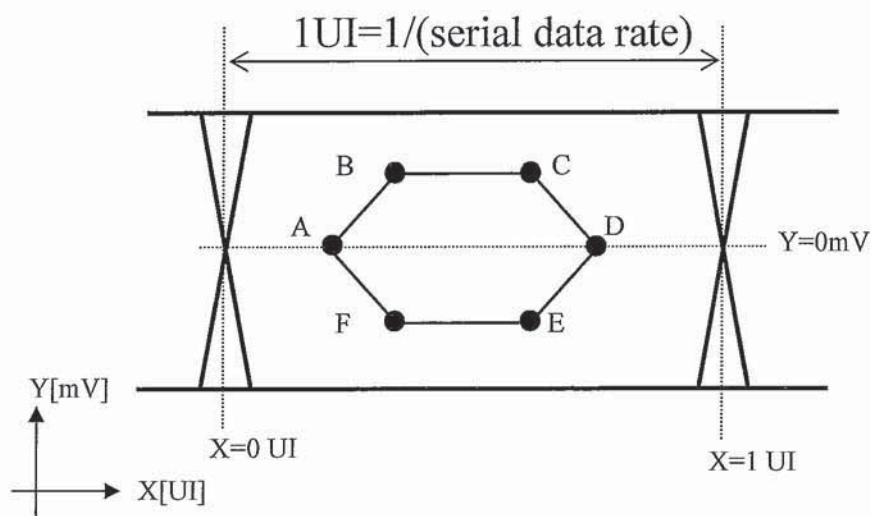Fig.2 Timing characteristics of input signal

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Input data signal and display position on the screen

Display position of Dat (V,H)

[Note] Scan direction is setting for using S-PWBs' side down.

7-3. V-by-One® HS Eye Diagram

	The item	X[UI]	Y[mV]
EYE MASK	A	0.25	0
	B	0.3	50
	C	0.7	50
	D	0.75	0
	E	0.7	-50
	F	0.3	-50

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8. Input Signal, Basic Display Colors and Gray Scale of Each Color

Colors & Gray scale		Gray Scale	Data signal																															
			R0	R1	R2	R3	R4	R5	R6	R7	R8	R9	G0	G1	G2	G3	G4	G5	G6	G7	G8	G9	B0	B1	B2	B3	B4	B5	B6	B7	B8	B9
Basic Color	Black	–	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	–	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Green	–	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Cyan	–	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	–	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	–	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1
	Yellow	–	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	White	–	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	↓					↓										↓										↓					
	⇩	↓					↓										↓										↓					
	Brighter	GS1021	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇩	GS1022	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS1023	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	↓					↓										↓										↓					
	⇩	↓					↓										↓										↓					
	Brighter	GS1021	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	⇩	GS1022	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
	Green	GS1023	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	⇧	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	⇩	↓					↓										↓										↓					
	⇩	↓					↓										↓										↓					
	Brighter	GS1021	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	1	1
	⇩	GS1022	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1
	Blue	GS1023	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1

0: Low level voltage,　　1: High level voltage.

Each basic color can be displayed in 1021 gray scales from 10 bits data signals. According to the combination of total 30 bits data signals, about one billion-color display can be achieved on the screen.

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9. Optical characteristics

Ta=25°C, Vcc=12.0V, V_LED =+122.8V, LED current=1040mA, Timing:240Hz (typ. value)

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing angle range	Horizontal	$\theta 21$ $\theta 22$	CR≧10	70	88	-	Deg.	[Note1,4]
	Vertical	$\theta 11$ $\theta 12$		70	88	-	Deg.	
Contrast ratio		CRn			5000	-	-	[Note2,4]
Response time		τ_{DRV}			4		ms	[Note3,4,5]
Luminance	White	x	θ =0 deg.	0.235	0.265	0.295	-	[Note4]
		y		0.233	0.263	0.293	-	
	Red	x		0.614	0.644	0.674	-	
		y		0.317	0.347	0.377	-	
	Green	x		0.276	0.306	0.336	-	
		y		0.612	0.642	0.672	-	
	Blue	x		0.122	0.152	0.182	-	
		y		0.022	0.052	0.082	-	
Luminance	White	Y_L		400	450	-	cd/m^2	
Luminance uniformity	White	δw		-	-	1.27		[Note 6]

Measurement condition: Set the value of LED current=1040mA and luminance of white.

*The measurement shall be executed 60 minutes after lighting at rating.

[Note]The optical characteristics are measured using the following equipment.
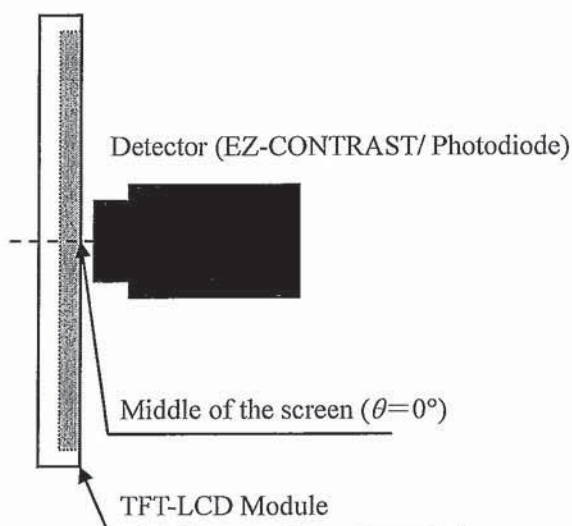
Fig.4-1 Measurement of viewing angle range and Response time.

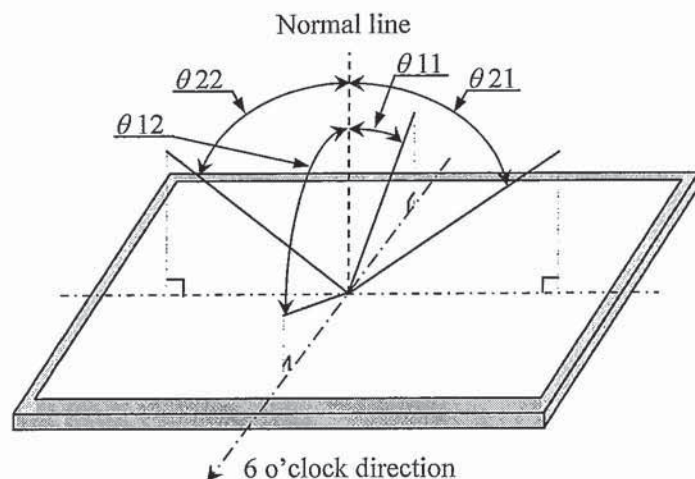Viewing angle range: EZ-CONTRAST
Response time: Photodiode
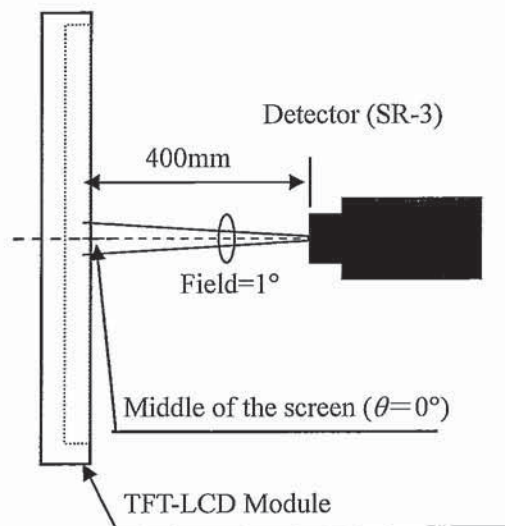
Fig.4-2 Measurement of Contrast, Luminance, Chromaticity.

[Note 1] Definitions of viewing angle range :

[Note 2] Definition of contrast ratio :

The contrast ratio is defined as the following.

$$\text{Contrast Ratio} = \frac{\text{Luminance (brightness) with all pixels white}}{\text{Luminance (brightness) with all pixels black}}$$

[Note 3] Definition of response time

The response time (τ_d and τ_r) is defined as the following figure and shall be measured by switching the input signal for "any level of gray (0%, 25%, 50%, 75% and 100%)" and "any level of gray (0%, 25%, 50%, 75% and 100%)".

	0%	25%	50%	75%	100%
0%		tr:0%-25%	tr:0%-50%	tr:0%-75%	tr:0%-100%
25%	td: 25%-0%		tr: 25%-50%	tr25%-75%	tr: 25%-100%
50%	td: 50%-0%	td: 50%-25%		tr: 50%-75%	tr: 50%-100%
75%	td: 75%-0%	td: 75%-25%	td: 75%-50%		tr: 75%-100%
100%	td: 100%-0%	td: 100%-25%	td: 100%-50%	td:100%-75%	

t*:x-y...response time from level of gray(x) to level of gray(y)

$\tau = \Sigma(t^*:x\text{-}y)/20$
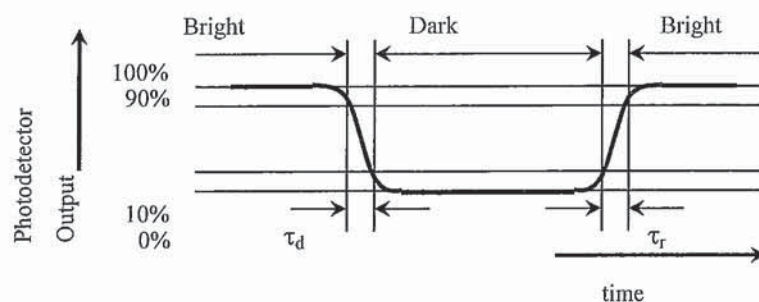
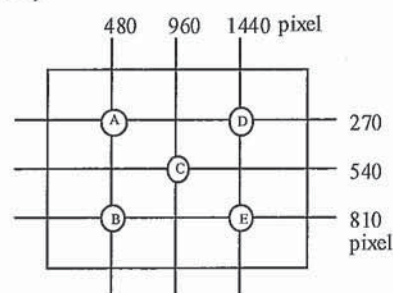[Note 4] This shall be measured at center of the screen.

[Note 5] This value is valid when O/S driving is used at typical input time value.

[Note 6] Definition of white uniformity ;

White uniformity is defined as the following with five measurements. (A~E)

$$\delta w = \frac{\text{Maximum luminance of five points (brightness)}}{\text{Minimum luminance of five points (brightness)}}$$

10. Handling Precautions of the module

a) Be sure to turn off the power supply when inserting or disconnecting the cable.

b) Voltage difference generated by this switching, ΔV_{LED}, may affect a sound output, etc. when the power supply is shared between the LED driver and its surrounding circuit. So, separate the power supply of the LED driver circuit with the one of its surrounding circuit.
  *Since LED driver board's GND is not connected to the frame of the LCD module, please connect it with the Customer's GND of LED driver power supply.

c) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.

d) Since the front polarizer is easily damaged, pay attention not to scratch it.

e) Since long contact with water may cause discoloration or spots, wipe off water drop immediately.

f) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.

g) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.

h) Since CMOS LSI is used in this module, take care of static electricity and take the human earth into consideration when handling.

i) The module has some printed circuit boards (PCBs) on the back side, take care to keep them form any stress or pressure when handling or installing the module; otherwise some of electronic parts on the PCBs may be damaged.

j) Observe all other precautionary requirements in handling components.

k) When some pressure is added onto the module from rear side constantly, it causes display non-uniformity issue, functional defect, etc. So, please avoid such design.

l) When giving a touch to the panel at power on supply, it may cause some kinds of degradation. In that case, once turn off the power supply, and turn on after several seconds again, and that is disappear.

m) When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.

n) This LCD module is designed to prevent dust from entering into it. However, there would be a possibility to have a bad effect on display performance in case of having dust inside of LCD module. Therefore, please ensure to design your TV set to keep dust away around LCD module.

o) This LCD module passes over the rust.

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11. Packing form

a) Piling number of cartons　　　　　　　: 2 Maximum

b) Packing quantity in one carton　　　　: 10pcs

c) Carton size　　　　　　　　　　　　: 1545(W)×1140(D)×1030(H)

d) Total mass of one carton filled with full modules : 245kg

12. Reliability test item

| No.	Test item	Condition
1	High temperature storage test	Ta=60°C　　240h
2	Low temperature storage test	Ta=-25°C　　240h
3	High temperature and high humidity operation test	Ta=40°C ; 95%RH　　240h (No condensation)
4	High temperature operation test	Ta=50°C　　240h
5	Low temperature operation test	Ta=0°C　　240h
6	Vibration test (non-operation) *[Note1]	Frequency: 10~57Hz/Vibration width (one side): 0.075mm : 58~500Hz/Acceleration: 9.8 m/s² Sweep time: 11 minutes Test period: 3 hours (1h for each direction of X, Y, Z)
7	Shock test (non-operation) *[Note1]	Maximum acceleration: 294m/s² Pulse width: 11ms, sinusoidal half wave Direction: +/-X, +/-Y, +/-Z, once for each direction.
8	ESD	* At the following conditions, it is a thing without incorrect operation and destruction. (1)Non-operation: Contact electric discharge ±10kV　　　　　　　　　　　Non-contact electric discharge ±20kV (2)Operation　　Contact electric discharge ±8kV　　　　　　　　　　　Non-contact electric discharge ±15kV Conditions: 150pF, 330ohm

[Result evaluation criteria]

Under the display quality test condition with normal operation state, there shall be no change, which may affect practical display function.

[Note1]
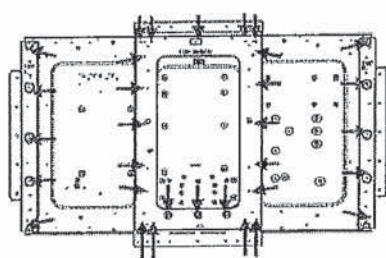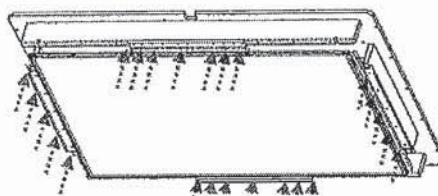
32 places(M3 bolt)　　　　　　　24 places(M8 bolt)

Fig(a) Module installation state figure (Rear view)　Fig(b) Base jig installation state figure

*Please fix the part in the above-mentioned figure. Otherwise, the warranty of the quality cannot be done.
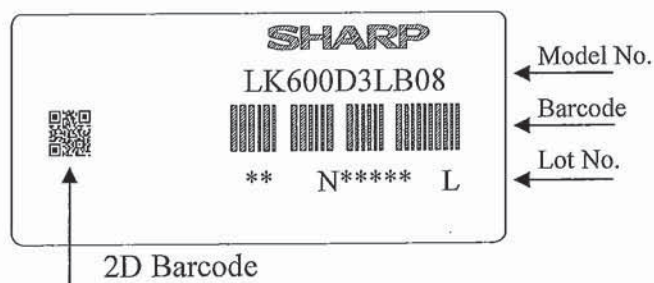
(*)Recommended fixed method is shown in the Figure

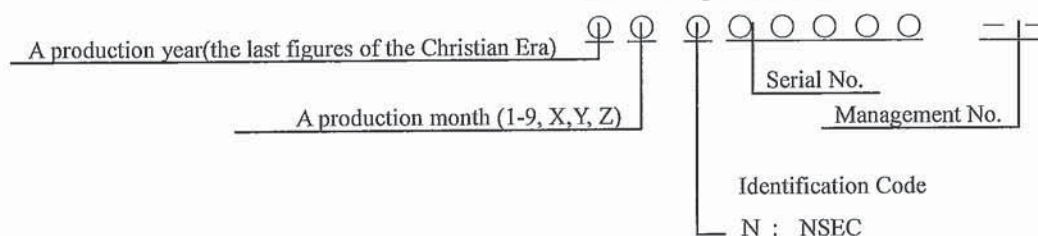Figure of Shock test JIG, Vibration test JIG and Module fixed position

13. Others

1) Lot No. Label ;

The label that displays SHARP, product model (LK600D3LB08), a product number is stuck on the back of the module.
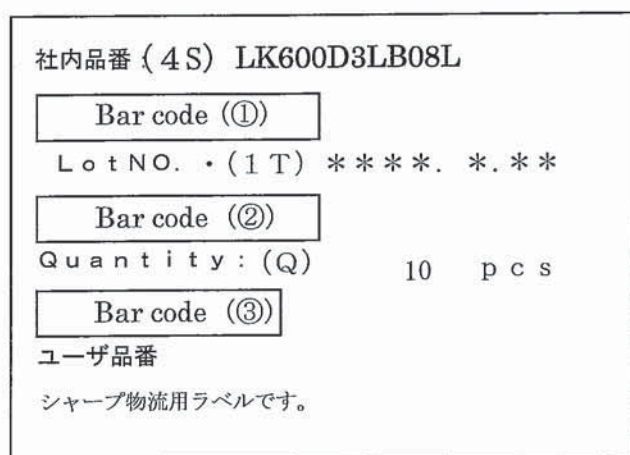
[LK600D3LB08L]   NSEC PRODUCTION

← Model No.
← Barcode
← Lot No.

SHARP
LK600D3LB08

** N***** L

2D Barcode

How to express Lot No.

A production year(the last figures of the Christian Era)
A production month (1-9, X,Y, Z)

Serial No.
Management No.

Identification Code
N : NSEC

2) Packing Label

[LK600D3LB08L]   NSEC PRODUCTION

社内品番 (4S) LK600D3LB08L

Bar code (①)

LotNO. · (1T) ****. *.**

Bar code (②)

Quantity: (Q)   10 pcs

Bar code (③)

ユーザ品番

シャープ物流用ラベルです。

① Management No.
② Lot No. (Date)
③ Quantity

3) Adjusting volume has been set optimally before shipment, so do not change any adjusted value.
   If adjusted value is changed, the specification may not be satisfied.

4) Disassembling the module can cause permanent damage and should be strictly avoided.

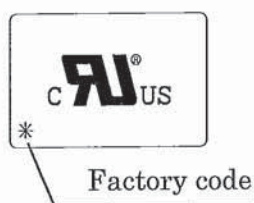5) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.

6) The chemical compound, which causes the destruction of ozone layer, is not being used.

7) When any question or issue occurs, it shall be solved by mutual di There is R.C. marking on the module packing box sid scussion.

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8) This module is corresponded to RoHS. e.

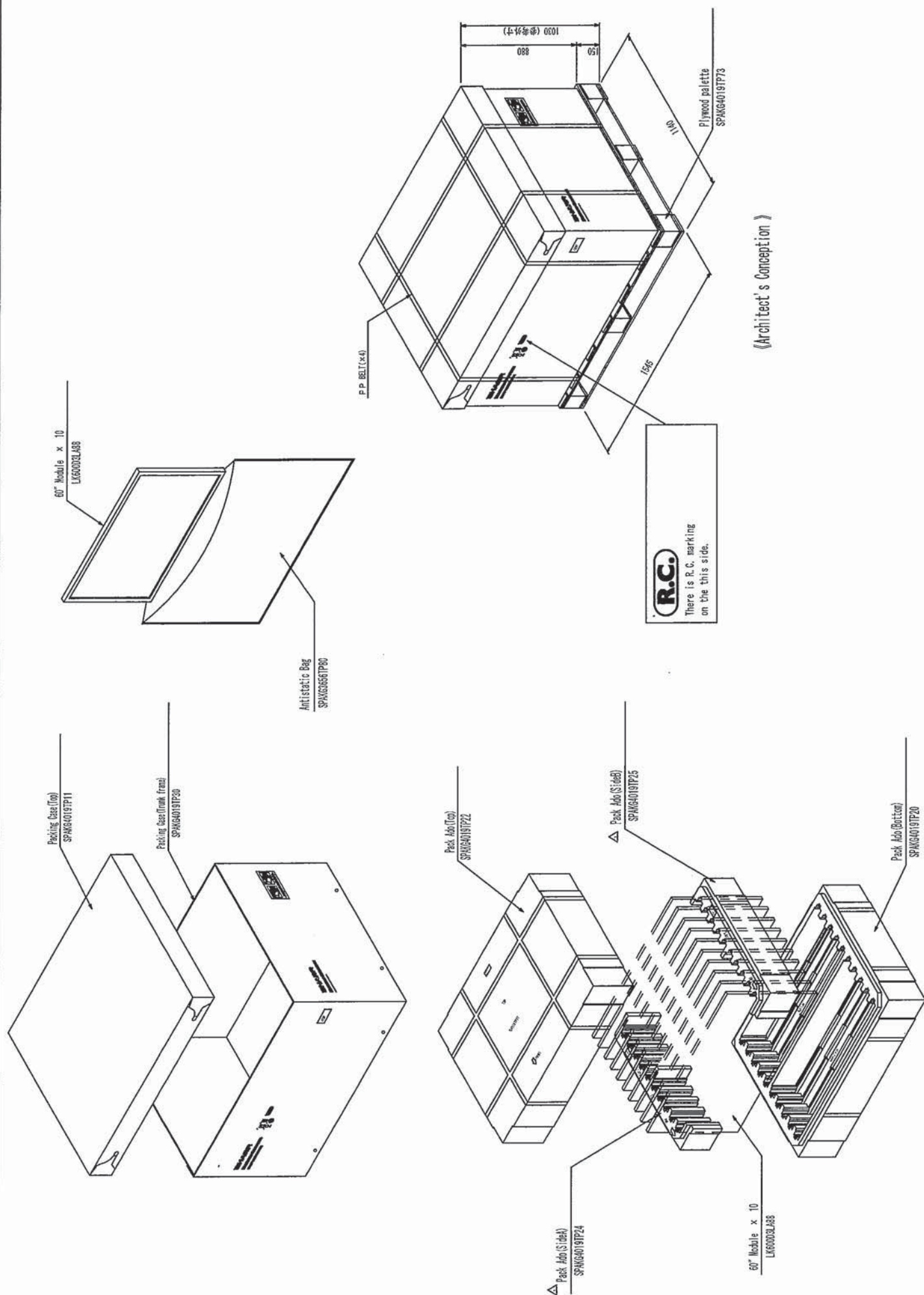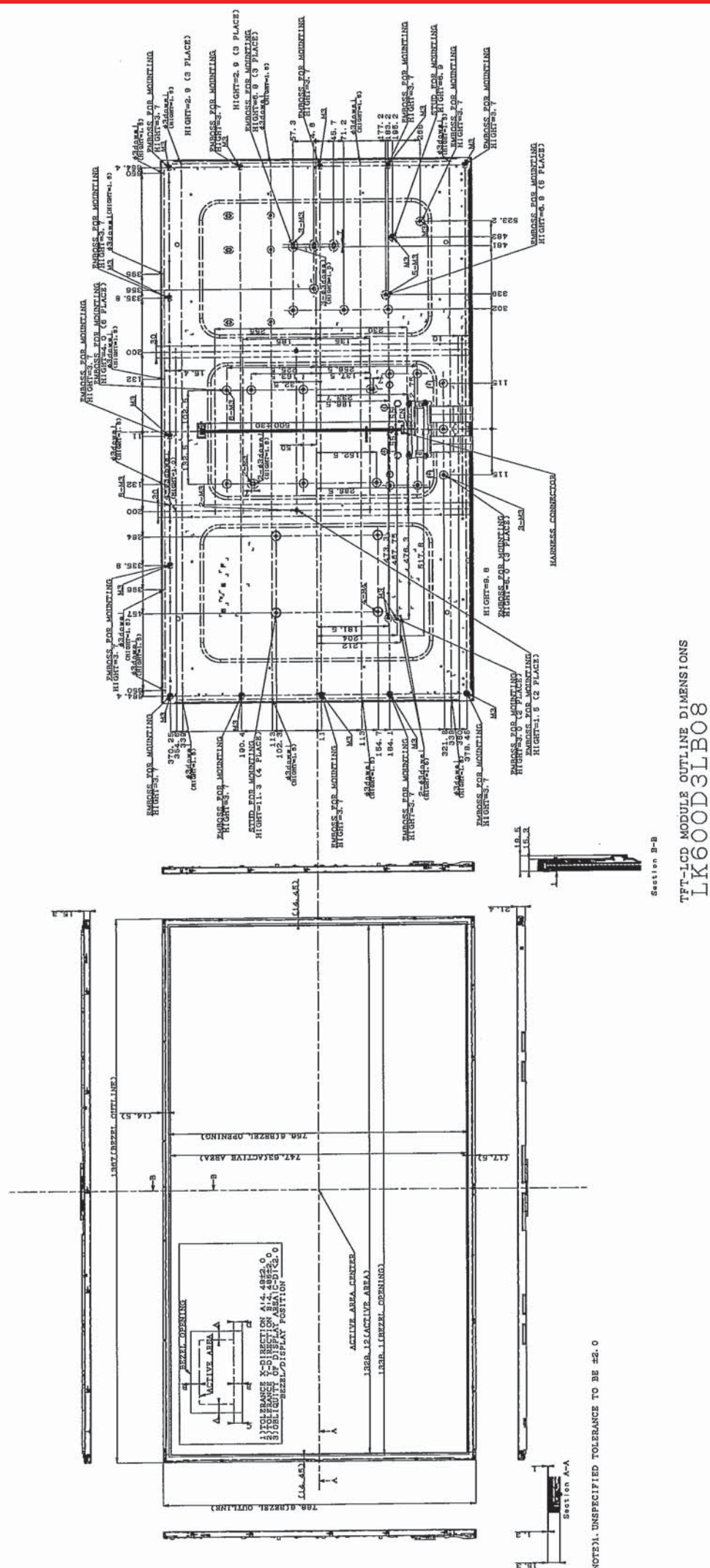9) This LCD is appropriate to UL. Below figure shows the UL label.

Factory code

14. Carton storage condition

Temperature	0°C to 40°C
Humidity	95%RH or less
Reference condition	: 20°C to 35°C, 85%RH or less (summer)
	: 5°C to 15°C, 85%RH or less (winter)
	the total storage time (40°C,95%RH) : 240H or less
Sunlight	Be sure to shelter a product from the direct sunlight.
Atmosphere	Harmful gas, such as acid and alkali which bites electronic components and/or wires must not be detected.
Notes	Be sure to put cartons on palette or base, don't put it on floor, and store them with removing from wall
	Please take care of ventilation in storehouse and around cartons, and control changing temperature is within limits of natural environment
Storage life	1 year

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1030 (番番的尺寸)

880

150

Plywood palette
SPAKG4019TP73

1140

PP BELT(×4)

1595

《Architect's Conception 》

R.C.
There is R.C. marking
on the this side.

60" Module × 10
LK600D3LA88

Antistatic Bag
SPAKG6866TP60

Packing Case(Top)
SPAKG4019TP11

Packing Case(Trunk frame)
SPAKG4019TP30

Pack.Ado(Top)
SPAKG4019TP22

Pack.Ado(SideB)
SPAKG4019TP25

Pack.Ado(Bottom)
SPAKG4019TP20

Pack.Ado(SideA)
SPAKG4019TP24

60" Module × 10
LK600D3LA88

TFT-LCD MODULE OUTLINE DIMENSIONS
LK600D3LB08