

LESD8D5.0CN3T5G ESD PROTECTION DIODE

Discription

The LESD8D5.0CN3T5G is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time ,make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.

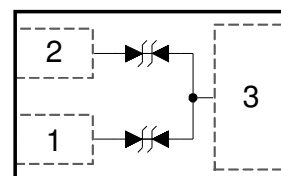
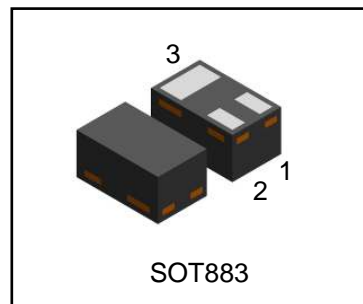
Applications

- | Cellular phones audio
- | MP3 players
- | Digital cameras
- | Portable applicationss
- | mobile telephone

Features

- | Small Body Outline Dimensions: 0.039" X 0.024"(1.0mmX0.6mm)
- | Low Body Height: 0.020"(0.50mm)
- | Low Leakage
- | Response Time is Typically < 1 ns
- | ESD Rating of Class 3 (> 16 kV) per Human Body Model
- | IEC61000-4-2 Level 4 ESD Protection
- | These are Pb-Free Devices
- | We declare that the material of product compliance with RoHS requirements.

LESD8D5.0CN3T5G



Ordering information

| Device | Marking | Shipping |
|-----------------|---------|-----------------|
| LESD8D5.0CN3T5G | P2 | 10000/Tape&Reel |

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--|---------|------------|------|
| IEC 61000-4-2 (ESD) Air discharge | | ±25 | kV |
| Contact discharge | | ±20 | kV |
| ESD Voltage Per Human Body Model | | 16 | kV |
| Total Power Dissipation on FR-5 Board (Note 1) @ T _A =25°C | PD | 200 | mW |
| Junction and Storage Temperature Range | TJ,TSTG | -55 to 150 | °C |
| Lead Solder Temperature – Maximum (10 Second Duration) | TL | 260 | °C |

Stresses exceeding Maximum Ratings may damage the device. Maximum Rating are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

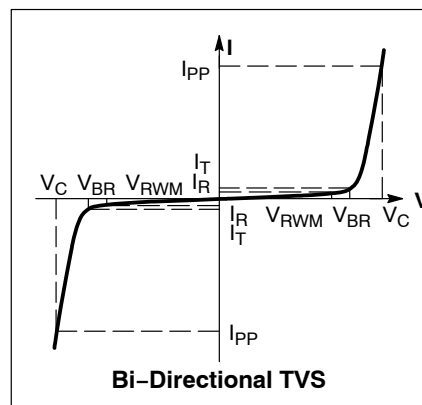
1. FR-5 = 1.0*0.75*0.62 in.

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ELECTRICAL CHARACTERISTICS

($T_A = 25^\circ\text{C}$ unless otherwise noted)

| Symbol | Parameter |
|-----------|---|
| I_{PP} | Maximum Reverse Peak Pulse Current |
| V_C | Clamping Voltage @ I_{PP} |
| V_{RWM} | Working Peak Reverse Voltage |
| I_R | Maximum Reverse Leakage Current @ V_{RWM} |
| V_{BR} | Breakdown Voltage @ I_T |
| I_T | Test Current |
| P_{pk} | Peak Power Dissipation |
| C | Capacitance @ $V_R = 0$ and $f = 1.0$ MHz |



ELECTRICAL CHARACTERISTICS

| | V_{RWM} (V) | $I_{R1}(\mu\text{A})$ @ V_{RWM} | $I_{R2}(\mu\text{A})$ @ $V_R = 3.5\text{V}$ | V_{BR} (V) @ I_T (Note 2) | I_T | V_C (V) @ $I_{PP} = 1\text{ A}$ (Note 3) | V_C (V) @ MAX I_{PP} (Note 3) | I_{PP} (A) (Note 3) | P_{PK} (W) (Note 3) | C (pF) |
|-----------------|------------------|--------------------------------------|--|----------------------------------|-------|--|---|--------------------------|--------------------------|----------|
| Device | Max | Max | Max | Min | mA | Max | Max | Max | Max | Max |
| LESD8D5.0CN3T5G | 5.0 | 0.5 | 0.3 | 5.6 | 1.0 | 9.8 | 12.5 | 5.5 | 69 | 15 |

Other voltage available upon request.

- V_{BR} is measured with a pulse test current I_T at an ambient temperature of 25°C
- Surge current waveform per Figure 3.

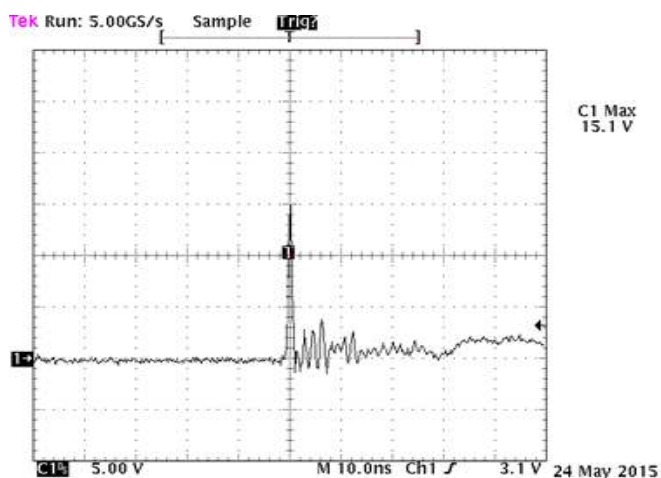


Fig1. ESD Clamping Voltage Screenshot
Positive 8 kV Contact per IEC61000-4-2

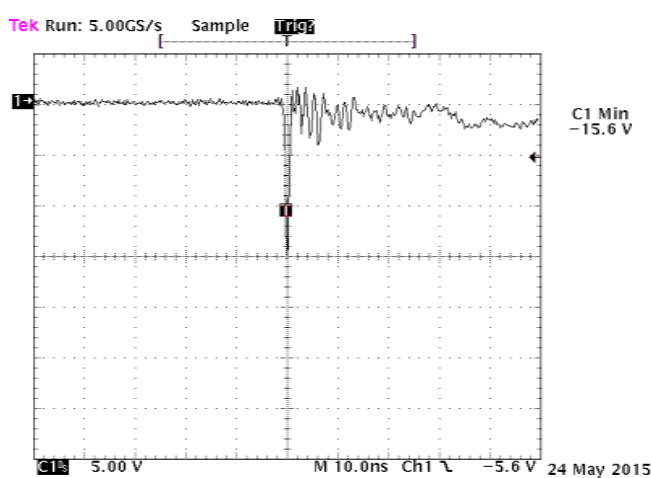


Fig2. ESD Clamping Voltage Screenshot
Negative 8 kV Contact per IEC61000-4-2

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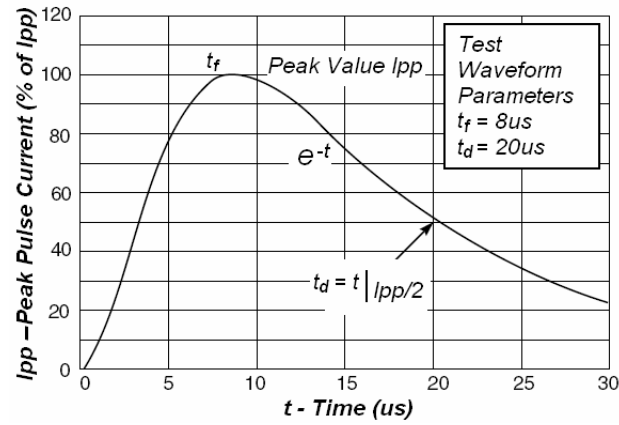
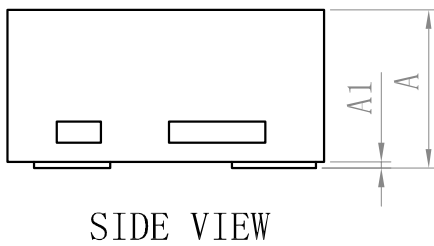
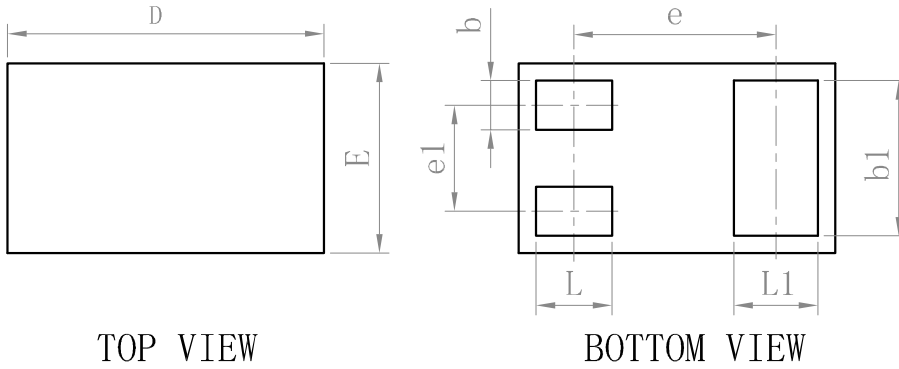


Fig3. Pulse Waveform

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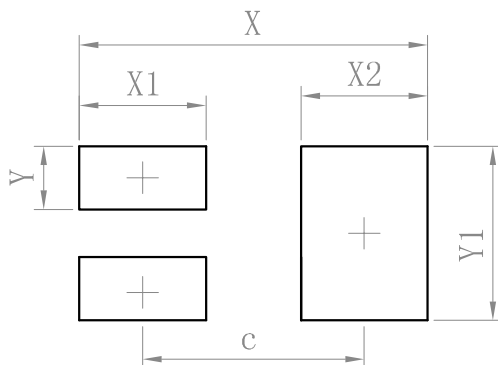
SOT883

Package Outline Dimension



| SOT883 | | | |
|----------------------|------|------|------|
| Dim | Min | Typ | Max |
| D | 0.95 | 1.00 | 1.05 |
| E | 0.55 | 0.60 | 0.65 |
| e | — | 0.64 | — |
| e1 | — | 0.34 | — |
| L | 0.19 | 0.24 | 0.29 |
| L1 | 0.22 | 0.27 | 0.32 |
| b | 0.10 | 0.15 | 0.20 |
| b1 | 0.44 | 0.49 | 0.54 |
| A | 0.43 | 0.48 | 0.53 |
| A1 | 0 | — | 0.05 |
| All Dimensions in mm | | | |

Suggested Pad layout



| Dimensions | (mm) |
|------------|------|
| c | 0.70 |
| X | 1.10 |
| X1 | 0.40 |
| X2 | 0.40 |
| Y | 0.20 |
| Y1 | 0.55 |