創維集團有限公司

Skyworth 创维 SKYWORTH (GROUP) CO., LTD.

ENGINEERING EVALUATION REPORT

<u>临</u>时 2008.10.24

(COMPONENTS)

(COMPONENTS)				
REPORT NO. : EDE0810118	SAMPLE IS :			
MODEL : 8M19	☑ NEW PARTS			
DATE COMPLETED : 2008 -10 - 24				
QTY SUBMITTED : 2 nd SUBMISSION				
DESCRIPTIONS :				
32.0" LG.PHILIPS TFT LCD LC320WXE-SBB1	□ OTHER REMARKS			
WXGA(1366 X 768) (NO BACKLIGHT)	SPECIFICATION ATTACHED YES \square NO \square			
REMARKS :				
SUPPLIER'S PART NO. : LC320WXE-SBB1				
OUR PARTS NO. : 7607-T32004-0000				
SUPPLIER :				
MANUFACTURER : LG DISPLAY CO.	,LTD			
TEST RESULT : Z APPROVED	□ REJECTED			
CONDITIONALLY	Y APPROVED : SEE ITEMS LISTED BELOW			
3. 请在内/外包装箱	問題而引起的損失,元件生產廠家需負全責。 上打印我公司物料编号 7607-T32004-0000。 :未能提供 ROHS 声明和测试报告。			
IF CONDITIONALLY APPROVED :				
□ (a) SUPPLIER IS REQUESTED TO SUBMI	T PIECES SAMPLES AGAIN.			
□ (b) FOR PURCHASING TO BUY	PIECES FOR PILOT PRODUCTION.			
TESTED BY ENGINEER (S)	APPROVED BY ENGINEERING MANAGER			
ELECTRICAL $\frac{+}{B}$ $\sqrt{\frac{1}{2}}$ 2wd. 10.24 $\frac{10.24}{\sqrt{10^{-9}}}$	THE IO/MY			

Rm. 1601-04 Westlands Centre, 20 Westlands Road, Quarry Bay, H.K. Tel: 2856 3138 SKYWO HX Fax: 2586 3590

.

创维集团有限公司研究院

新增电子 EDE 编号申请表

资料室填写:	Γ , T , 0 , 0
EDE 号:	EDE 0810118
通用物料编号:	/
专用物料编号:	7607-732004-0000

设计师填写:(请在相关项目栏的"□"中打上"√",即为 ☑,并在内容栏中填写具体信息。)

序号	项目		内 容			说明
1	🗹 申请新物料编号	口 申请通用厂家物料组	鳥号 ☑	申请专用厂家	R物料编号	两者选
2	口 在现有编号下增 加其他厂家的规格书	× ·	:	_		填一项
3	☑ 申请原因	LGI	I DA		-4	
4	☑ 物料名称及种类	ja B	E-			
5	☑ 物料型号	Lezzo	WXE-SBB	/		必须填写
6	☑ 机芯/机型	8MIP				必须填与
7	☑ 厂家信息	LG				
8	口 代理商信息					
9	口 IC 功能					
	口 10 类型	口 单片数字集成	口 单片非数	空集成	口混合集成	IC
10		线宽:		于来戏		必须填写
11	口 物料本体丝印				- 4	
12	口 尺寸封装规格					
13	口 主要技术参数					建议 填写
14	☑ 符合 ROHS	讲注意	需.为入飞	DZ.		
15	备注	V				•
ŧ	请(设计师签名)	107元	7	日期	27/10-70	≈8
事样	该(项目经理签名)	洪文生	(行)	日期	/	
ţ	批准(领导签名)	in the second se	-	日期	08.10.2	3

注意事项:

1、 物料名称及种类: 请写明物料具体名称及种类:

2、在现有编号下增加其他厂家的规格书:请写明现有编号;

3、物料型号: 请填写完整准确的物料型号或厂家编号;

4、厂家信息:请写明完整准确的厂家信息;代理商信息:如果有代理商,请写明完整准确的代理商信息;

5、IC 功能描述:如果您申请的是 IC,请在相对应的栏目中写明 IC 的中文功能:

6、物料本体丝印:如果您申请的是 IC,请在相对应的栏目中写明来料 IC 本体上的丝印(注明主体部分即可);

7、符合 ROHS (即环保要求):若符合,请提供相关资料,并注明"符合"字样(只有在资料完整的情况下才可以打勾!)

;• ."

.

8、批准栏均由各所副所长级别以上领导亲笔签名。



SPECIFICATION FOR APPROVAL

(●) Preliminary Specification

) Final Specification

Title

(

This Product must be used for a TV Application This is not designed for the public display.

32.0" WXGA TFT LCD

BUYER	
MODEL	

SUPPLIER	LG Display Co., Ltd.
*MODEL	LC320WXE
SUFFIX	SBB1

*When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE DATE	APPROVED BY	SIGNATURE DATE
/		S.S. Kim / Team Leader	
		REVIEWED BY	
/		J.S. PARK / Sub Leader	
		PREPARED BY	
/		Y.J. KIM / Engineer	
Please return 1 copy for your c your signature and cor		TV Product Developme LG Display Co., L	

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RECORD OF REVISIONS

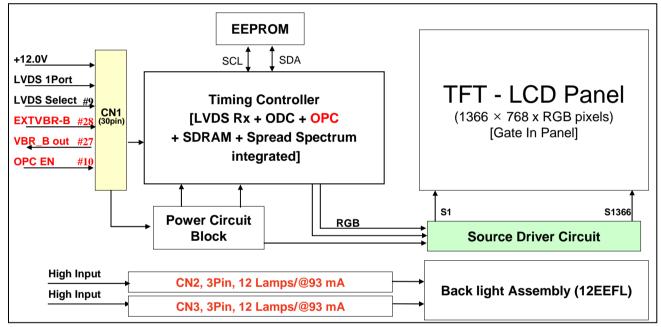
Revision No.	Revision Date	Page	Description
0.1	Jun, 30, 2008	-	Preliminary Specification(First Draft)
0.2	Jul, 14, 2008	21,22	Change of Mechanical drawing(Cover bottom)
	Aug. 40, 0000	1,A3-6,14	Change of Model Name(SBD1→SBB1)
0.3	Aug, 12, 2008	22	Change of Mechanical drawing(UDM Hole Size)
0.4	Aug, 20, 2008	-	Change of TDB SPEC

1. General Description

The LC320WXE is a Color Active Matrix Liquid Crystal Display with an integral External Electrode Fluorescent Lamp(EEFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 31.51 inch diagonally measured active display area with WXGA resolution (768 vertical by 1366 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in Horizontal stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus presenting a palette of more than 16.7M(true) colors.

It has been designed to apply the 8-bit 1-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	31.51 inches(800.4mm) diagonal
Outline Dimension	760.0 mm(H) x 450.0 mm(V) x 43.0 mm(D) (Typ.)
Pixel Pitch	510.75//m x 170.25//m x RGB
Pixel Format	1366 horiz. by 768 vert. pixels RGB stripe arrangement
Color Depth	8bit, 16,7 M colors
Luminance, White	500 cd/m ² (Center 1 point) (Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total 87.8Watt (Typ.) (Logic=3.8 W, Back Light= 84W @ with Inverter)
Weight	5,800g(Typ.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H), anti-glare treatment of the front polarizer (Haze 10%)

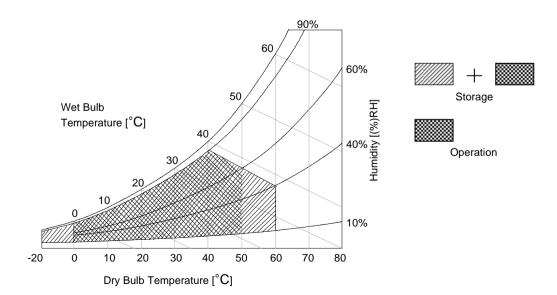
2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value		Unit	Remark	
Γc	raiametei		Min	Max	Unit	Remark	
Power Input Voltage	LCM	VLCD	-0.3	+14.0	Vdc	at 25 \pm 2 °C	
B/L Input voltage	Operating Voltage (one side)	Vop	680	1260	V[RMS]	at 25 ± 2 °C Burst Dimming Duty 100%	
Operating Ter	Operating Temperature Storage Temperature		0	+50	°C		
Storage Temp			-20	+60	°C	Note 1.2	
Operating Ambient Humidity		Нор	10	90	%RH	Note 1,2	
Storage Humi	Storage Humidity		10	90	%RH		

- Notes : 1. Temperature and relative humidity range are shown in the figure below.
 - Wet bulb temperature should be 39 °C Max. and no condensation of water.
 - 2. Gravity mura can be guaranteed under 40 $^\circ\!C$ condition.



3. Electrical Specifications

3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit.

The other Is used for the EEFL backlight and inverter circuit.

Table 2. ELECTRICAL CHARACTERISTICS

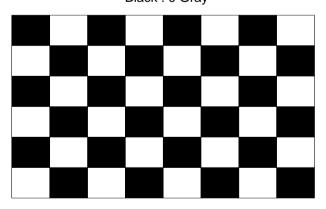
Parameter	Symbol	Value			Unit	Note		
	Cymbol	Min	Тур	Max	Onic	11010		
Circuit :								
Power Input Voltage	V _{LCD}	10.8	12.0	13.2	V _{DC}			
Dower Input Current		-	311	405	mA	1		
Power Input Current	LCD	-	270	360	mA	2		
Power Consumption	P _{LCD}	-	3.73	4.75	Watt	1		
Rush current	I _{RUSH}	-	-	3.0	А	3		

Notes : 1. The specified current and power consumption are under the V_{LCD}=12.0V, 25 \pm 2°C, f_V=60Hz

condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.

2. The current is specified at maximum current pattern.

3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).



White : 255 Gray Black : 0 Gray

Mosaic Pattern(8 x 6)

Table 3. ELECTRICAL CHARACTERISTICS for IPB & Lamp

Parameter		Symbol		Values		Unit	Notes
		Symbol	Min	Тур	Max		
Backlight Assembly :							
Operating Voltage (one side,fBL=63KHz, IBL= 9)3mArms))	VBL	850	1040	1170	V _{RMS}	1, 2
Operating Current (one side)	IBL	78	93	102	mA _{RMS}	1
Established Starting	℃		-	-	1080		1.0
Voltage (one side)	25 ℃	- Vs	-	-	900	V _{RMS}	1, 3
Operating Frequency		fBL	61	63	65	kHz	4
Striking Time		S TIME	-	-	5	sec	3
Power Consumption		PBL		84		Watt	6
Burst Dimming Duty		PWM duty	20		100	%	9
Burst Dimming Frequency		1/T	98	-	182	Hz	9
Parameter		Symbol	Values		Unit	Neteo	
Falameter		Symbol	Min	Тур	Max		Notes
Lamp : APPENDIX-V							
Lamp Voltage (one side)		VLAMP	730	1110	1180	V _{RMS}	2
Lamp Current (one side)		ILAMP	3	7.75	8.5	mA _{RMS}	
Discharge Stabilization Time)	Ts	-	-	3	Min	5
Lamp Frequency		f lamp	40	63	80	KHz	
Lamp Temperature		TLAMP			130	°C	
Established Starting	0 ℃	Vs			1080		0
Voltage (one side)	25 ℃	Vs			930	V _{RMS}	3
Life Time		i i	50,000	1		Hrs	7

Notes : The design of the inverter must have specifications for the lamp in LCD Assembly.

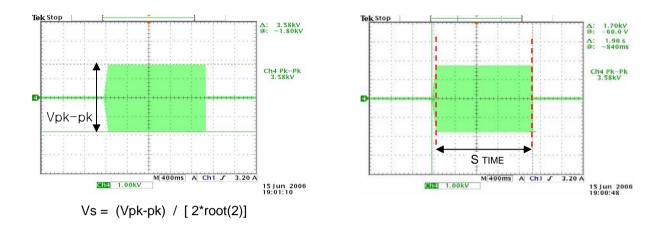
The electrical characteristics of inverter are based on High-High Driving type.

The performance of the lamps in LCM, for example life time or brightness, is extremely influenced by the characteristics of the DC-AC inverter. So, all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure unwanted lighting caused by the mismatch of the lamp and the inverter (no lighting, flicker, etc) has never been occurred. When you confirm it, the LCD– Assembly should be operated in the same condition as installed in your instrument.

Do not attach a conductive tape to lamp connecting wire. If you attach conductive tape to the lamp wire, not only luminance level can be lower than typical one but also inverter operate abnormally on account of leakage current which is generated between lamp wire and conductive tape.

1. Specified values are defined for a Backlight Assembly.(IBL : 12 lamp, 7.75mA/Lamp)

- 2. Operating voltage is measured at $25 \pm 2^{\circ}$ C(after 2hr.aging). The variance range for operating voltage is $\pm 10\%$.
- 3. The established starting voltage [Vs] should be applied to the lamps for more than Striking time (S TIME) for start-up. Inverter open voltage must be more than established starting voltage. Otherwise, the lamps may not be turned on. The used lamp current is typical value.

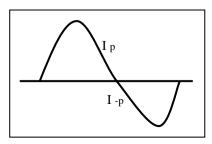


- 4. Lamp frequency may produce interference with horizontal synchronous frequency and as a result this may cause beat on the display. Therefore, lamp frequency shall be as away possible from the horizontal synchronous frequency and its harmonics range in order to prevent interference.
- 5. The brightness of the lamp after lighted for 5minutes is defined as 100%.
 T_S is the time required for the brightness of the center of the lamp to be not less than 95% at typical current.
- The screen of LCD module may be partially dark by the time the brightness of lamp is stable after turn on. 6. Maximum level of power consumption is measured at initial turn on.
- Typical level of power consumption is measured after 2hrs aging at $25 \pm 2^{\circ}$ C.
- 7. The life time is determined as the time at which brightness of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at $25 \pm 2^{\circ}$ C, based on duty 100%.
- 8. The output of the inverter must have symmetrical (negative and positive) voltage and current waveform (Unsymmetrical ratio is less than 10%). Please do not use the inverter which has not only unsymmetrical voltage and current but also spike wave.

Requirements for a system inverter design, which is intended to achieve better display performance, power efficiency and more reliable lamp characteristics.

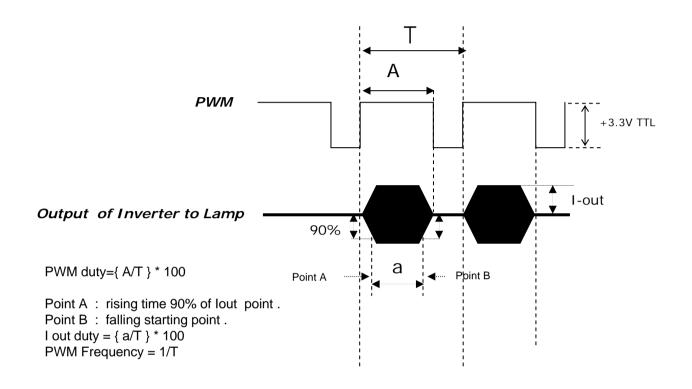
It can help increase the lamp lifetime and reduce leakage current.

- a. The asymmetry rate of the inverter waveform should be less than 10%.
- b. The distortion rate of the waveform should be within $\sqrt{2}$ $\pm10\%.$
 - * Inverter output waveform had better be more similar to ideal sine wave.



* Asymmetry rate: $|I_{p} - I_{-p}| / I_{op} \times 100\%$ * Distortion rate I_{p} (or I_{-p}) / I_{op}

 9. The reference method of burst dimming duty ratio.
 It is recommended to use synchronous V-sync frequency for prevent waterfall (Vsync x 2 =Burst Frequency)



- * We recommend not to be much different between PWM duty and lout duty .
- * Dimming current output rising and falling time may produce humming and inverter trans' sound noise.
- * Burst dimming duty should be 100% for 1second after turn on
- ※ Equipment

Oscilloscope :TDS3054B(Tektronix) Current Probe : P6022 AC (Tektronix) High Voltage Probe: P5100(Tektronix)

- 10. The Cable between the backlight connector and its inverter power supply should be connected directly with a minimized length. The longer cable between the backlight and the inverter may cause the lower luminance of lamp and may require more higher starting voltage (Vs).
- 11. The operating current must be measured as near as backlight assembly input.
- 12. The operating current unbalance between left and right must be under typ \pm 10%.

3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 30-pin connector is used for the module electronics and One connectors(14-pin) are used for the integral backlight system.

3-2-1. LCD Module

-LCD Connector(CN1) : FI-X30SSL-HF (Manufactured by JAE) or Equivalent -Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No.	Symbol	Description	Note
1	VLCD	Power Supply +12.0V	
2	VLCD	Power Supply +12.0V	
3	VLCD	Power Supply +12.0V	
4	VLCD	Power Supply +12.0V	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
7	GND	Ground	
9	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	
10	OPC Enable	OPC Enable('L' = Disable, 'H' = Enable)	
11	GND	Ground	
12	RA-	LVDS Receiver Signal(-)	
13	RA+	LVDS Receiver Signal(+)	
14	GND	Ground	
15	RB-	LVDS Receiver Signal(-)	
16	RB+	LVDS Receiver Signal(+)	
17	GND	Ground	
18	RC-	LVDS Receiver Signal(-)	
19	RC+	LVDS Receiver Signal(+)	
20	GND	Ground	
21	RCLK-	LVDS Receiver Clock Signal(-)	
22	RCLK+	LVDS Receiver Clock Signal(+)	
23	GND	Ground	
24	RD-	LVDS Receiver Signal(-)	Ì
25	RD+	LVDS Receiver Signal(+)	
26	GND	Ground	
27	OPC OUT	OPC Output (From LCM)	
28	EXT_VBR	External VBR (From System)	
29	GND	Ground	
30	GND	Ground	5

Notes :

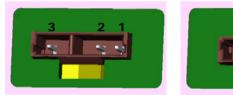
- 1. All GND(ground) pins should be connected together to the LCD module's metal frame.
- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the EIA 644 Standard.
- 4. Specific pins(pin No. #10,#27,#28) are used for OPC function of the LCD module. If not used, these pins are no connection. (Please see the Appendix V for more information.)
- 5. Specific pin No. #30 is used for "No signal detection" of system signal interface. It should be GND for NSB(No Signal Black) during the system interface signal is not. If this pin is "H", LCD Module displays AGP(Auto Generation Pattern).

3-2-2. Backlight Module

[Master]	[Slave]
1) Balance Connector	1) Balance Connector
: 65002WS-03 (manufactured by YEONHO)or equivalent	: 65002WS-03 (manufactured by YEONHO)or equivalent
2) Mating Connector	2) Mating Connector
: 65002HS-03 (manufactured by YEONHO) or equivalent	. : 65002HS-03 (manufactured by YEONHO) or equivalent.
Table 5. BACKLIGHT CONNECTOR PIN CONFIGURA	ATION(CN2,CN3)

	No	Symbol	Symbol Master Slave		Note
	1	H_Input	High_Input	High_Input	
	2	H_Input	High_Input	High_Input	
Γ	3	FB	NC	NC	

Rear view of LCM



Master

Slave

3-3. Signal Timing Specifications

Table 6-1 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

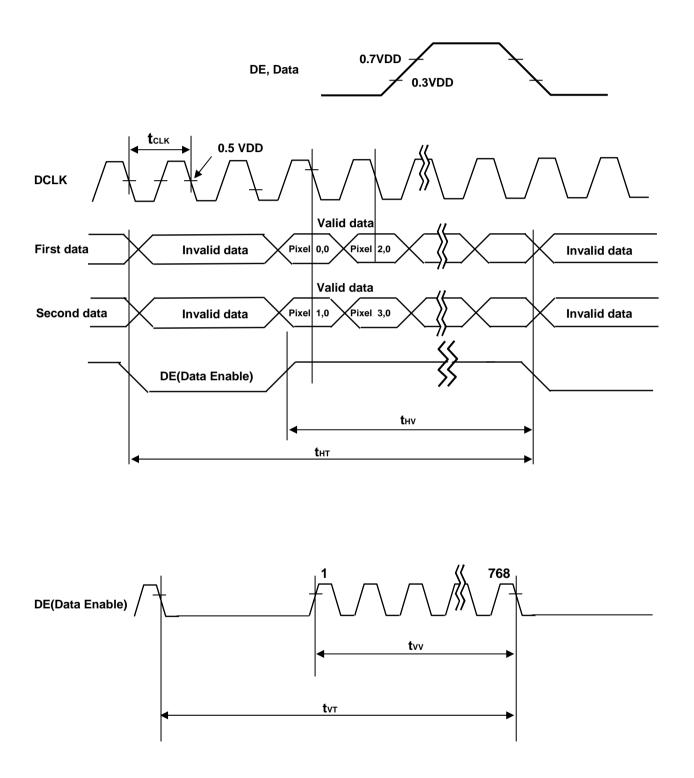
[DE (Data Enable) Only]

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Period	tCLK	12.5	13.8	15.8	ns	
DCLK	Frequency	-	63	72.4	80	MHz	
	Period	tH⊤	1456	1528	1920	tCLK	
	Horizontal Valid	tH∨	1366	1366	1366	t CLK	
	Horizontal Blank	-	tHP- tH∨	162	tHP- tHV		
Hsync	Frequency	fн	45	47.4	50	KHz	
	Width	twн	-	32	-	tCLK	
	Horizontal Back Porch	tнвр	24	50	-		
	Horizontal Front Porch	tHFP	40	80	-		
	Period	t∨⊤	776 (894)	790 (948)	1063 (1008)	tHP	
	Vertical Valid	t∨∨	768	768	768	tHP	
	Vertical Blank	-	tvp-tvv	22	tvp-tvv	tHP	
Vsync	Frequency	f∨	57 (47)	60 (50)	63 (53)	Hz	Note 1) NTSC : 57~63Hz
	Width	tw∨	-	5 (12)	-	tHP	(PAL : 47~53Hz)
	Vertical Back Porch	tVBP	5	15 (128)	-	Hz	
	Vertical Front Porch	tVFP	1	2 (40)	-	tHP	

Note :

- 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode). If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.
- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency
- 3. Timing should be set based on clock frequency.

3-4. Signal Timing Waveforms



3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

													Inpu	ut Co	olor	Data	a									
	Color					RE	D							GRE	EEN							BL	UE			
				B							MS							SB								SB
			R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0		B6	B5	B4	B3	B2	B1	B0
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Basic	Green (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																										
	RED (254)		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																										
	GREEN (254)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																										
	BLUE (254)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

3-6. Power Sequence

3-6-1. LCD Driving circuit

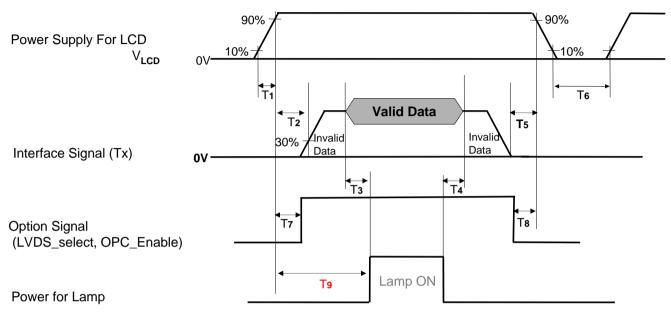


Table 9. POWER SEQUENCE

Deremeter		l la it	Natas			
Parameter	Min	Тур	Max	Unit	Notes	
T1	0.5	-	20	ms		
T2	0.5	-	-	ms	4	
Т3	200	-	-	ms	3	
T4	200	-	-	ms	3	
T5	0	-	-	ms		
Т6	2.0	-	-	s	5	
T7	0.5	-	T2	ms	4	
Т8	0	-	-	ms	4	
Т9	T2 + T3	-	5	S		

- Note: 1. Please avoid floating state of interface signal at invalid period.
 - 2. When the interface signal is invalid, be sure to pull down the power supply V_{LCD} to 0V.
 - 3. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
 - 4. If the on time of signals(Interface signal and Option signals) precedes the on time of Power(V_{LCD}), it will be happened abnormal display.
 - 5. T6 should be measured after the Module has been fully discharged between power off and on period.

4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and for 30 minutes in a dark environment at $25\pm2^{\circ}$ C. The values are specified at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method.

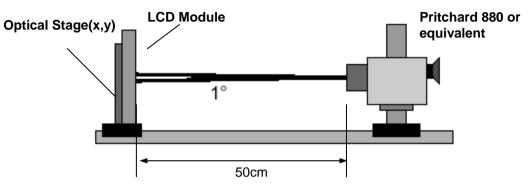


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 10. OPTICAL CHARACTERISTICS

```
Ta= 25±2°C, V<sub>LCD</sub>=12.0V, fv=60Hz, Dclk=72.4MHz, VBR-A =1.65V, Ext. VBR-B =100%
```

Dom	o motor	Queenal		Value		l locit	Nata
Para	ameter	Symbol	Min	Тур	Max	Unit	Note
Contrast Ratio		CR	800	1100	-		1
Surface Lumina	nce, white	L _{WH}	400	500		cd/m ²	2
Luminance Vari	ation	δ _{WHITE} 5P	-	-	1.3		3
Deenense Time	Gray-to-Gray	G to G	-	8	12	ms	4
Response Time	Uniformity	δ _{G TO G}	-	-	1		5
		Rx		0.639			
	RED	Ry		0.333			
Color Coordinates [CIE1931]	ODEEN	Gx		0.292			
	GREEN	Gy	Тур -0.03	0.603	Тур +0.03		1
		Bx		0.145			
	BLUE	By		0.063			
		Wx		0.279			1
	WHITE	Wy		0.292			
Viewing Angle (CR>10)						
Xá	axis, right(∳=0°)	θr	89	-	-		
Xa	x axis, left (φ=180°)		89	-	-		
уа	axis, up (φ=90°)	θu	89	-	-	degree	6
y a	axis, down (φ=270°)	θd	89	-	-		
Gray Scale				2.2			7

Notes :1. Contrast Ratio(CR) is defined mathematically as :

CR = Surface Luminance at all white pixels

Surface Luminance at all black pixels It is measured at center 1-point.

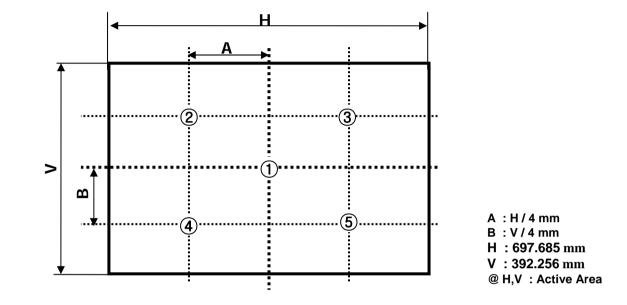
- Surface luminance are determined after the unit has been 'ON' and 30min after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance , δ WHITE is defined as : δ WHITE(5P) = Maximum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5}) / Minimum(L_{on1},L_{on2}, L_{on3}, L_{on4}, L_{on5})

Where $L_{_{0n1}}$ to $L_{_{on5}}$ are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.

- 4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 3. (N<M)
 ※ G to G Spec stands for average value of all measured points.
- 5. Gray to Gray Response time uniformity is Reference data. Please see Appendix VIII.
- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 7. Gray scale specification Gamma Value is approximately 2.2. For more information, see the Table 11.

Gray Level	Luminance [%] (Typ.)
LO	0.09
L15	0.32
L31	1.10
L47	2.60
L63	4.90
L79	8.10
L95	12.1
L111	16.7
L127	21.6
L143	28.0
L159	35.4
L175	43.9
L191	53.3
L207	64.1
L223	75.8
L239	88.0
L255	100

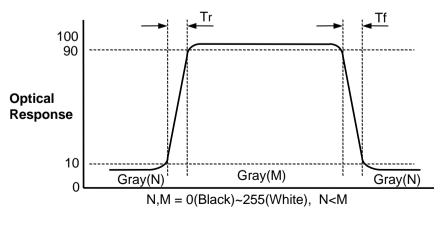
Table 11. GRAY SCALE SPECIFICATION



Measuring point for surface luminance & measuring point for luminance variation.

FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".





Dimension of viewing angle range

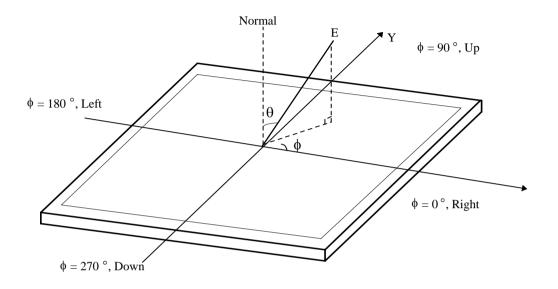


FIG. 4 Viewing Angle

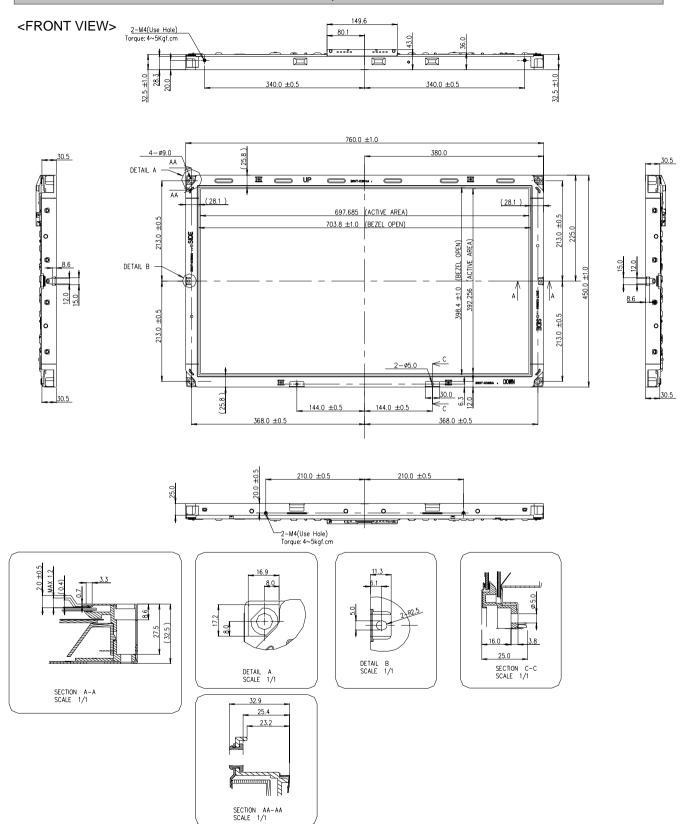
5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

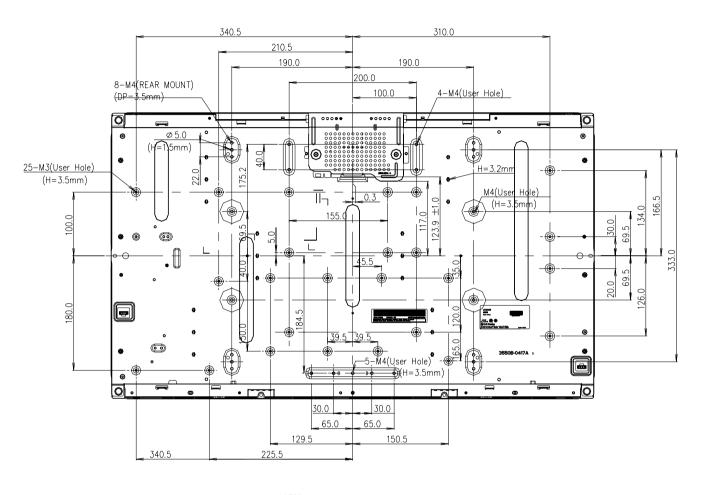
Table 12.	MECHANICAL	CHARACTERISTICS
-----------	------------	------------------------

Item		Value		
	Horizontal	760.0mm		
Outline Dimension	Vertical	450.0 mm		
	Depth	43.0 mm		
	Horizontal	703.8mm		
Bezel Area	Vertical	398.4mm		
Active Display Area	Horizontal	697.685mm		
Active Display Area	Vertical	392.256mm		
Weight	5,800 g(Typ.), 6,100g(Max)			

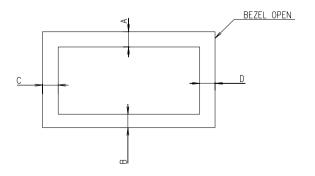
Note : 1.Please refer to a mechanic drawing in terms of tolerance at the next page.



<REAR VIEW>



NOTES 1.UNSPECIFIED TOLERANCES TO BE ±0.5MM 1.UNSPECIFIED TOLERANCES TO BE ± 0.5 MM 2.I/F Connector Specification. - FI-X30SSL-HF(JAE) or Equivalent 3.TILT AND PARTIAL DISPOSITION TOLERANCE OF DISPLAY AREA ARE AS FOLLOWING. (1) Y-DIRECTION: I A-B I \leq 1.5 (2) X-DIRECTION: I C-D I \leq 1.5



6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-300Hz Duration : X,Y,Z, 10 min One time each direction
6	Shock test (non-operating)	Shock level : 100G Waveform : half sine wave, 2ms Direction : $\pm X$, $\pm Y$, $\pm Z$ One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude operating storage / shipment	0 - 15,000 ft 0 - 40,000 ft

Note : Before and after Reliability test, LCM should be operated with normal function.

7. International Standards

7-1. Safety

- a) UL 60065, 7th Edition, dated June 30, 2003, Underwriters Laboratories, Inc., Standard for Audio, Video and Similar Electronic Apparatus.
- b) CAN/CSA C22.2, No. 60065:03, Canadian Standards Association, Standard for Audio, Video and Similar Electronic Apparatus.
- c) IEC60065:2001, 7th Edition CB-scheme and EN 60065:2002, Safety requirements for Audio, Video and Similar Electronic Apparatus.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) CISPR13 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment" CISPR22 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" International Special Committee on Radio Interference.
- c) EN55013 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
 EN55022 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" European Committee for Electro Technical Standardization.(CENELEC), 1988(Including A1:2000)

8. Packing

8-1. Designation of Lot Mark

a) Lot Mark



A,B,C : SIZE(INCH) D : YEAR F : PANEL CODE

H : ASSEMBLY CODE

E : MONTH G : FACTORY CODE I,J,K,L,M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

- a) LCM quantity in one pallet : 24 pcs
- b) Pallet Size : 1030mm X 870mm X 1210mm

9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

9-1. Mounting Precautions

- (1)You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for here here here any sloth (Some competing are detrimented).

Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)

- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}(\text{Over and under shoot voltage})$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
- (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) Partial darkness may happen during 3~5 minutes when LCM is operated initially in condition that luminance is under 40% at low temperature (under 5°C). This phenomenon which disappears naturally after 3~5 minutes is not a problem about reliability but LCD characteristic.

9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normalhexane.

APPENDIX-I-1

Required signal assignment for Flat Link Transmitter(Pin9="L")

Host System	C	S90C385				Timing
24 Bit	or	Compatible	IS10	0-L30B	-C23	Controller
RED0	51					
RED1	52	TxOUT0-	48	12		RxIN0-
RED2		TxOUT0+	47	13	100Ω ≶	RxIN0+
RED3	55					
RED4	56					
RED5	3	TxOUT1-	46	15		RxIN1-
RED6	50	TxOUT1+	45	16	100요 ≶	RxIN1+
RED7	2					
GREEN0	4		40			
GREEN1	6	TxOUT2-	42	18	4000 2	RxIN2-
GREEN2	7	TxOUT2+	41	19	100요 ≶	RxIN2+
GREEN3	11					
GREEN4	12					
GREEN5	14	TxCLKOUT-	40	21		RxCLKIN-
GREEN6	8	TxCLKOUT+	39	22	100요 ≶	RxCLKIN+
GREEN7	10					
BLUE0	15					
BLUE1	19	TxOUT3-	38	24	4000 2	RxIN3-
BLUE2	20	TxOUT3+	37	25	100Ω ≶	RxIN3+
BLUE3	22					
BLUE4	23			9		
BLUE5	24			30		LCD Test
BLUE6	16					
BLUE7	18					
Hsync	27					
Vsync	28		ର ଜ	1		
Data Enable			GND			
CLOCK	31					Iodule

Notes:

- 1. The LCD module uses a 100 $Ohm(\Omega)$ resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (DS90C385 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

APPENDIX-I-2

Required signal assignment for Flat Link Transmitter(Pin9="H")

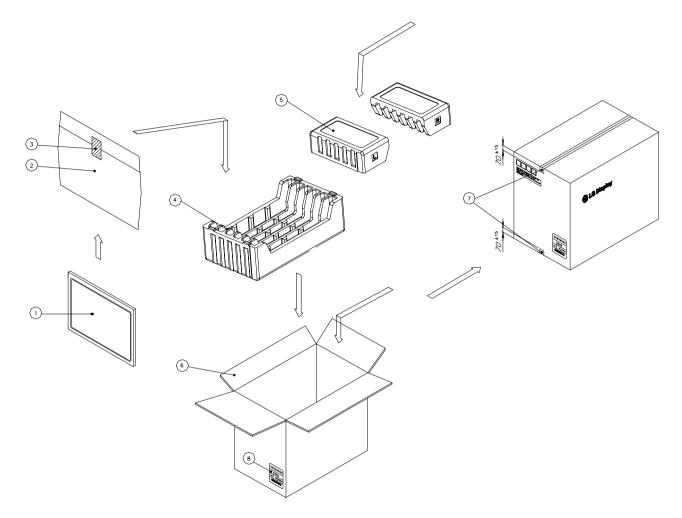
Host System						Timing
24 Bit	or	Compatible	IS10	0-L30E	3-C23	Controller
RED0	50		40		1	
RED1	2	TxOUT0-	48	12	100Ω ≷	RxIN0-
RED2	51	TxOUT0+	47	13	10022 5	RxIN0+
RED3	52					
RED4	54		46			
RED5	55	TxOUT1-	46	15	100Ω ≷	RxIN1-
RED6	56	TxOUT1+	45	16	10022 5	RxIN1+
RED7	3					
GREEN0			42			
GREEN1	10	TxOUT2-	42	18	100Ω ≷	RxIN2-
GREEN2	4	TxOUT2+	41	19	10022 S	RxIN2+
GREEN3	6					
GREEN4	7					
GREEN5	11	TxCLKOUT-	40	21	- (00 0 >	RxCLKIN-
GREEN6	12	TxCLKOUT+	39	22	100요 ≶	RxCLKIN+
GREEN7	14					
BLUE0	16					
BLUE1	18	TxOUT3-	38	24		RxIN3-
BLUE2	15	TxOUT3+	37	25	100Ω ≶	RxIN3+
BLUE3	19					
BLUE4	20			9		
BLUE5	22			30		LCD Test
BLUE6	23					
BLUE7	24					
Hsync	27					
Vsync	28		< 0	I		
Data Enable			GND Vcc			
CLOCK	31		J		LCD M	odule

Notes:

- 1. The LCD module uses a 100 $Ohm(\Omega)$ resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (DS90C385 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

APPENDIX- ||-1

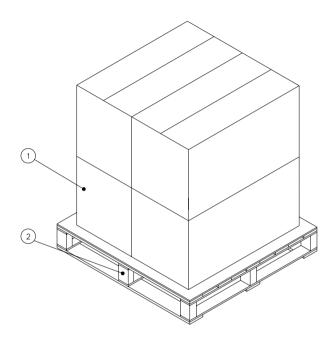
■ LC320WXE-SBB1 Packing Ass'y

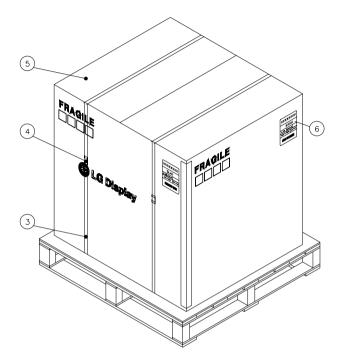


NO.	DESCRIPTION	MATERIAL
1	LCD MODULE	
2	BAG	AL
3	TAPE	MASKING 20MM X 50M
4	PACKING, BOTTOM	EPS
5	PACKING, TOP R_L	EPS
6	BOX	PAPER_DW3
7	TAPE	OPP 70MMX300M
8	LABEL	YUPO PAPER 100X100

APPENDIX- ||-2

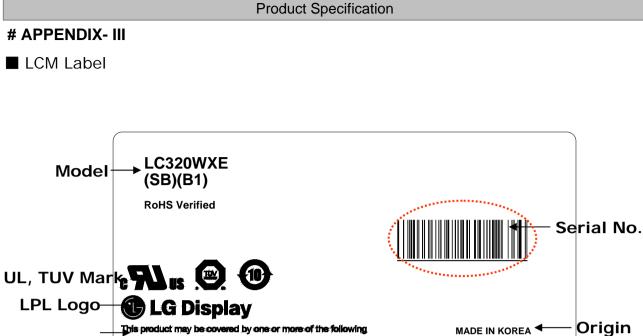
■ LC320WXE-SBB1 Pallet Ass'y





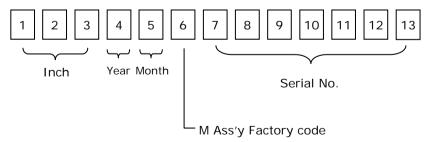
Box quantity per pallet: 4ea Pallet size: L1030 x W870 x H1210

NO.	DESCRIPTION	MATERIAL
1	PACKING ASS'Y	
2	PALLET	Plywood
3	BAND	PP
4	CLIP, BAND	STEEL
5	ANGLE, PACKING	PAPER (SWR4)
6	LABEL	PAPER



This product may be covered by one or more of the following US Patent Nos. 5,041,823 ; 5,061,920 ; 5,280,371 ; 5,835,139





APPENDIX- IV

Box Label

LC320WXE							
SBB1							
6 PCS	001/01-01						
MADE IN	KOREA	RoHS Verified					

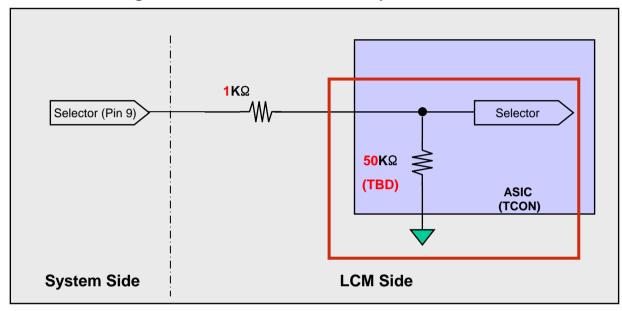
Pallet Label

LC320WXE						
SBB1						
24 PCS	001/01-01					
MADE I	MADE IN KOREA					

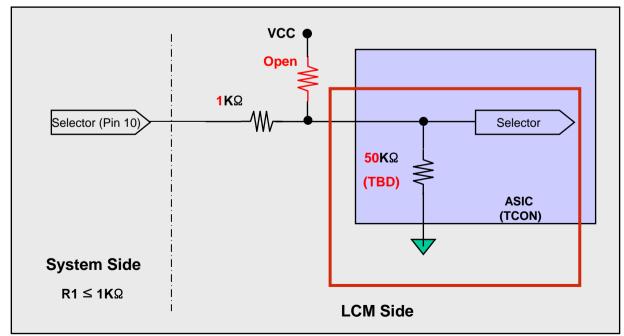
APPENDIX- V

Option Pin Circuit Block Diagram

Circuit Block Diagram of LVDS Format Selection pin



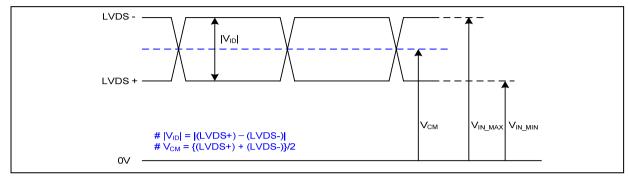
Circuit Block Diagram of OPC_Enable pin



APPENDIX- VI

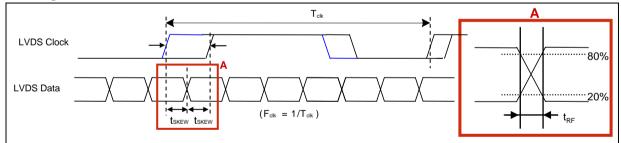
LVDS Input characteristics

1. DC Specification



Description	Symbol	Min	Мах	Unit	Notes
LVDS Single end Voltage	V _{ID}	200	600	mV	-
LVDS Common mode Voltage	V _{CM}	1.1	1.5	V	-
LVDS Input Voltage Range	V _{IN}	0.7	1.8	V	-
Change in common mode Voltage	ΔV_{CM}		150	mV	-

2. AC Specification



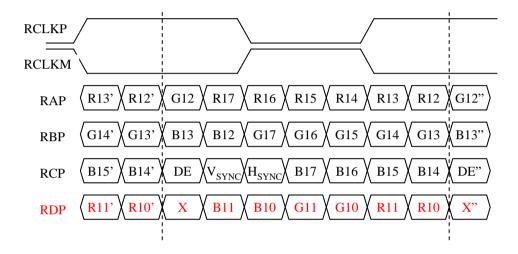
Description	Symbol	Min	Мах	Unit	Notes
LVDS Clock to Data Skew Margin	t _{skew}		480	ps	$78MHz > Fclk \ge 70MHz$
LVDS Clock/DATA Rising/Falling time	t _{RF}	260	(0.3*T _{clk})/7	ps	-

Note. All Input levels of LVDS signals are based on the EIA 644 Standard.

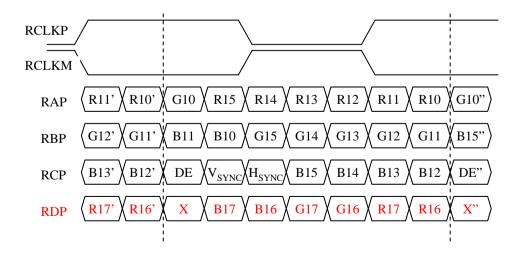
APPENDIX- VII

LVDS Data-Mapping info. (8bit)

LVDS Select : "H" Data-Mapping (JEIDA format)



LVDS Select : "L" Data-Mapping (VESA format)



APPENDIX- VIII

Gray to Gray Response Time Uniformity

This is only the reference data of G to G and uniformity for LC320WXE-SBB1 model.

1. G to G Response Time :

Response time is defined as Figure3 and shall be measured by switching the input signal for "Gray (N)" and "Gray(M)".(32Gray Step at 8bit)

2. G to G Uniformity

The variation of G to G Uniformity , δ G to G is defined as :

G to G Uniformity = $\frac{Maximum(GtoG) - Typical(GtoG)}{Typical(GtoG)} \leq 1$

*Maximum (GtoG) means maximum value of measured time (N, M = 0 (Black) ~ 255(White), 32 gray step).

	0Gray	32Gray	64Gray		223Gray	255Gray
0Gray		TrR:0G→32G	TrR:0G→64G		TrR:0G→223G	TrR:0G→225G
32Gray	TrD:32G→0G		TrR:32G→64G		TrR:32G→223G	TrR:32G→255G
64Gray	TrD:64G→0G	TrD:64G→32G			TrR:64G→223G	TrR:64G→255G
				/		
223Gray	TrD:223G→0G	TrD:223G→32G	TrD:223G→64G			TrR:223G→255G
255Gray	TrD:255G→0G	TrD:255G→32G	TrD:255G→64G		TrD:255G→223G	

3. Sampling Size : 2 pcs

4. Measurement Method : Follow the same rule as optical characteristics measurement.

5. Current Status

Below table is actual data of production on ---. --, 2008 (LGD RV Event Sample)

	G to G Respo	Liniformity	
	Min.	Max.	Uniformity
# 1	8.8	8.9	
# 2	8.8	8.9	

