



LC1486C

600mA High PSRR, Fast Response Linear Regulator

DESCRIPTION

LC1486C series is a group of positive voltage output, low power consumption, low dropout voltage regulator.

LC1486C can provide output value adjustable from 0.8V to 5.0V.

LC1486C includes high accuracy voltage reference, error amplifier, current limit circuit and output driver module with discharge capability.

LC1486C has excellent load and line transient response and good temperature characteristics, which can assure the stability of chip and power system. It uses trimming technique to guarantee output voltage accuracy within $\pm 2\%$. And it also provides fold back short-circuit protection, thermal shutdown and output current limit function.

LC1486C is available in SOT23-5 package which is lead free.

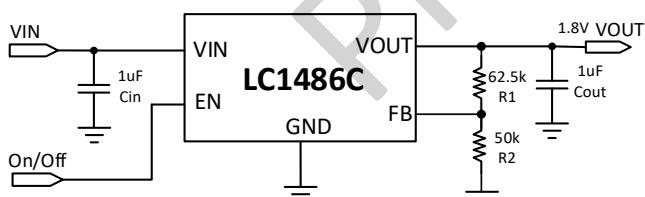
FEATURES

- Low power consumption: 65uA (Typ.)
- Maximum output current: 600mA
- Low dropout voltage:
 $360mV@I_{OUT}=600mA, V_{OUT}=3.3V$
- Build-in chip enable and discharge circuit
- Input voltage range: 2~6V
- Adjustable output from 0.8V to 5.0V
- Output voltage accuracy: $\pm 2\%$
- Output current limit
- Short circuit protection
- Over temperature protection

APPLICATIONS

- Power source for cellular phones and various kind of PCs
- Battery powered equipment
- Power management of MP3, PDA, DSC, Mouse, PS2 games
- Reference voltage source
- Regulation after switching power

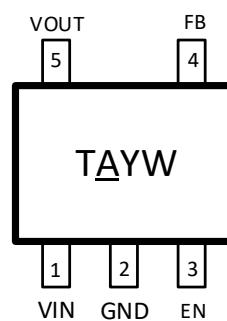
TYPICAL APPLICATION



Note:

$$1) V_{OUT} = V_{FB} * \left(1 + \frac{R_1}{R_2}\right), V_{FB} = 0.8V$$

PIN OUT & MARKING



SOT23-5

TA: Product code

YW: Date code (Year & Week)

ORDERING INFORMATION

Part No.	Package	Tape&Reel
LC1486CCB5TR	SOT23-5	3000pcs/reel

ABSOLUTE MAXIMUM RATING

Parameter	Value
Max input voltage	8V
Operating junction temperature(T_J)	125°C
Power dissipation	400mW
Package thermal resistance (θ_{JA})	220°C/W
Storage temperature(T_S)	-40°C -150°C
Lead temperature & time	260°C,10S

Note: Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.

RECOMMENDED WORK CONDITIONS

Parameter	Value
Input voltage range	2V to 6V
Ambient temperature	-40°C –85°C

ELECTRICAL CHARACTERISTICS

(Test Conditions: $C_{IN}=1\mu F$, $C_{OUT}=1\mu F$, $T_A=25^\circ C$, unless otherwise stated.)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
V_{IN}	Input voltage		2 ¹		6	V
V_{FB}	Regulated feedback voltage	$V_{IN}=3.3V$, $I_{OUT}=10mA$	0.784	0.8	0.816	V
V_{DROP}	Dropout voltage ²	$V_{OUT}=1.2V$, $I_{OUT}=600mA$		1020	1200	mV
		$V_{OUT}=1.8V$, $I_{OUT}=600mA$		630	750	mV
		$V_{OUT}=3.3V$, $I_{OUT}=600mA$		360	450	mV
$\frac{\Delta V_{out}}{\Delta V_{in} + V_{out}}$	Line regulation	$I_{OUT}=10mA$, $2.5V \leq V_{IN} \leq 6V$		0.05	0.2	%/V
ΔV_{out}	Load regulation	$V_{IN}=4.3V$, $V_{OUT}=3.3V$ $10mA \leq I_{OUT} \leq 600mA$		50	80	mV
I_Q	Supply current	$V_{IN}=V_{OUT}+1V$, $V_{IN}=V_{EN}$		65	100	uA
$I_{STANDBY}$	Supply current (standby)	$V_{IN}=V_{OUT}+1V$, $V_{EN}=GND$		0.1	1.0	uA
$\frac{\Delta V_{out}}{\Delta T + V_{out}}$	Output voltage temperature coefficient	$I_{OUT}=10mA$		±100		ppm/°C
PSRR	Ripple rejection	$F=1KHz$, Ripple=1Vp-p $V_{IN}=V_{OUT}+1V$		65		dB
I_{LIM}	Current limit	$V_{IN}=4.3V$, $V_{OUT}=3.3V$	600			mA
I_{SHORT}	Short current limit	$V_{IN}=5V$, $V_{OUT}=0V$		100		mA
RDISCHARGE	Discharge resistor	$EN=0$, $V_{OUT}=3V$		2K		Ω
V_{ENH}	EN input voltage "H"		1.5		V_{IN}	V
V_{ENL}	EN input Voltage "L"		0		0.4	V
T_{SD}	Thermal shutdown temp			150		°C
T_{SH}	Thermal shutdown hysteresis			25		°C

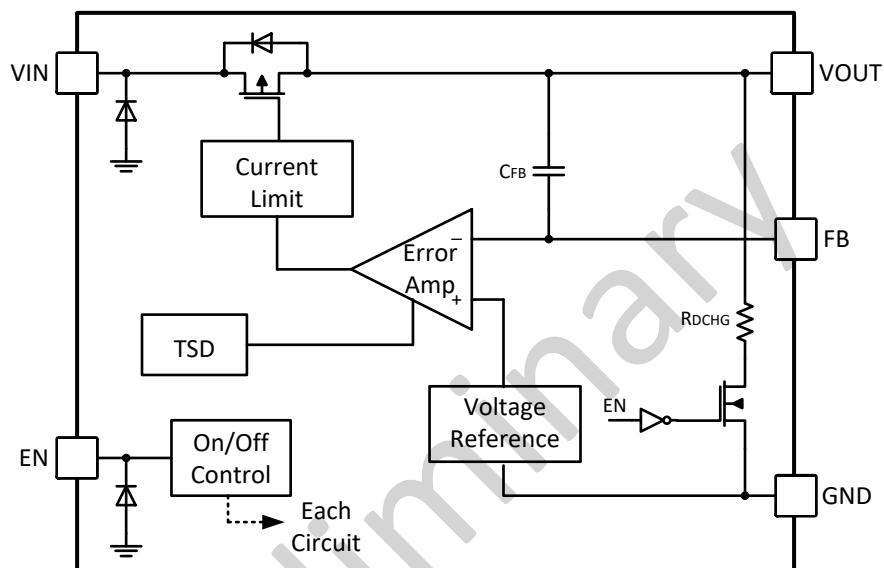
Note: 1) The output current capability depends on the input voltage and the minimum dropout voltage.

2) $V_{DROP}=V_{IN}-V_{OUT}$ when V_{OUT} drops below 98% of the normal V_{OUT} .

PIN DESCRIPTION

Pin #	Name	Description
1	VIN	Supply voltage input. Supply voltage can range from 2V to 6V.
2	GND	Ground pin
3	EN	Enable pin. This pin has an internal pull-down resistor. A logic low reduces the supply current to less than 1 μ A. Connect to IN for normal operation.
4	FB	Feedback pin. This is used to set the output voltage of the device.
5	VOUT	Output voltage

BLOCK DIAGRAM



EXPLANATION

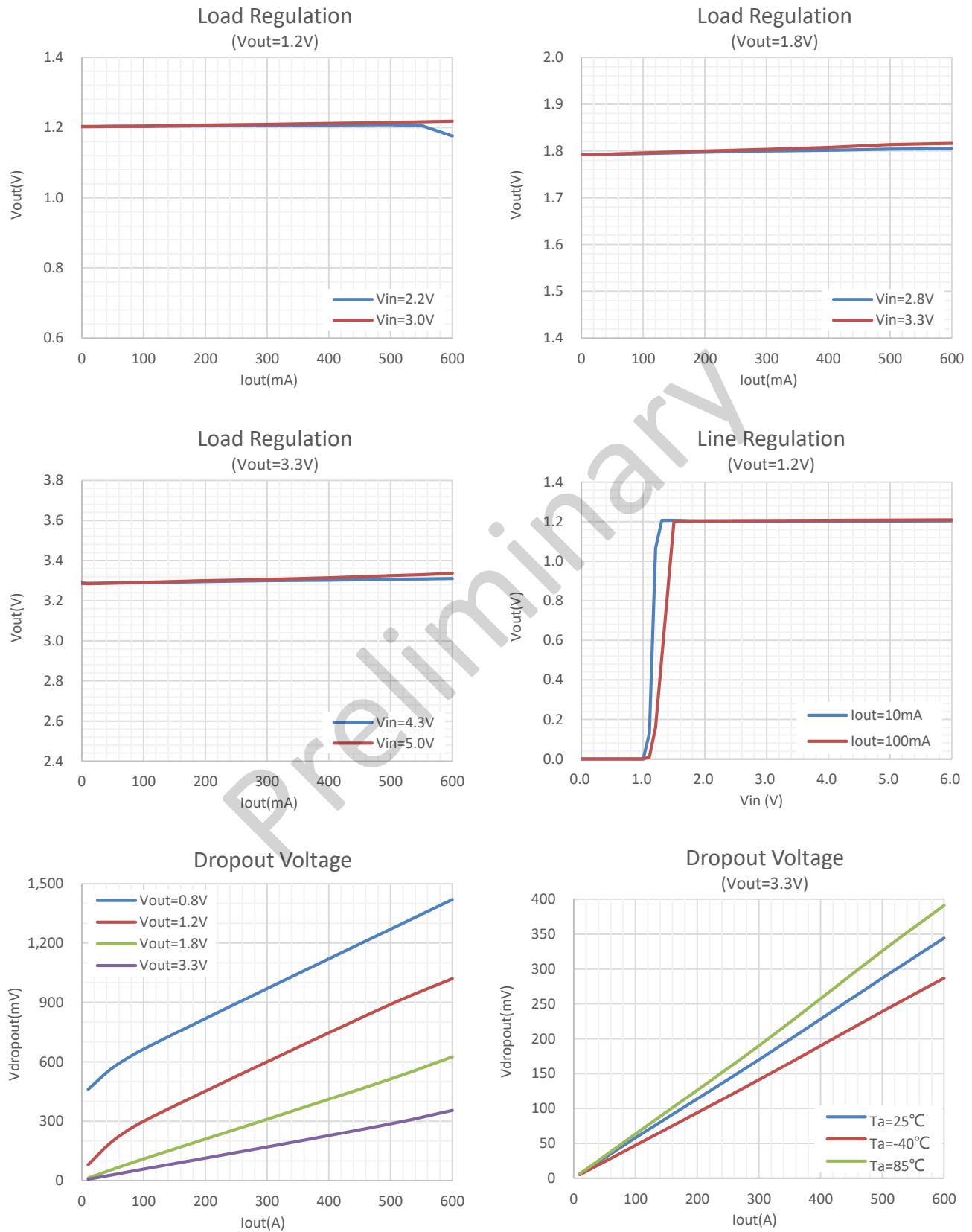
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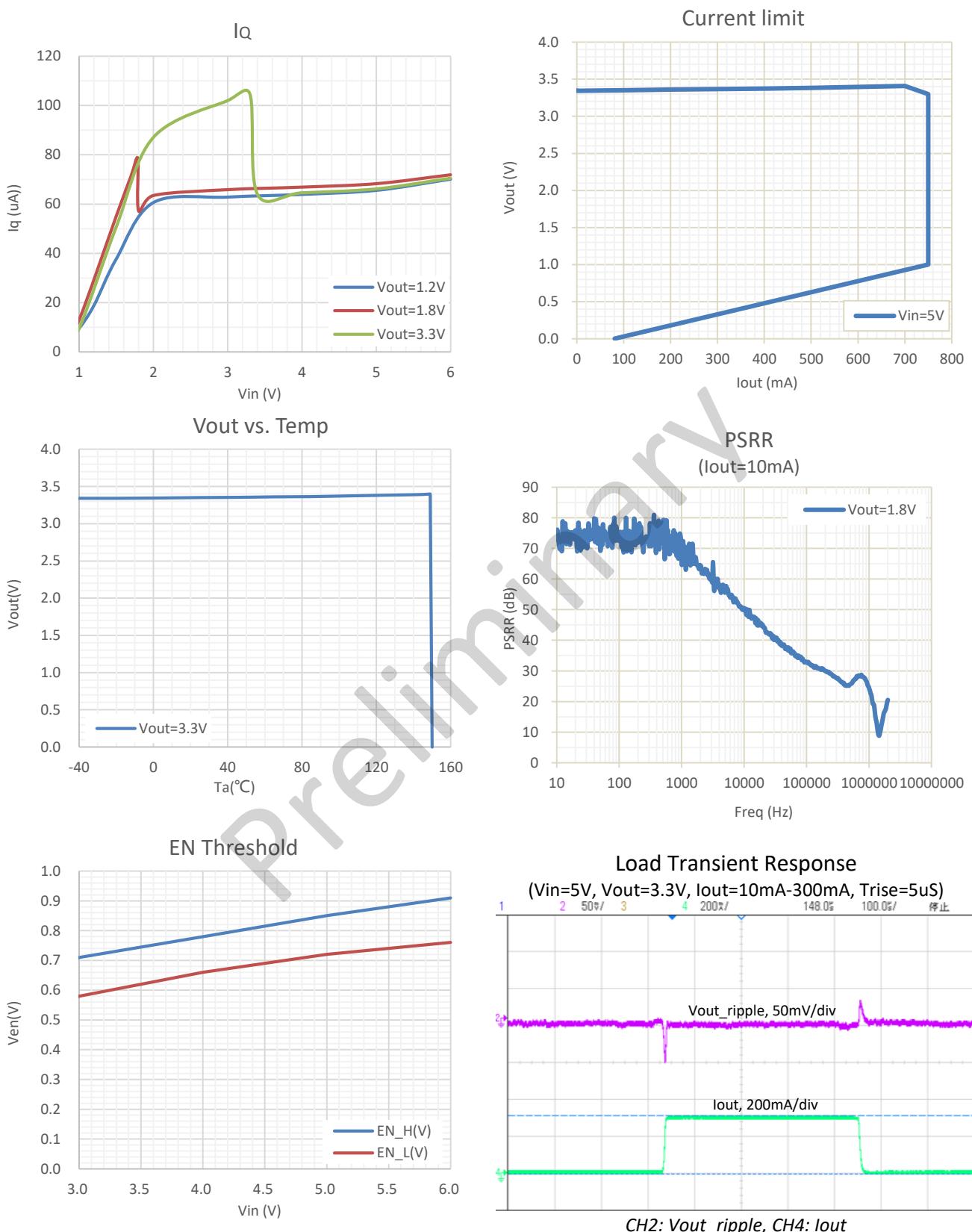
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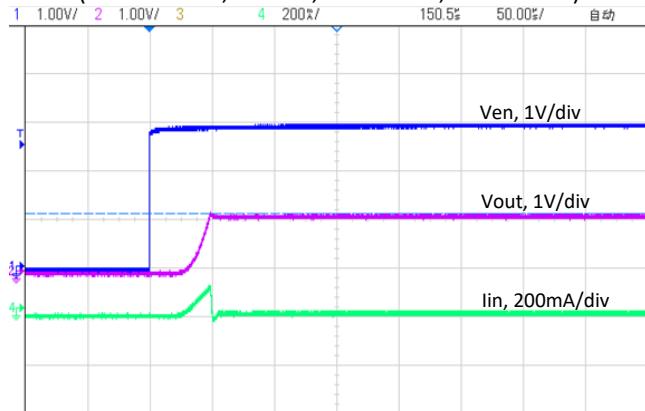
TYPICAL PERFORMANCE CHARACTERISTICS





EN Chip Enable Response

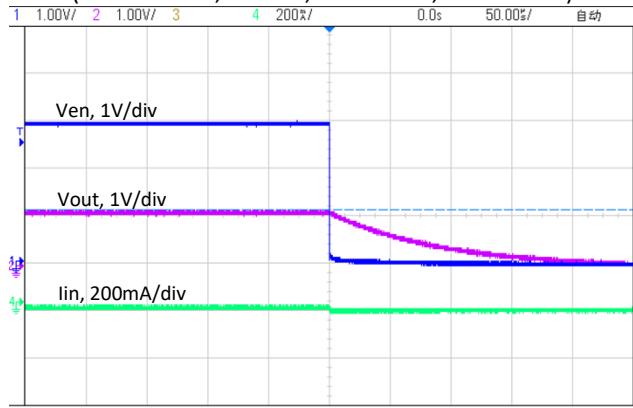
(EN=0V to 3V, Vin=5V, Vout=1.2V, Iout=10mA)



CH1: EN; CH2: Vout; CH4: lin

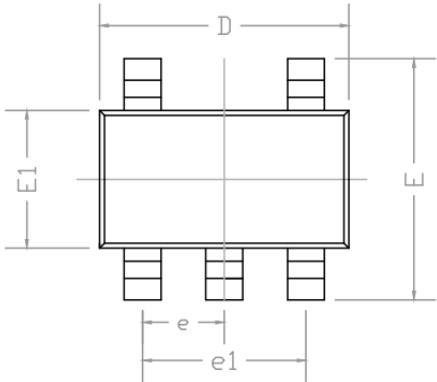
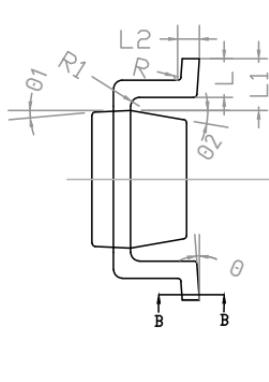
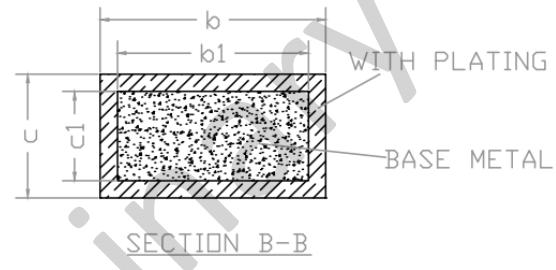
EN Chip Enable Response

(EN=3V to 0V, Vin=5V, Vout=1.2V, Iout=10mA)



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PACKAGE OUTLINE

Package	SOT-23-5	Devices per reel	3000pcs																																																																																																																																																																					
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