



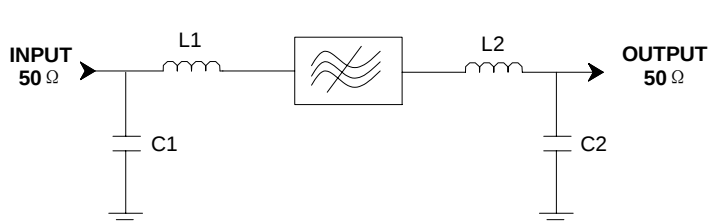
Specifications

Parameter		Unit	Minimum	Typical	Maximum
Center Frequency		MHz	139.85	140	140.15
Insertion Loss		dB	-	28.1	29
1 dB Bandwidth		MHz	-	9.55	-
3 dB Bandwidth		MHz	9.75	9.8	-
40 dB Bandwidth		MHz	-	10.6	-
50 dB Bandwidth		MHz	-	10.7	-
Passband Variation		dB	-	0.9	1.5
Absolute Delay		usec	-	3.93	4
Ultimate Rejection	(f0±5.3MHz)	dB	35	51	-
	(f0±5.5MHz)	dB	45	52	-
	(f0±5.9MHz)	dB	50	53	-
	(f0±9.9MHz)	dB	55	56	-
Material Temperature coefficient		KHz/°C	-13.16		
Ambient Temperature		°C	25		
Package Size		DIP3512 (35.2x12.7x5.2mm3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



$$L1=L2=33\text{nH}$$

$$C1=C2=47\text{pF}$$

Source / Load Impedance = 50 ohm

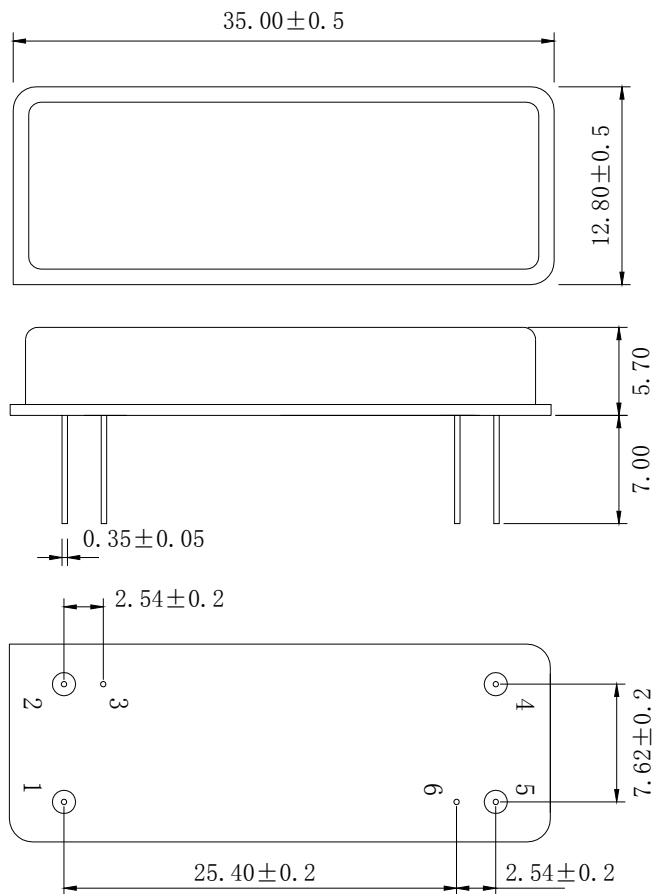
Notes - Component values may change depending on board layout.



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Package Dimension



Package:DIP3512

Unit:mm

Input 1
Output 5
Ground 2,3,4,6

Package: DIP3512

Unit: mm

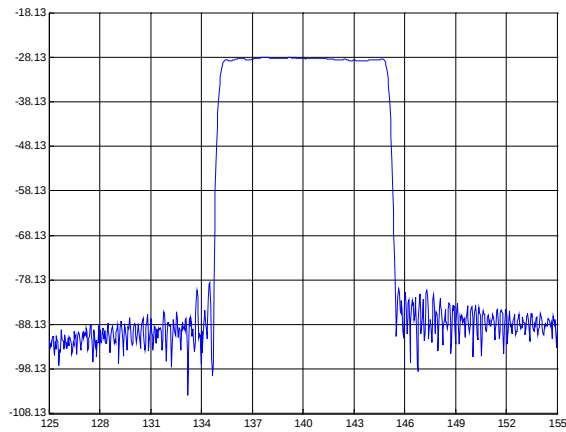


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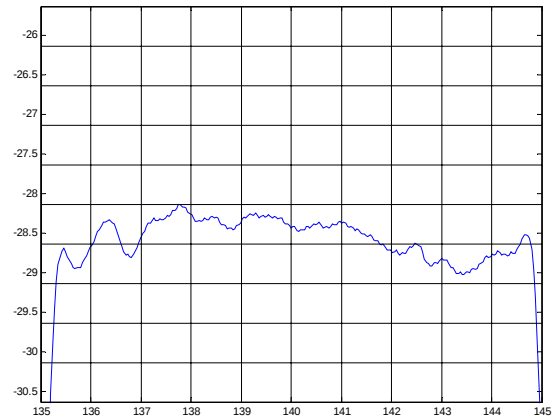
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Typical Performance

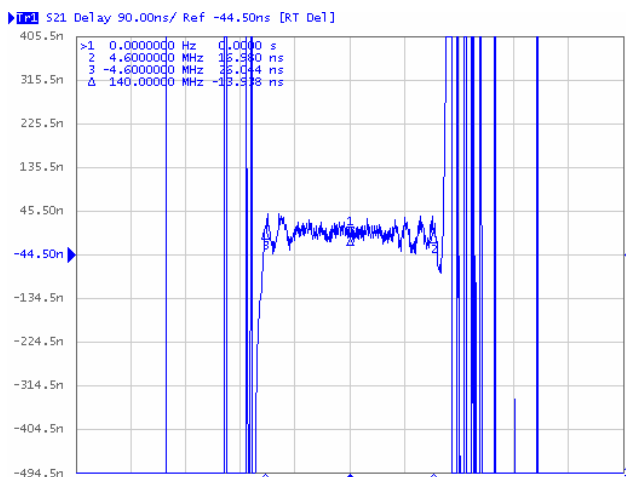
Frequency Respond



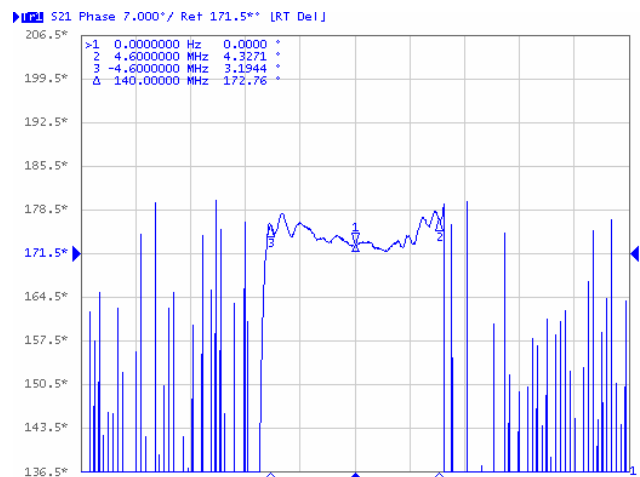
Passband Respond



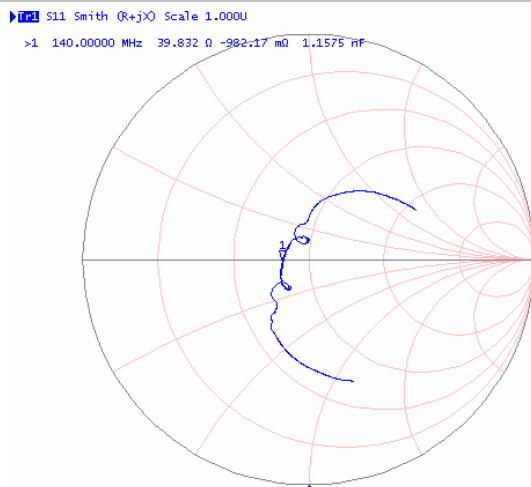
Group Delay Variation($f_0 \pm 4.6\text{MHz}$)



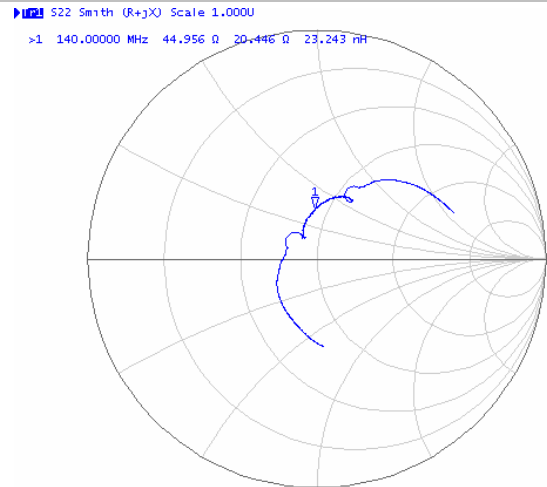
Phase Linearity($f_0 \pm 4.6\text{MHz}$)



Smith Chart S11



Smith Chart S22



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