

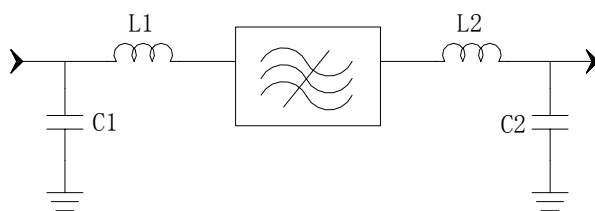
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz		327.5	
Insertion Loss	dB		14.8	16
1 dB Bandwidth	MHz	35	38.6	
3 dB Bandwidth	MHz		40.7	
35 dB Bandwidth	MHz		47.4	67
Passband Variation	dB		0.6	1
Ultimate Rejection($f_0 \pm 35\text{MHz}$)	dB	35	40	
Group Delay Variation	nsec		60	
Substrate Material			YZ	
Ambient Temperature	°C		25	
Package Size		SMD0705A (7x5x1.6mm ³)		

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1 = 27nH L2 = 27nH
C1 = 10pF C2 = 10pF

Source / Load Impedance = 50 ohm

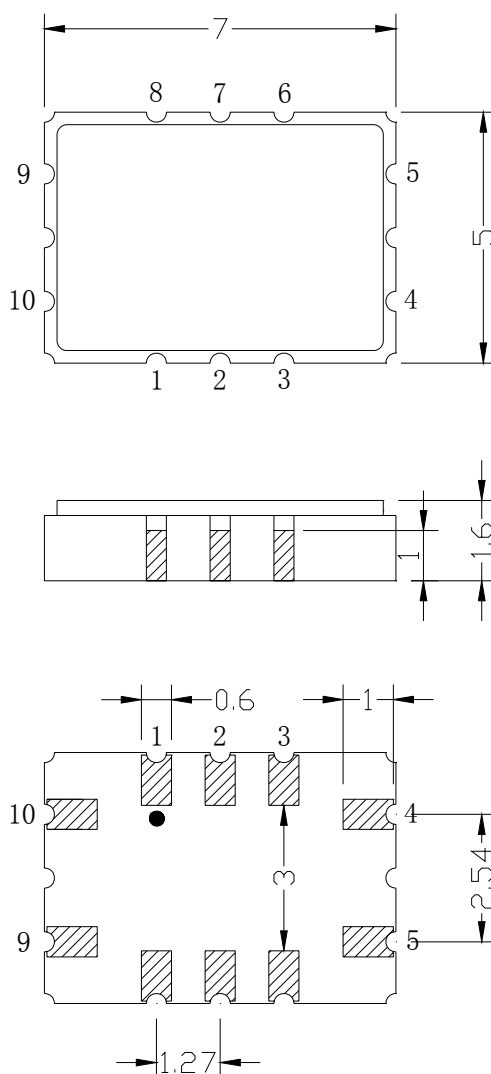
Notes - Component values may change depending
on board layout.



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Package Dimension



Input:9
Output:4

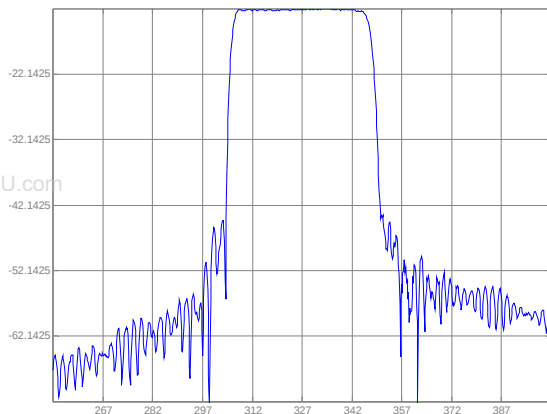


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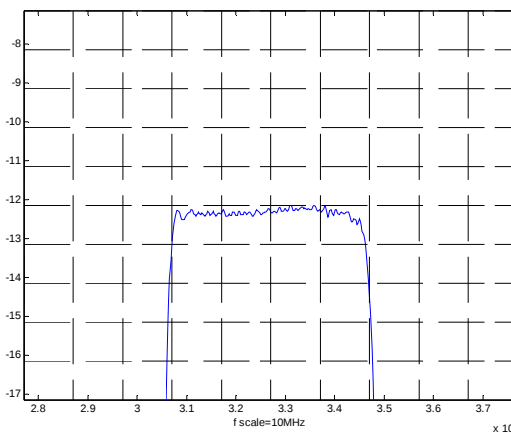
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Typical Performance

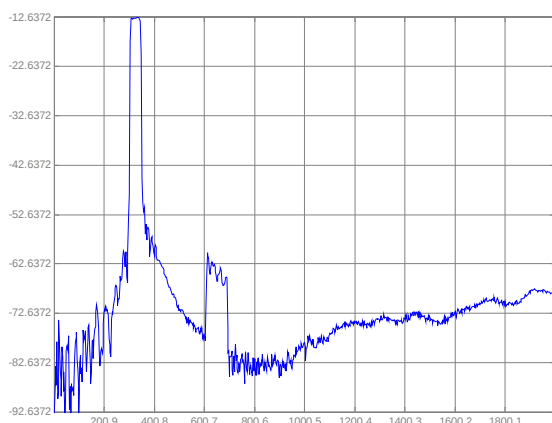
Frequency Respond



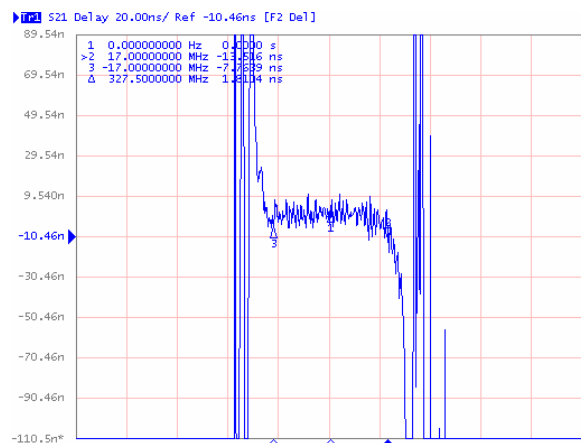
Passband Respond



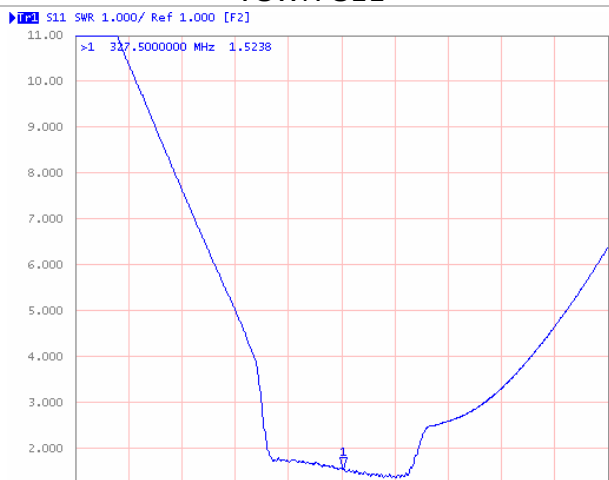
Wideband Respond



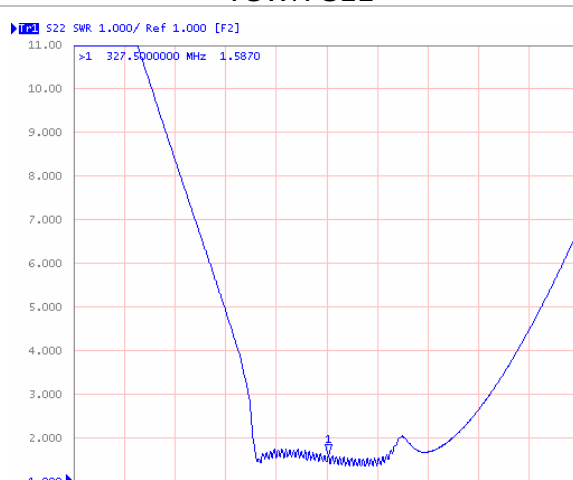
Group delay variation(f0±17MHz)



VSWR S11



VSWR S22



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