

SANYO: DIP14

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Ambient Temperature, Ta - °C

O3195YK/ 8227KI/8065MW/0071KI,TS No.880-1/3

LB1407,1417

Absoluto Mavimum Patings at	T -25°C					• .
Absolute Maximum Ratings at Maximum Supply Voltage	V _{CC} max	Pin l	-0.3 to +18		unit	
Input Voltage		Pin 4,5	-0.3 to V_{CC}		V	
D ₁ to D ₇ Output Voltage	V _{IN} Vout(d)	D_1 to D_7 OFF	$-0.3 \text{ to } v_{CC}$ -0.3 to +18		V V	
D ₁ to D ₉ Output Current		Pins 8 to 14, D1 to D7 ON		+30		
Reference Flow-out Currer	I _{OL} (D)	Pin 3 Pin 3	-1 to 0		mA m d	
V _{OUT} Supply Voltage	202	Pin 6	-0.3 to +6		mA V	
Allowable Power Dissipation	V _{OUT}		-0.3 t	500	mW	
Operating Temperature	~	Ta=55°C			°C	
÷ –	Topr		-20 to +60 -40 to +125		°C	
Storage Temperature	^T stg		-40 to 4	120	-	C
Allowable Operating Conditions at Ta=25°C					un	it
Recommended Supply Voltag	re V _{CC}	Pin l	+5.5 to +16		v	
Input Voltage	V _{IN} +orV _I	INT Pin 4 or 5	-0.3 to V_{CC}		v	
Output Pin Load Resistance R _L		Between pin 6 OUT and pin 7 GND.	15k to 20k		ohm	
Electrical Characteristics at Ta=25°C,V _{CC} =12V(Unless V _{CC} is otherwise specified)						
		C-120 (Onless VCC is Ot)	merwise min t			
Input Bias Current	$T \rightarrow + (\Lambda)$	Dind Mart ON Mary - 20	-2	-yp i	nax 0	unit
(Amplifier)	I _{IN} +(A)	Pin4,V _{IN} +=OV,V _{IN} -=3V	-2		0	μA
(Mubilier)	T ==== (7)	GND=OV	-2		~	
	I _{IN} -(A)	Pin5,V _{IN} +=3V,V _{IN} -=0V GND=0V	-2		0	Aىر
Input Bias Current	I _{IN} +(C)+	<pre>Pin6,VIN+=OV,VIN-=3V,</pre>	-10		0	JUA
(Comparator) + Output	$I_{OL}(A)$	OUT=OV,GND=OV				
Leak Current						
Offset Voltage (1)	Voffset(1)	Pin6,V _{CC} =6V,V _{IN} +=V _{IN} - =0V,GND=-6V,GAIN=20dB		+]	150	mV
Offset Voltage (2)	$V_{offset(2)}$	Pin6,V _{IN} +=V _{IN} -=OV,	0	+]	150	mV
-	•====(=,	GND=OV, GAIN=20dB				
Reference Voltage	Vref	Pin2, Iref=0 to lmA	2.7	3	3.1	v
Current Dissipation	ICC	Pinl, VIN+=3V, VIN-=0V		8	15	mA
Amplifier Gain	VG	Open loop	30			dB
Output Flow-out Current	IOH	Pin6,VIN+=3V,VIN-=0V		-	-10	mA
		Vour=0V				
Pin D Output ON Voltage	V _{OL} (D)	Pin8 to 14, D1 to D7, IOL	=]	L.2	v
		20mA,VIN+=3V,VIN-0V				
Pin D Output Leak Current	IOH(D)	Pin8 to 14, D1 to D7, VIN	+=		10	Au
	(2)	0V, VIN-=3V, VD1 to D7=1				
Output Voltage (Amplifier	VOH	$Pin6, V_{CC}=5.5V, V_{IN}+=3V$				v
		V_{IN} -=0V, R _L =15kohms	•			
		Pin6, V_{CC} =12V, V_{IN} +=3V,	9.5			v
		V_{IN} -=0V, R_L =15kohms				



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Application Circuit



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