

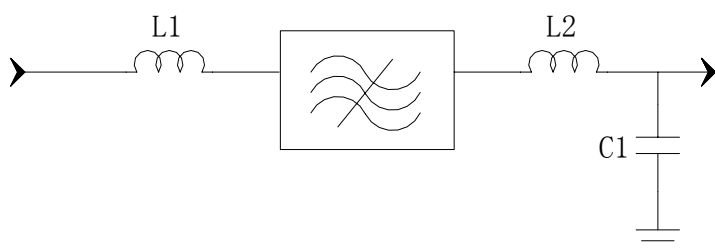
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	139.7	140	139.3
Insertion Loss	dB	-	10	12.5
1 dB Bandwidth	MHz	2.5	3.03	-
3 dB Bandwidth	MHz	3.5	3.95	-
40 dB Bandwidth	MHz	-	7.18	8
Passband Variation	dB	-	0.4	1
Absolute Delay	usec	-	0.85	-
Group Delay Variation(f0±0.6MHz)	nsec	-	60	100
Phase Linearity (f0±0.6MHz)	degrees	-	3	6
Ultimate Rejection	dB	-	42	-
Material Temperature coefficient	KHz/°C	-2.52		
Ambient Temperature	°C	25		
Package Size	SMP-53			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration



L1=56nH L2=82nH

C1=39pF

Source / Load Impedance=50 ohm

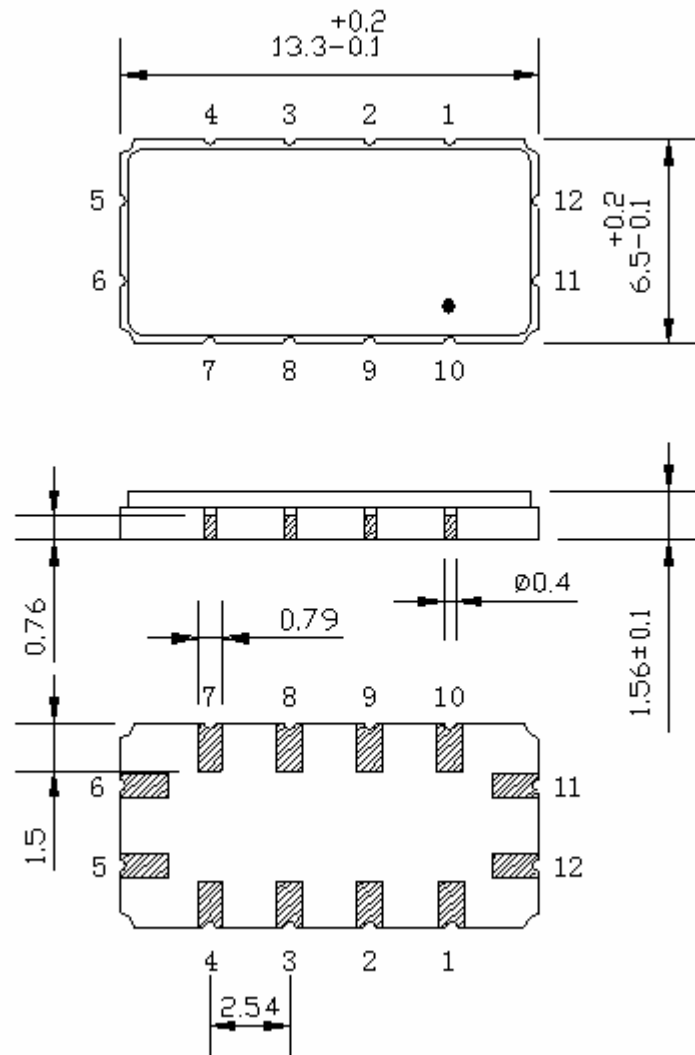
Notes - Component values may change depending
on board layout.



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Package Dimension



Input: 1 2
Output: 6

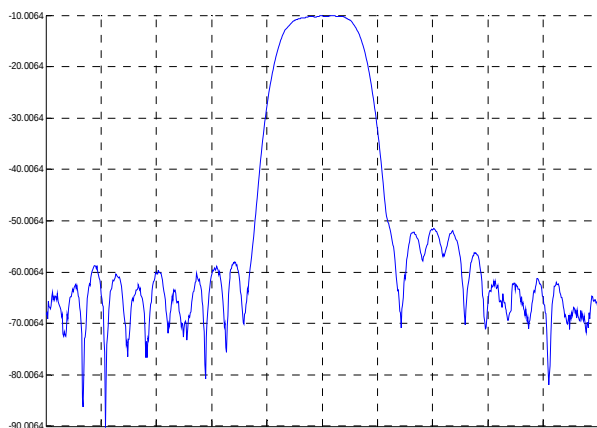


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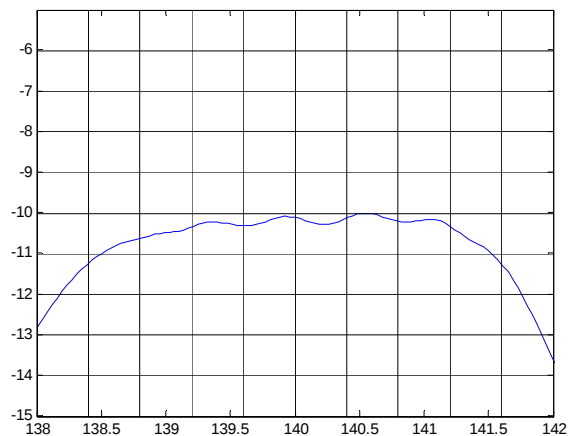
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Typical Performance

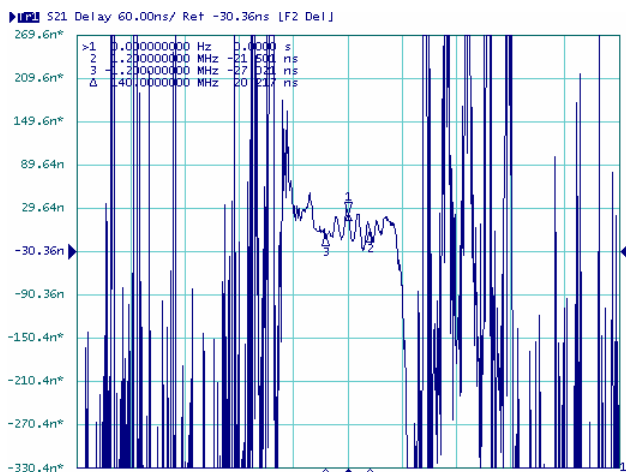
Frequency Response



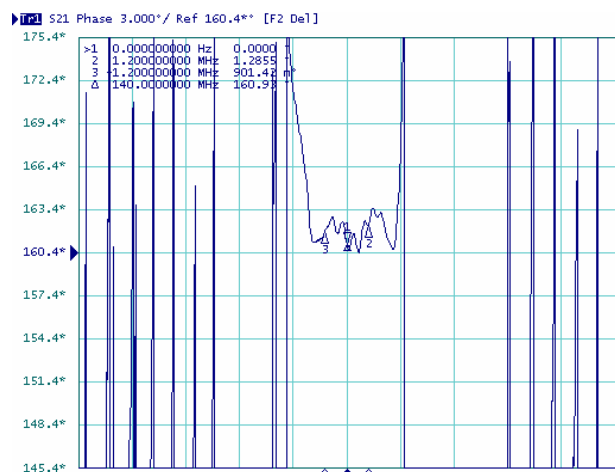
Passband Response



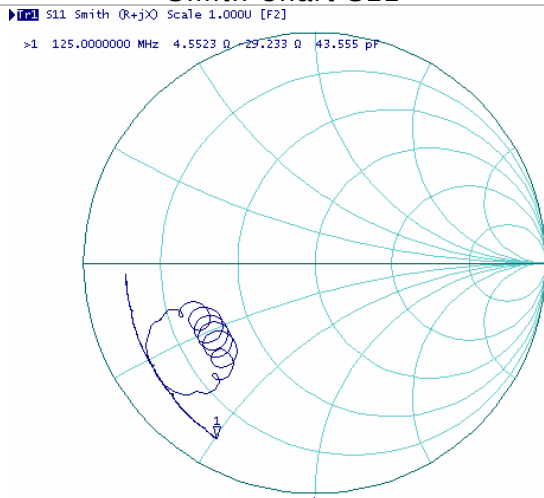
Group Delay Variation ($f_0 \pm 1.2$ MHz)



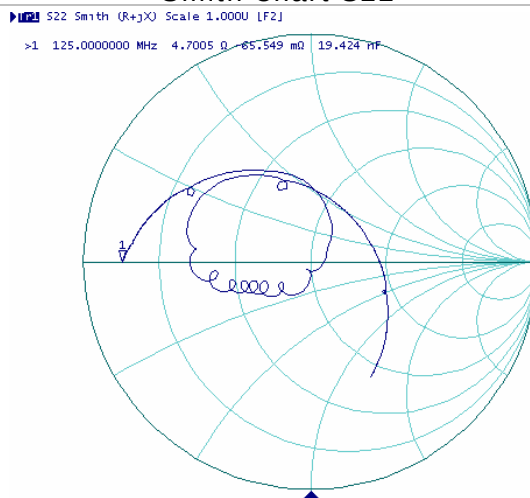
Phase Linearity ($f_0 \pm 1.2$ MHz)



Smith Chart S11



Smith Chart S22



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