

SANYO Semiconductors

DATA SHEET



Monolithic Linear IC Video Driver for DSC

Overview

The LA73079CL is a low voltage drive (2.7V to 3.6V) video driver developed for portable appliances including digital still cameras and cell phones. It incorporates a minus-voltage generator that allows the LA73079CL to generate its output with the pedestal voltage set to 0V, so that no output coupling capacitor is required. This enables substantial reduction in mounting space without concerned about V-sag.

Features

- Output coupling capacity not required
- Low-voltage drive ($V_{CC} = 2.7V$ to 3.6V)
- No V-sag
- Sextic LPF incorporated (fc = 9MHz)
- \bullet Current drain of $0\mu A$ in the standby mode
- Amplifier gain selectable from three options (6, 12, and 16dB) (Pin control (GND/Open/V_{CC}))
- Output drive capable of covering maximum 75Ω output, one channel

Specifications

Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		4.0	V
Allowable power dissipation	Pd max	Ta \leq 80°C, *Mounted on a specified board	160	mW
Operating temperature	Topr		-20 to +85	°C
Storage temperature	Tstg		-55 to +150	°C

*: Mounted on a specified board: 10mm×20mm×0.8mm, Paper phenol

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Recommended Operating Conditions at $Ta = 25^{\circ}C$

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Parameter	Symbol	Conditions	Ratings	Unit
Recommended Operating supply voltage	V _{CC} STD		3.1	V
Operating supply voltage range	V _{CC} RANGE		2.7 to 3.6	V

Electrical Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 3.1V$

	0		Ratings			1.1 34
Parameter	Parameter Symbol Conditions		min	typ	max	Unit
Current Drain Block		·				
Current dissipation 1	ICC	4pin = Low	14	22	20	
(V _{IN} = White50%)		Input = White50%	14	22	30	MA
Current dissipation 2	I _{CC} 2	4pin = Low	7	11.5	15	m۸
(Non-signal mode)		Input = No signal	'	11.5	15	ША
Current dissipation 3	I _{CC} -STBY	4pin = Open (High)		0	5	цΔ
(Standby mode)				0	5	μΛ
Video Block						
Voltage gain V6	V _{G-L}	V _{IN} = 1Vp-p 100% white 2pin = Low (GND)	5.7	6.2	6.7	dB
Voltage gain V12	V _{G-M}	V _{IN} = 0.5Vp-p 100% white 2pin = MID (Open)	11.7	12.2	12.7	dB
Voltage gain V16	V _{G-H}	$V_{IN} = 317 \text{mVp-p} 100\%$ white 2pin = High (Vcc)		16.2	16.7	dB
Freq. Characteristics	Vf	f = 100kHz/5MHz	-1.5	-0.5	+0.5	dB
Differential Gain	D _G	V _{OUT} = 2Vp-p (Modulated Ramp)	-2.0	0	-2.0	%
Differential Phase	DP	V _{OUT} = 2Vp-p (Modulated Ramp)	-2.0	0	-2.0	deg
Output leak current at standby 1	IOUTH	Current when 3V is applied to pin 9, with pin 4 at H (Standby Mode) and pin 9 (VOUT)	-5.0	0	+5.0	μΑ
Output leak current at standby 2	IOUTL	Current when 0.1V is applied to pin 9, with pin 4 at H (Standby Mode) and pin 9 (V _{OUT})	-5.0	0	+5.0	μΑ
Control Terminal Block						
Stand-by control pin H voltage (SET = STANDBY MODE)	VTH-STBY-H	Pin 4 pin voltage range at which $I_{CC} \le 5\mu A$	V _{CC} -0.5		3.6	V
Stand-by control pin L voltage	VTH-STBY-L	Pin 4 pin voltage range at which			0.0	14
(SET = ACTIVE MODE)		the operation mode is effective.	GND		0.3	V
Gain selection control pin H voltage (SET = 16dB)	VTH-G-H	Pin 2 pin voltage range at which Amp Gain becomes 16dB.	V _{CC} -0.3		V _{CC}	V
Gain selection control pin M voltage (SET = 12dB)	VTH-G-M	Pin 2 pin voltage range at which Amp Gain becomes 12dB.	1.0	1.2 (OPEN)	1.4	V
Gain selection control pin L voltage (SET = 6dB)	VTH-G-L	Pin 2 pin voltage range at which Amp Gain becomes 6dB.	nge at which 6dB. GND		0.3	V

Package Dimensions

unit : mm (typ) 3334



Pin Assignment, Pin Function Diagram and Block Diagram



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Pin Fu	Inctions			www.DrataSheet4U.com
Pin No	Symbol	Voltage	Description	Equivalent Circuit
1	AGND	0V	Analog GND	
2	GAINCTL	1.2V	$\begin{array}{ c c c c } \hline Gain select pin \\ \hline \hline Control of Pin2 & GAIN \\ \hline H(V_{CC}) & \Rightarrow & 16dB \\ \hline M(OPEN) & \Rightarrow & 12dB \\ \hline L(GND) & \Rightarrow & 6dB \\ \hline \end{array}$	$10^{V}CC$
3	VIN	1.1V	Video input terminal (Sync-chip clamp (input High-impedance)) GAIN SET: 6dB \Rightarrow 1.0Vp-p GAIN SET: 12dB \Rightarrow 500mVp-p GAIN SET: 16dB \Rightarrow 317mVp-p	$\begin{array}{c} VCC \\ 10 \\ 2k\Omega \\ 2k\Omega \\ 200\Omega \\ 200\Omega \\ 200\Omega \\ 2k\Omega \\ 2k\Omega \\ 3 \\ 5 \\ 6ND \\ 6 \\ 7 \\ 7 \\ 7 \\ 7 \\ 7 \\ 7 \\ 7 \\ 7 \\ 7$
4	PSAVCTL	V _{CC} or oV	Power save mode select pinControl of Pin4MODE $H(V_{CC})$ OPEN or $V_{CC}\pm0.5V$ \Rightarrow STANDBYL(GND)0V to 0.3V \Rightarrow	$\begin{array}{c} 10 \\ \hline 10 \\ \hline 50k\Omega \\ \hline $
5	GND	0V		

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Pin No	Symbol	Voltage Description		Equivalent Circuit		
6	CLKOUT	+3.0V ↑↓ 0V	Pin6 : Clock output terminal	10^{VCC}		
7	ND1	+0.5V ↑↓ -2.6V (-V _{CC})	Pin7 : The terminal which transmits an electric charge	10 5 GND 5 100kΩ		
8	V _{CC} N	0V ↑↓ -2.5V (-V _{CC})	Pin8 : Negative V _{CC}	V _{CC} N 8 ND1 7		
9	Vout	OV	Video output terminal (Push-pull output Low-impedance) 1.4V **********************************	$\begin{array}{c} V_{CC} \\ \hline 10 \\ \hline \\ Active: Low-impedance \\ Standby: High-impedance \\ \hline \\ VOUT \\ 9 \\ \hline \\ GND \\ \hline \\ 500\Omega \\ \hline \\ \\ 500\Omega \\ \hline \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ $		
10	Vcc	2.7V to 3.6V				

Test Circuit Diagram

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