



2-Channel BTL-Use or 4-Channel Driver

Overview

The LA6533 is a 2-channel BTL-use driver designed for compact disc pickup actuation or a 4-channel driver for general-purpose applications.

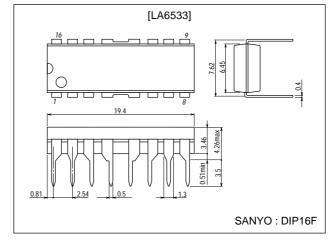
Functions and Features

- High output current (I_O max=0.5A).
- Wide operating voltage range (4 to 15V).
- Low input bias current.
- On-chip thermal shutdown.
- Output of amps 1 to 4 at muting-ON. mode:OFF.

Package Dimensions

unit:mm

3054A-DIP16F



Specifications

Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		16	V
Allowable power dissipation	Pd max		1.9	W
Maximum input voltage	V _{INB} max	Buffer amplifier	15	V
Muting pin current	I _M max		1	mA
Maximum output current	I _O max		0.7	А
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-55 to +150	°C

Operating Conditions at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC}		5	V
Load resistance	R_{L}	Pins 3 to 6, 11 to 14	8	Ω

Operating Characteristics at Ta = 25°C, $V_{CC}=5.0$ V

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
No-loaded current drain 1	I _{CC} 1	Mute OFF, Note1	5	10	20	mA
No-loaded current drain 2	I _{CC} 2	Mute ON	3	7	15	mA
No-loaded current drain 3	I _{CC} 3	Mute OFF, Note2	10	20	30	mA
No-loaded current drain 4	I _{CC} 4	Mute ON	4	8	16	mA

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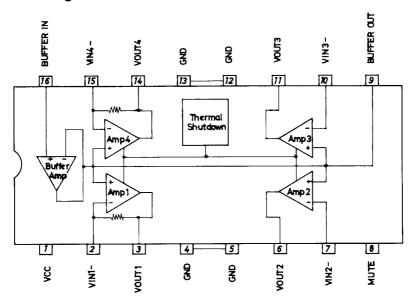
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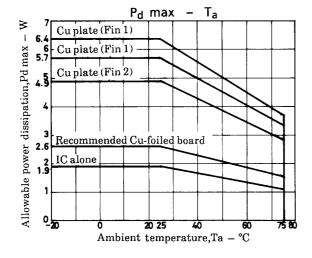
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Uill
Output offset voltage 1	V _{OF} 1	Out 1 and Out 2	-50		+50	mV
Output offset voltage 2	V _{OF} 2	Out 3 and Out 4	-50		+50	mV
Buffer input-output voltage difference	V _{BIO}	Buffer amplifier	-30		+30	mV
Buffer input voltage range	VBICM	Buffer amplifier	1.5		V _{CC} -1.5	V
Amp input voltage range	VICM		1.0		V _{CC} -1.5	V
Input bias current	IB			50		nA
Output voltage	Vo	R _L =8.0Ω	2.8	3.3		V
Bridge output voltage difference	V _{OD}	8Ω load between pins 3 and 6, 11 and 14.	1.8	2.2		V
Closed-circuit voltage gain	٧ _G			6.0		dB
Muting pin on-state voltage	٧M			0.7		V
Muting pin flow-in current	I _M			3.0		μΑ

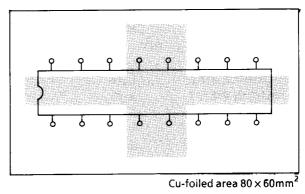
Note 1) Pins 2, 7, 10, 15 : GND Note 2) Pins 2, 7, 10, 15 : 1/2V_{CC}

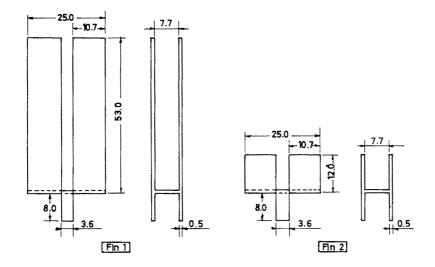
Equivalent Circuit Block Diagram



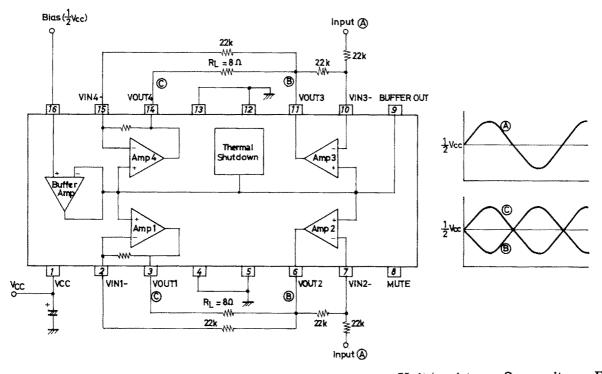


Sample Printed Circuit Pattern





Sample Application Circuit



Unit (resistance: Ω capacitance:F)

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