

SANYO Semiconductors **DATA SHEET**

LA6393D ____ Monolithic Linear IC High-Performance Dual Comparator

Overview

The LA6393D and 6393S are high-performance dual comparators that are capable of operating from a single power supply voltage over a wide range of 2 to 36V. Because of their excellent input characteristics and low power, they can be very conveniently applied to multisignal parallel comparator circuits that require high-density assembly.

Features

- LA6393D: DIP-8 pin package, LA6393S: SIP-9 pin package.
- Wide operating power-supply voltage range (Single power supply: 2.0 to 36.0V, dual power supplies: ± 1.0 to ± 18.0 V).
- Wide common-mode input voltage range (0 to V_{CC}-1.5V).
- Open-collector output enabling wired OR.
- Small current drain (0.6mA) and low power.

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V _{CC} max		36	٧
Differential input voltage	V _{ID}		36	V
Common-mode input voltage range	VICM		-0.3 to +36	٧
Allowable power dissipation	Pd max		570	mW
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		-55 to +125	°C

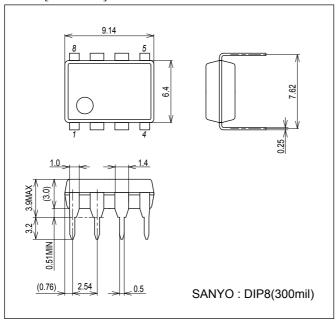
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Operating Characteristics at $Ta = 25^{\circ}C$, $V_{CC} = 5V$

Parameter	Symbol	Conditions	Test Circuit	Ratings			
				min	typ	max	unit
Input offset voltage	V _{IO}		1		±1	±5	mV
Input offset current	I _{IO}		2		±5	±50	nA
Input bias current	ΙΒ		3		25	250	nA
Common-mode input voltage range	V _{ICM}			0		V _{CC} -1.5	V
Supply current	Icc	R _L = ∞	4		0.6	1	mA
Voltage gain	VG	$R_L = 15k\Omega$	5		200		V/mV
Response time		V_{RL} = 5V, R_L = 5.1k Ω	6		1.3		μS
Output sink current	ISINK	$V_{IN}^{-} = 1V, V_{IN}^{+} = 0V, V_{O} \le 1.5V$	7	6	16		mA
Output saturation current	V _{OL}	$V_{IN^{-}} = 1V, V_{IN} + = 0V, I_{SINK} \le 3mA$	8		0.2	0.4	V
Output leakage current	ILEAK	$V_{IN^{-}} = 0V, V_{IN} + = 1V, V_{O} = 5V$	9		0.1		nA

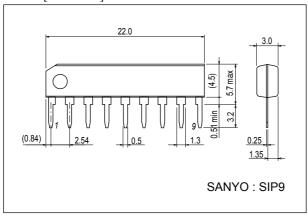
Package Dimensions

unit: mm (typ) 3001D [LA6393D]



Package Dimensions

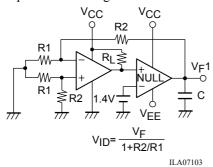
unit: mm (typ) 3017D [LA6393S]



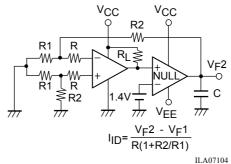
LA6393D / LA6393S

Test Circuits

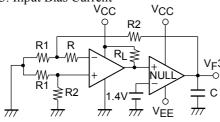
1. Input Offset Voltage

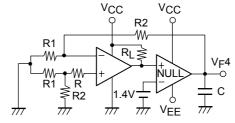


2. Input Offset Current



3. Input Bias Current VCC

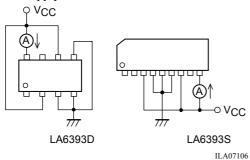


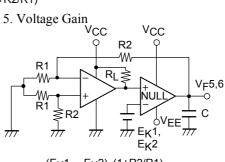


 $I_B = \frac{|V_F3 - V_F4|}{2R(1+R2/R1)}$

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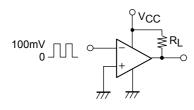
4. Supply Current





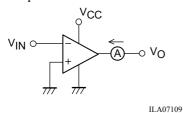
 $VG = \frac{(E_{K}1 - E_{K}2) (1+R2/R1)}{(E_{K}1 - E_{K}2) (1+R2/R1)}$ V_F6 - V_F5 ILA07107

6. Response Time

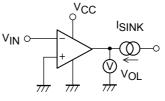


90% **VOUT** V_{IN} ∆V(over-drive) ΔV: Overdrive

7. Output Sink Current



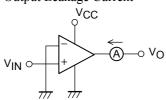
8. Output Saturation Voltage



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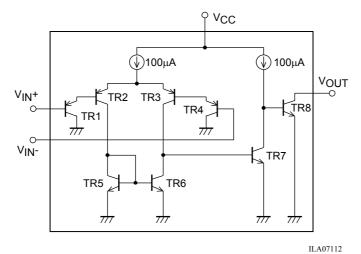
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9. Output Leakage Current



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Equivalent Circuit



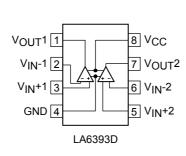
Pin Assignment

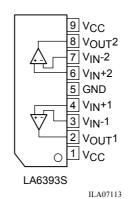
0.12

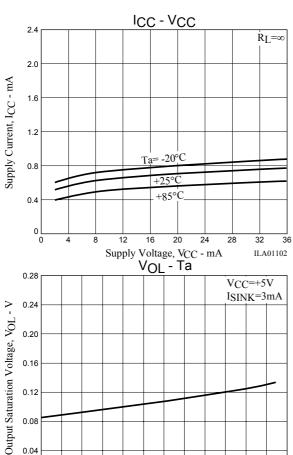
0.08

0.04

-30 -20





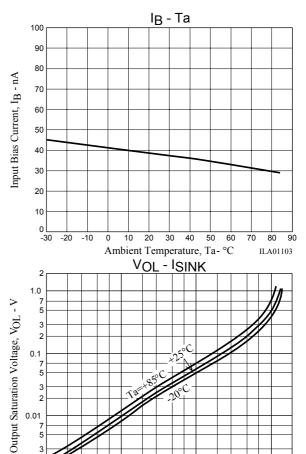


20 30 40 50

Ambient Temperature, Ta-°C

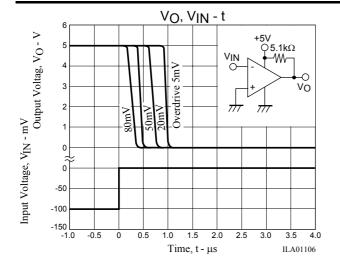
80

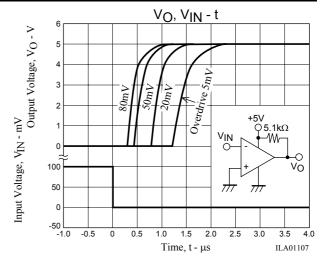
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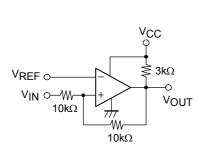
2 3 5 7 1.0

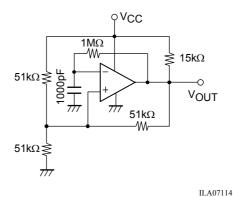
Output Sink Current, ISINK - mA





Sample Application Circuits





Voltage comparator (with hysteresis)

Square wave generator

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