

April 2015

# **KSH44H11 / KSH44H11I NPN Epitaxial Silicon Transistor**

## **Features**

# • Lead Formed for Surface Mount Application (No Suffix)

- Straight Lead (I-PAK, "- I" Suffix)
- Electrically Similar to Popular KSE44H
- · Fast Switching Speeds
- Low Collector-Emitter Saturation Voltage

# **Applications**

- · Switching Regulators
- Converters
- Power Amplifiers

# Description

Designed for general-purpose power and switching, such as output or driver stages in applications.



# **Ordering Information**

Part Number	Top Mark	Package	Packing Method
KSH44H11TF	KSH44H11	TO-252 3L (DPAK)	Tape and Reel
KSH44H11TM	KSH44H11	TO-252 3L (DPAK)	Tape and Reel
KSH44H11ITU	KSH44H11-I	TO-251 3L (IPAK)	Rail

# **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit	
V <sub>CEO</sub>	Collector-Emitter Voltage	80	V	
V <sub>EBO</sub>	Emitter-Base Voltage	5	V	
I <sub>C</sub>	Collector Current (DC)	8	Α	
I <sub>CP</sub>	Collector Current (Pulse)	16	А	
D	Collector Dissipation (T <sub>C</sub> = 25°C)	20.00	W	
P <sub>C</sub>	Collector Dissipation (T <sub>A</sub> = 25°C)	1.75	VV	
T <sub>J</sub>	Junction Temperature	150	°C	
T <sub>STG</sub>	Storage Temperature	- 65 to +150	°C	

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# **Electrical Characteristics**

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage <sup>(1)</sup>	$I_C = 30 \text{ mA}, I_B = 0$	80			V
I <sub>CEO</sub>	Collector Cut-Off Current	$V_{CE} = 80 \text{ V}, I_{B} = 0$			10	μΑ
I <sub>EBO</sub>	Emitter Cut-Off Current	$V_{EB} = 5 \text{ V}, I_{C} = 0$			50	μΑ
h <sub>FE</sub> DC	DC Current Gain	$V_{CE} = 1 \text{ V, } I_{C} = 2 \text{ A}$	60			
	De Guiterit Gairi	$V_{CE} = 1 \text{ V}, I_{C} = 4 \text{ A}$	40			
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = 8 \text{ A}, I_B = 0.4 \text{ A}$			1	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	$I_C = 8 \text{ A}, I_B = 0.8 \text{ A}$			1.5	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = 10 \text{ V}, I_{C} = 0.5 \text{ A}$		50		MHz
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> = 10 V, f = 1 MHz		130		pF
t <sub>ON</sub>	Turn-On Time			300		ns
t <sub>STG</sub>	Storage Time	I <sub>C</sub> = 5 A,   I <sub>B1</sub> = - I <sub>B2</sub> = 0.5 A		500		ns
t <sub>F</sub>	Fall Time	101 102 310 71		140		ns

# Note:

1. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2%.

# **Typical Performance Characteristics**

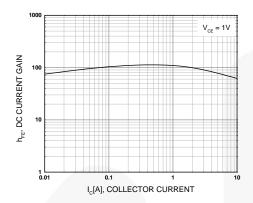


Figure 1. DC Current Gain

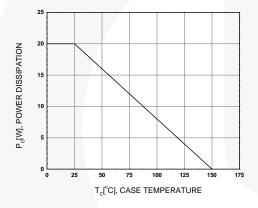


Figure 3. Power Derating

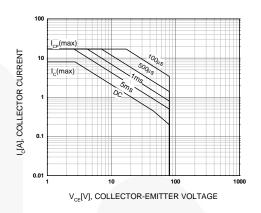
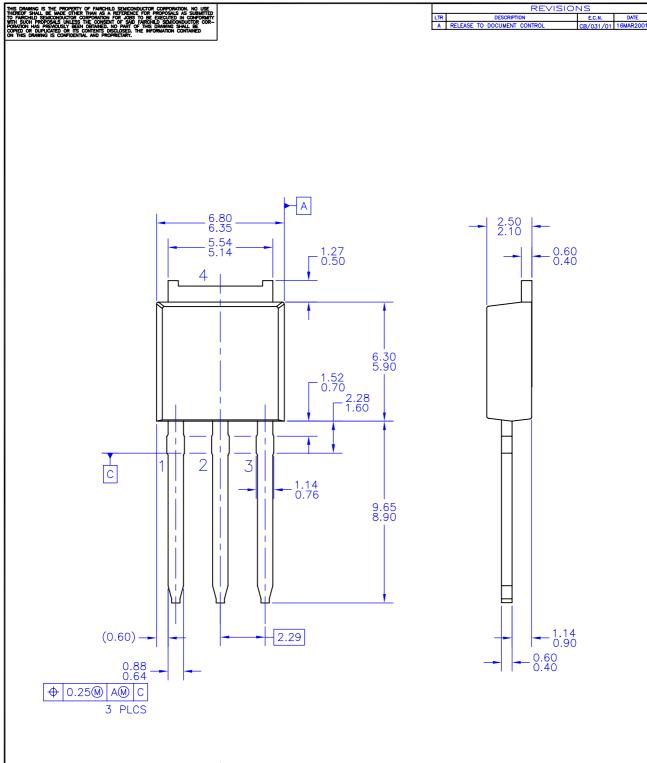


Figure 2. Safe Operating Area



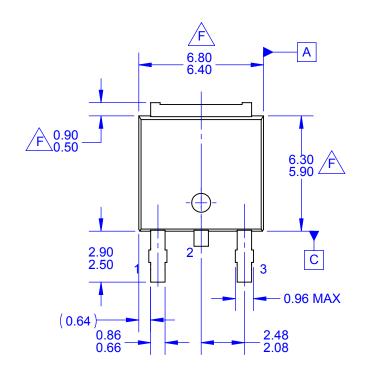


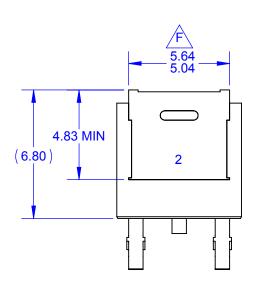
# NOTES: UNLESS OTHERWISE SPECIFIED

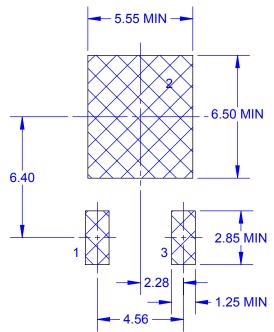
- A) ALL DIMENSIONS ARE IN MILLIMETERS.
- B) THIS PACKAGE CONFORMS TO JEDEC, TO-251, ISSUE C, VARIATION AA, DATED SEP 1988.
- C) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

APPROVALS	DATE				
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CHECKED: R. MANABIT		SEMIC		DUCTOF	THILIPPINES
APPROVED: M. GESTOLE		TO-2	251	(I-PAI	<) MOLDED,
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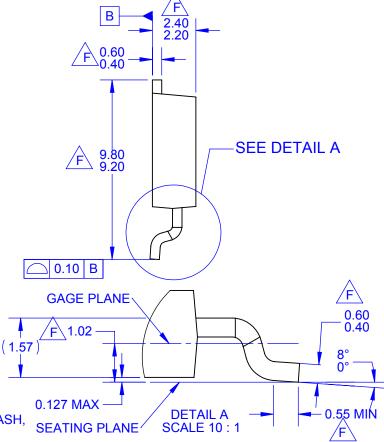
TO251A03REVA







LAND PATTERN RECOMMENDATION



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- NOT COMPLIANT TO JEDEC TO-252 VARIATION AB ALL DIMENSION ARE IN MILLIMETER DIMENSIONS ARE EXCLUSIVE OF BURRS,MOLD FLASH, C) AND TIE BAR EXTRUSIONS
- D) LAD PATTERN PER IPC7351A ATANDARD TO228P991X239-3N
- DRAWING FILE NAME:MKT-TO252D03REV3.
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- G) FAIRCHILD SEMICONDUCTOR.





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Definition of Terms					
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