

isc Silicon PNP Power Transistor**KSA1156****DESCRIPTION**

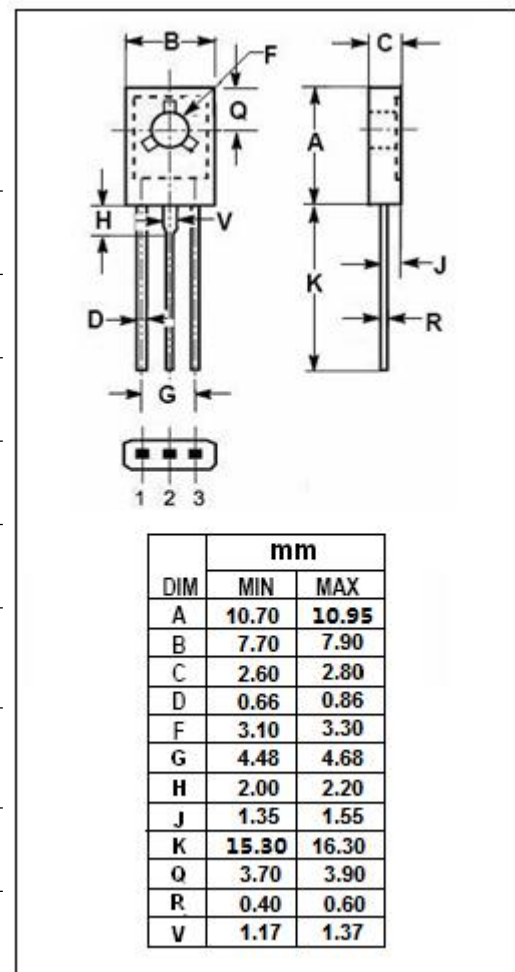
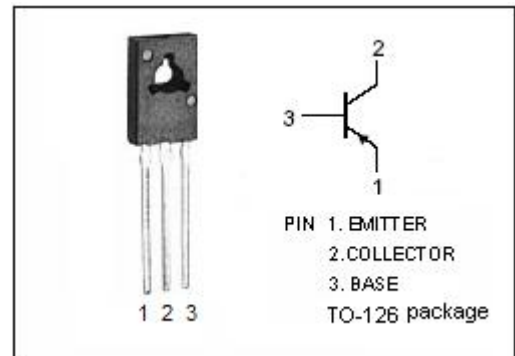
- Low Collector Saturation Voltage
- High voltage
- High speed switching
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- High voltage switching
- Low power switching regulator
- DC-DC converter

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-400	V
V_{CEO}	Collector-Emitter Voltage	-400	V
V_{EBO}	Emitter-Base Voltage	-7	V
I_C	Collector Current-Continuous	-0.5	A
P_C	Total Power Dissipation @ $T_a=25^\circ\text{C}$	1.0	W
	Total Power Dissipation @ $T_C=25^\circ\text{C}$	10	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-55~150	$^\circ\text{C}$



isc Silicon PNP Power Transistor**KSA1156****ELECTRICAL CHARACTERISTICS****T_c=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -100mA; I _B = -10mA			-1.0	V
V _{BE(sat)}	Collector-Emitter Saturation Voltage	I _C = -100mA; I _B = -10mA			-1.2	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -400V; I _E = 0			-100	μ A
I _{EBO}	Emitter Cutoff Current	V _{EB} = -5V; I _C = 0			-10	μ A
h _{FE}	DC Current Gain	I _C = -100mA; V _{CE} = -5V	30		200	

◆ h_{FE} Classifications

N	R	O	Y
30-60	40-80	60-120	100-200

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