

KC8SF80

Thyristors
800V, 8A

Feature

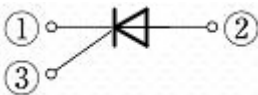
- Full Molded
- High Voltage
- Pb free terminal
- RoHS:Yes

OUTLINE

Package (House Name): FTO-220AG
Package (JEITA Code): SC-91



Equivalent circuit



Absolute Maximum Ratings (unless otherwise specified : Tc=25°C)

Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	T _{stg}		-55 to 150	°C
Junction temperature	T _j		-40 to 150	°C
Repetitive peak off-state voltage	V _{DRM}	AC, RGK=1KΩ	800	V
Repetitive peak reverse voltage	V _{RRM}	AC, RGK=1KΩ	800	V
Average on-state Current	I _{T(AV)}	T _c =130°C, 50Hz sine wave, θ=180°	8	A
Peak surge on-state current	I _{TSM}	T _j =25°C, 50Hz sine wave, Non-repetitive 1 cycle peak value	120	A
Current squared time	I ² t	T _j =25°C, t=10ms, Non-repetitive	72	A ² s
Peak gate dissipation	P _{FGM}	f≥50Hz, Duty≤10%	5	W
Average gate dissipation	P _{FG(AV)}		0.5	W
Peak gate forward current	I _{FGM}	f=50Hz, Duty≤10%	2	A
Peak gate forward voltage	V _{FGM}		10	
Peak gate reverse voltage	V _{RGM}	f≥50Hz, Duty≤10%	5	V
Critical rate of rise of on-state current	di/dt		50	A/μs
Dielectric strength	V _{dis}	Terminals to case, AC 1 minute	2	kV
Mounting torque	TOR	(Recommended torque : 0.3N·m)	0.5	N·m

※ :See the original Specifications

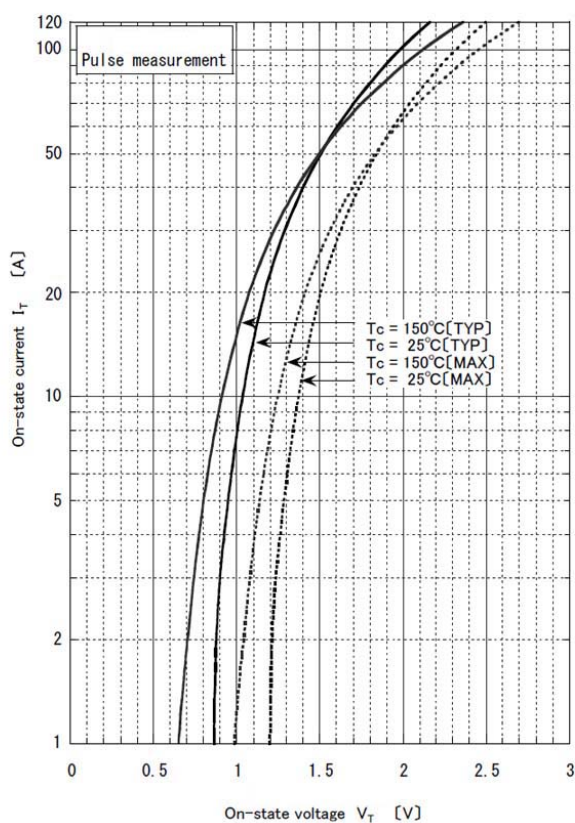
Electrical Characteristics (unless otherwise specified : T_c=25°C)

Item	Symbol	Conditions	Ratings			Unit
			MIN	TYP	MAX	
Repetitive off-state current	I _{DRM}	VD=800V, RGK=1kΩ, Pulse measurement			100	μA
Repetitive reverse current	I _{RRM}	VR=800V, RGK=1kΩ, Pulse measurement			100	μA
On-state voltage	V _{TM}	ITM=20A, Pulse measurement			1.5	V
Gate trigger voltage	V _{GT}	VD=6V, RL=100Ω			1	V
Gate trigger current	I _{GT}	VD=6V, RL=100Ω			15000	μA
Gate non-trigger voltage	V _{GD}	T _j =150°C, VD=1/2V _{DRM} , RGK=1kΩ	0.2			V
Holding Current	I _H	IT=100mA, RGK=1kΩ			100	mA
Critical rate of rise of off-state voltage	dVD/dt	T _j =150°C, VD=2/3×V _{DRM} , RGK=1kΩ		420		V/μs
Thermal Resistance	R _{th(j-c)}	Junction to case, With heatsink			1.49	°C/W

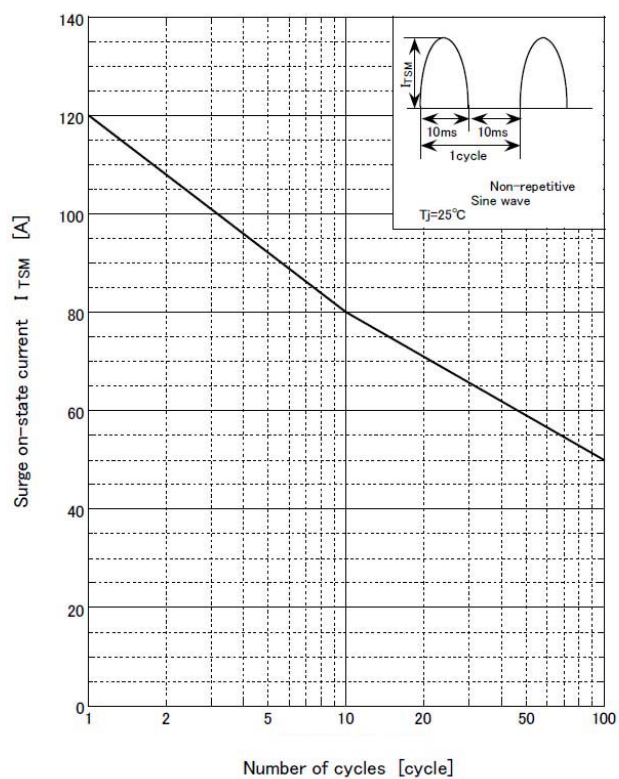
※ :See the original Specifications

CHARACTERISTIC DIAGRAMS

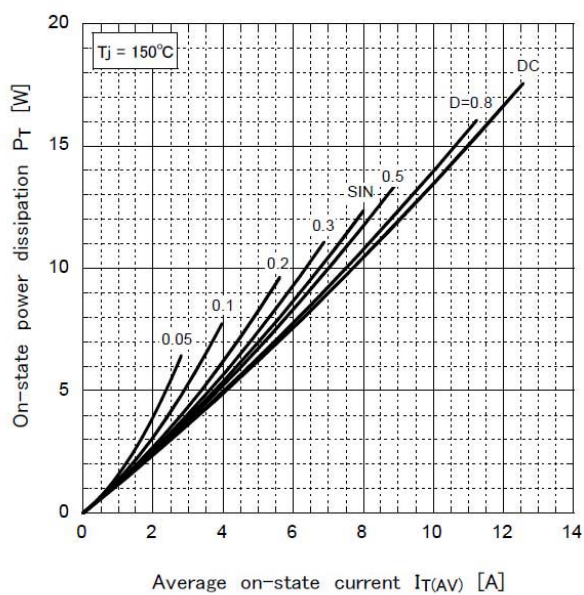
On-state voltage – On-state current



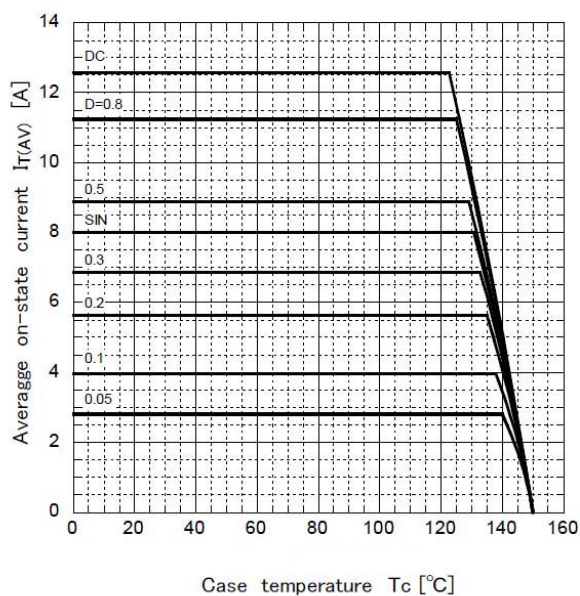
Surge on-state current capability

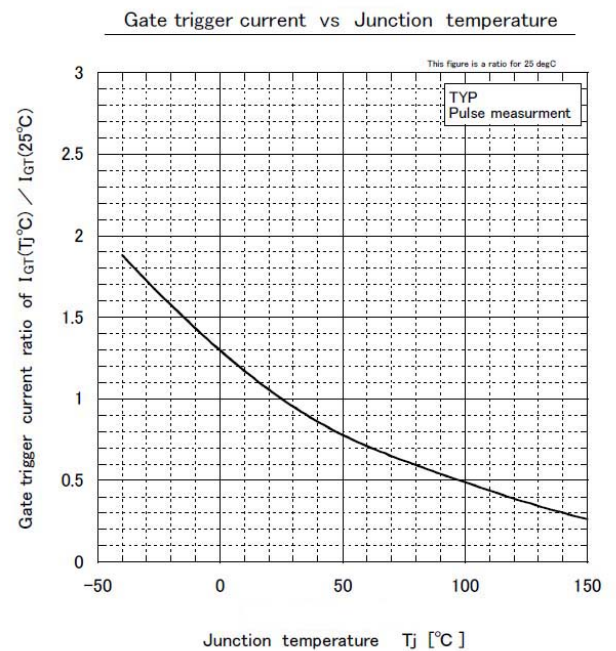
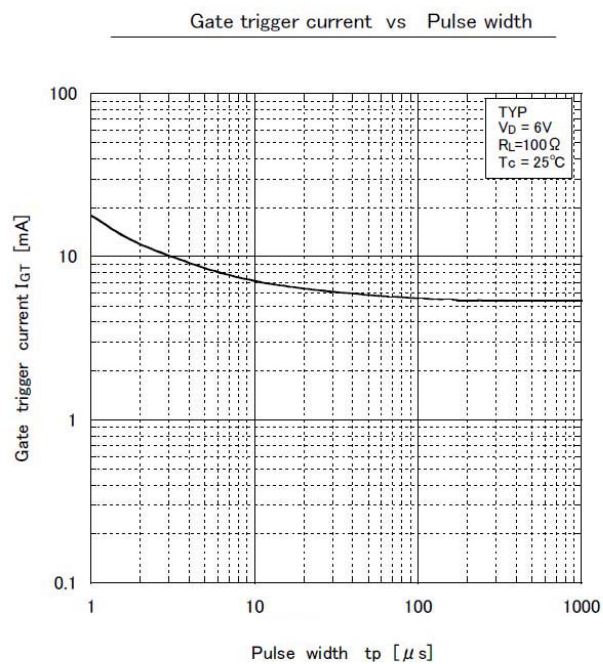
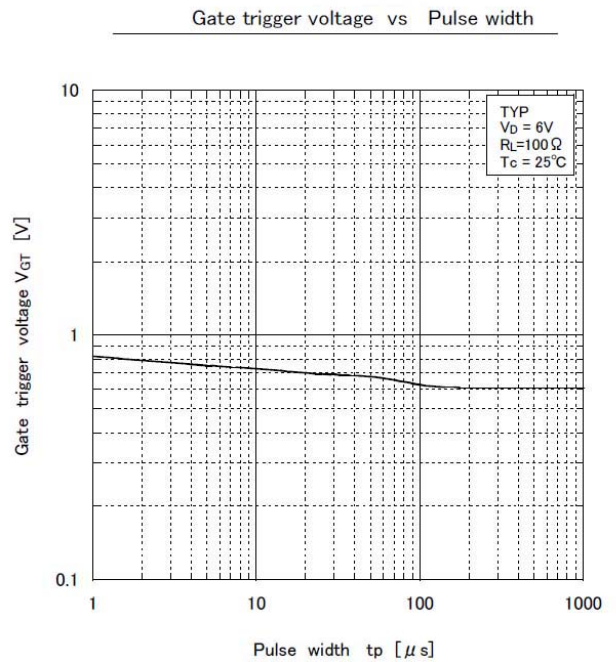
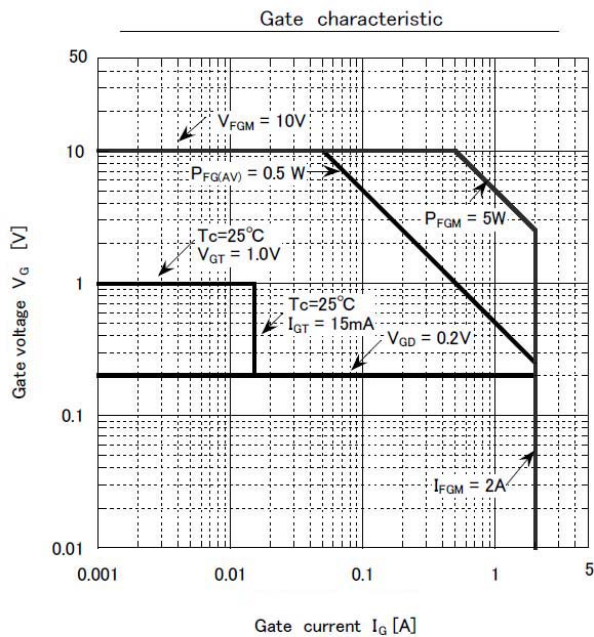


On-state power dissipation

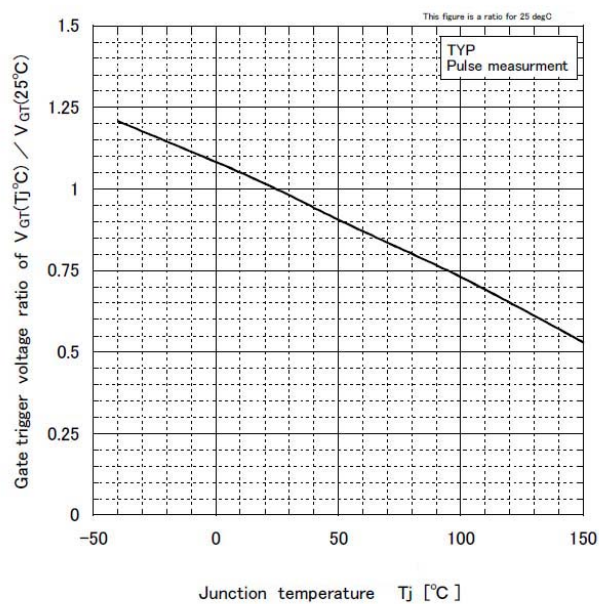


Derating curve T_C - $I_{T(AV)}$

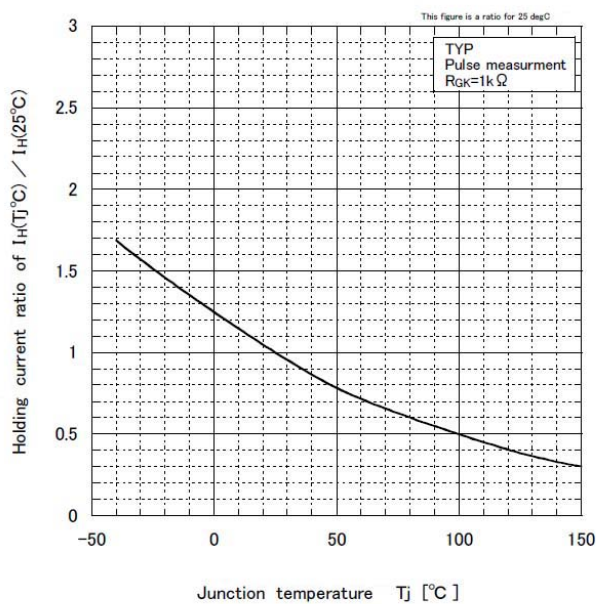




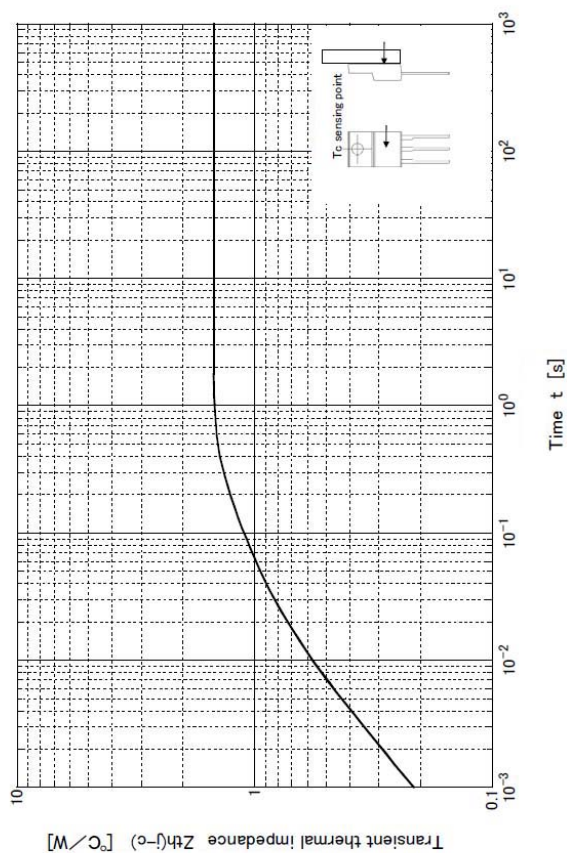
Gate trigger voltage vs Junction temperature



Holding current vs Junction temperature

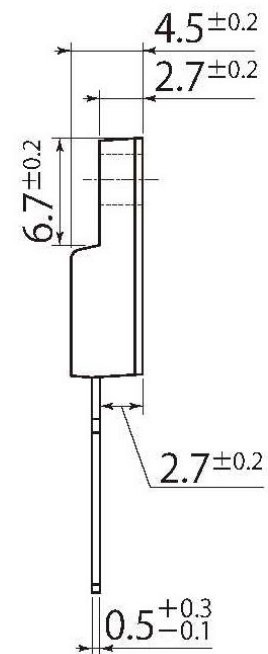
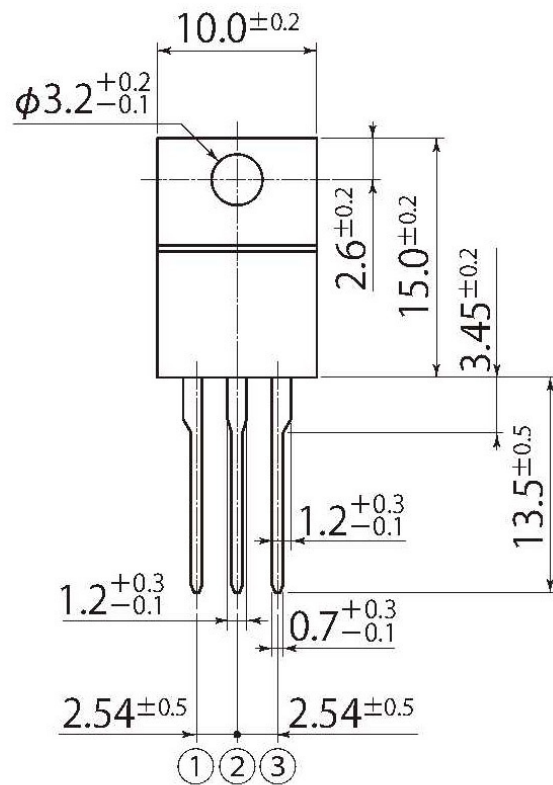


Transient thermal impedance



J8

JEDEC Code	—
JEITA Code	SC-91
House Name	FTO-220AG(3pin)



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