

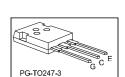
# High speed DuoPack: IGBT in Trench and Fieldstop technology with soft, fast recovery anti-parallel diode

#### Features:

TRENCHSTOP™ technology offering

- very low V<sub>CEsat</sub>
- low EMI
- Very soft, fast recovery anti-parallel diode
- maximum junction temperature 175°C
- qualified according to JEDEC for target applications
- Pb-free lead plating; RoHS compliant
- complete product spectrum and PSpice Models:

http://www.infineon.com/igbt/



#### Applications:

- uninterruptible power supplies
- welding converters
- · converters with high switching frequency

Туре	<b>V</b> CE	<i>l</i> c	V∕CEsat, Tvj=25°C	$\mathcal{T}_{vjmax}$	Marking	Package
IKW25N120H3	1200V	25A	2.05V	175°C	K25H1203	PG-TO247-3

#### **Maximum ratings**

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V∕CE	1200	V
DC collector current, limited by $T_{vjmax}$ $T_C = 25^{\circ}C$ $T_C = 100^{\circ}C$	/c	50.0 25.0	А
Pulsed collector current, the limited by Tvjmax	Cpuls	100.0	А
Turn off safe operating area $V_{CE} \le 1200V$ , $T_{vj} \le 175^{\circ}C$	-	100.0	Α
Diode forward current, limited by $T_{vjmax}$ $T_C = 25^{\circ}C$ $T_C = 100^{\circ}C$	Æ	25.0 12.5	А
Diode pulsed current, $t_0$ limited by $T_{vjmax}$	<b>/</b> Fpuls	100.0	Α
Gate-emitter voltage	V <sub>GE</sub>	±20	V
Short circuit withstand time $V_{\text{GE}}$ = 15.0V, $V_{\text{CC}} \le 600\text{V}$ , $T_{\text{vj}} \le 175^{\circ}\text{C}$ Allowed number of short circuits < 1000 Time between short circuits: $\ge 1.0\text{s}$	<i>t</i> sc	10	μs
Power dissipation $T_C = 25^{\circ}C$ Power dissipation $T_C = 100^{\circ}C$	P <sub>tot</sub>	326.0 156.0	W
Operating junction temperature	T <sub>vj</sub>	-40+175	°C
Storage temperature	T <sub>stg</sub>	-55+150	°C
Soldering temperature, wavesoldering 1.6 mm (0.063 in.) from case for 10s		260	°C
Mounting torque, M3 screw Maximum of mounting processes: 3	M	0.6	Nm



#### **Thermal Resistance**

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction - case	$R_{th(j^-c)}$		0.46	K/W
Diode thermal resistance, junction - case	$R_{th(j-c)}$		1.49	K/W
Thermal resistance junction - ambient	$R_{th(j^{-}a)}$		40	K/W

# Electrical Characteristic, at $T_{vj}$ = 25°C, unless otherwise specified

Danamatan	0	b.a.l. Canditiona		Value		
Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Static Characteristic			1			•
Collector-emitter breakdown voltage	V(BR)CES	$V_{GE} = 0V$ , $I_{C} = 0.50$ mA	1200	-	-	V
Collector-emitter saturation voltage	<b>V</b> CEsat	$V_{GE} = 15.0V$ , $I_{C} = 25.0A$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 125^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	2.05 2.50 2.70	2.40 - -	V
Diode forward voltage	V <sub>F</sub>	$V_{GE} = 0V, \not = 12.5A$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 175^{\circ}C$	-	1.80 1.85	2.35	V
Diode forward voltage	V <del></del> F	$V_{GE} = 0V$ , $f_F = 25.0A$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 125^{\circ}C$ $T_{vj} = 175^{\circ}C$	- - -	2.40 2.60 2.60	3.05 - -	V
Gate-emitter threshold voltage	VGE(th)	$I_C = 0.85$ mA, $V_{CE} = V_{GE}$	5.0	5.8	6.5	V
Zero gate voltage collector current	<b>/</b> CES	$V_{CE} = 1200V, V_{GE} = 0V$ $T_{vj} = 25^{\circ}C$ $T_{vj} = 175^{\circ}C$	-		250.0 2500.0	μA
Gate-emitter leakage current	/GES	V <sub>CE</sub> = 0V, V <sub>GE</sub> = 20V		-	600	nA
Transconductance	$g_{fs}$	$V_{CE} = 20V$ , $I_{C} = 25.0A$	-	13.0	-	S



## Electrical Characteristic, at $T_{vj}$ = 25°C, unless otherwise specified

Danamatan	Symbol Conditions		Value			11:4
Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Dynamic Characteristic			•		•	•
Input capacitance	Cies		-	1430	-	
Output capacitance	Coes	$V_{CE} = 25V$ , $V_{GE} = 0V$ , $f = 1MHz$		115	-	pF
Reverse transfer capacitance	$C_{res}$		-	75	-	
Gate charge	$Q_{\mathrm{G}}$	$V_{CC} = 960V$ , $I_{C} = 25.0A$ , $V_{GE} = 15V$	-	115.0	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	LΕ		-	13.0	-	nH
Short circuit collector current Max. 1000 short circuits Time between short circuits: ≥ 1.0s	<b>/</b> C(SC)	$V_{GE} = 15.0V, \ V_{CC} \le 600V, \ T_{vj} \le 175^{\circ}C, \ t_{SC} \le 10\mu s$	-	87	-	А

## Switching Characteristic, Inductive Load, at $T_{vj}$ = 25°C

Parameter	Comple ed	Conditions	Value			1114
	Symbol	Symbol Conditions	min.	typ.	max.	Unit
IGBT Characteristic						
Turn-on delay time	<i>t</i> <sub>d(on)</sub>	$T_{\rm vj}$ = 25°C,	-	27	-	ns
Rise time	<i>t</i> r	$V_{CC} = 600 \text{V}, I_{C} = 25.0 \text{A},$ $V_{GE} = 0.0/15.0 \text{V},$ $I_{C} = 23.0 \Omega, I_{C} = 80 \text{nH},$	-	41	-	ns
Turn-off delay time	<i>t</i> d(off)		-	277	-	ns
Fall time	<i>t</i> f	$\begin{array}{c} C_{\sigma} = 67 \mathrm{pF} \\ L_{\sigma}, C_{\sigma} \text{ from Fig. E} \end{array}$	-	17	-	ns
Turn-on energy	<i>E</i> on	Energy losses include "tail" and diode reverse recovery.	-	1.80	-	mJ
Turn-off energy	E <sub>off</sub>		-	0.85	-	mJ
Total switching energy	Ets		-	2.65	-	mJ

### Anti-Parallel Diode Characteristic, at $T_{vj}$ = 25°C

Diode reverse recovery time	<i>t</i> rr	$T_{\rm vj} = 25^{\circ}{\rm C},$	-	290	-	ns
Diode reverse recovery charge	<b>Q</b> rr	$V_{R} = 600V$ , $I_{F} = 25.0A$ ,	-	1.20	-	μC
Diode peak reverse recovery current	/ <sub>rrm</sub>	<i>di</i> ⊧/ <i>dt</i> = 500A/µs	-	10.4	-	Α
Diode peak rate of fall of reverse recovery current during <i>t</i> <sub>6</sub>	di <sub>tt</sub> /dt	-	-150	-	A/µs	

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## Switching Characteristic, Inductive Load, at $T_{vj}$ = 175°C

Davamatav	Symbol Conditions	Conditions	Value			11
Parameter	Symbol	Symbol Conditions		typ.	max.	Unit
IGBT Characteristic						
Turn-on delay time	t <sub>d(on)</sub>	$T_{\rm vj} = 175^{\circ}{\rm C}$ ,	-	26	-	ns
Rise time	<i>t</i> <sub>r</sub>	$V_{CC} = 600V$ , $I_{C} = 25.0A$ , $V_{GE} = 0.0/15.0V$ ,	-	35	-	ns
Turn-off delay time	<i>t</i> <sub>d(off)</sub>	$r_{\rm G}$ = 23.0 $\Omega$ , $L_{\rm \sigma}$ = 80nH,	-	347	-	ns
Fall time	<i>t</i> f	$C_{\sigma} = 67 \text{pF}$ $L_{\sigma}, C_{\sigma}$ from Fig. E	-	50	-	ns
Turn-on energy	<i>E</i> on	Energy losses include "tail" and	-	2.60	-	mJ
Turn-off energy	E <sub>off</sub>	diode reverse recovery.	-	1.70	-	mJ
Total switching energy	Ets		-	4.30	-	mJ

# Anti-Parallel Diode Characteristic, at $T_{vj}$ = 175°C

Diode reverse recovery time t <sub>rr</sub>		T <sub>vj</sub> = 175°C,	-	505	-	ns
Diode reverse recovery charge	<b>Q</b> rr	<i>V</i> <sub>R</sub> = 600V,   <sub>f</sub> = 25.0A,	-	2.75	-	μC
Diode peak reverse recovery current	/ <sub>rrm</sub>	<i>di</i> ⊧/ <i>dt</i> = 500A/µs	-	12.8	-	Α
Diode peak rate of fall of reverse recovery current during &	di <sub>rr</sub> /dt		-	-85	-	A/µs



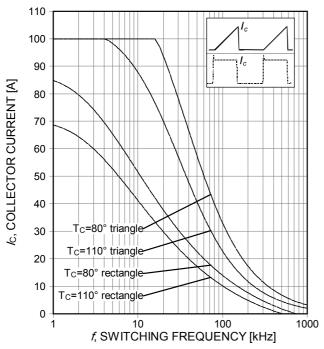


Figure 1. Collector current as a function of switching frequency ( $T_j \le 175^{\circ}\text{C}$ , D=0.5,  $V_{\text{CE}}=600\text{V}$ ,  $V_{\text{GE}}=15/0\text{V}$ ,  $R_{\text{G}}=23\Omega$ )

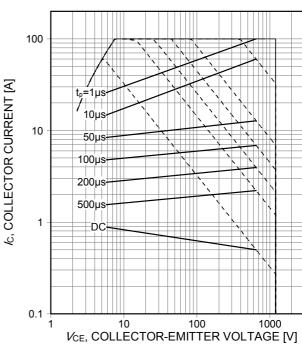


Figure 2. Forward bias safe operating area (D=0,  $T_C$ =25°C,  $T_J$ ≤175°C;  $V_{GE}$ =15V)

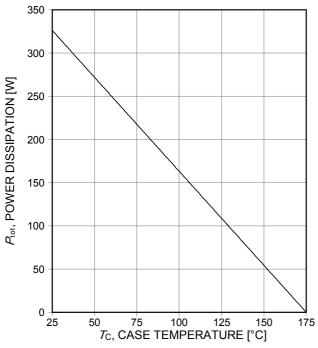


Figure 3. Power dissipation as a function of case temperature (T≤175°C)

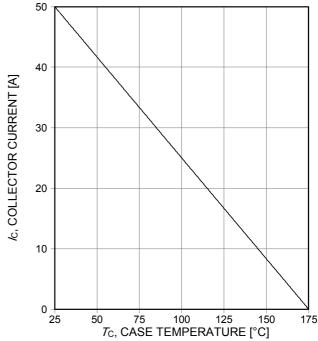


Figure 4. Collector current as a function of case temperature ( $V_{\text{GE}} \ge 15\text{V}$ ,  $T_{\text{j}} \le 175^{\circ}\text{C}$ )



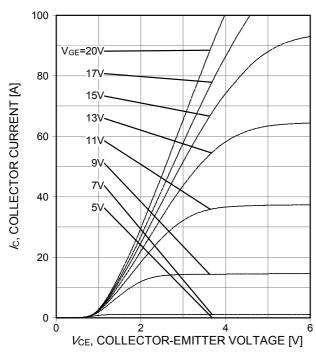


Figure 5. Typical output characteristic  $(T_i=25^{\circ}C)$ 

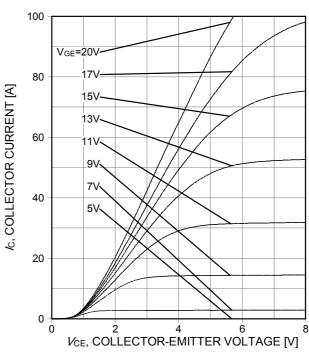


Figure 6. Typical output characteristic  $(T_i=175^{\circ}C)$ 

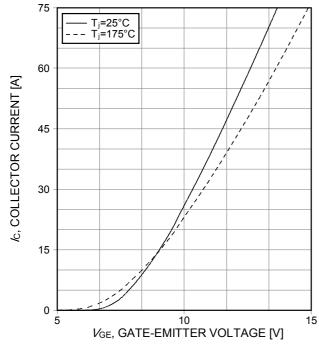


Figure 7. Typical transfer characteristic  $(V_{CE}=20V)$ 

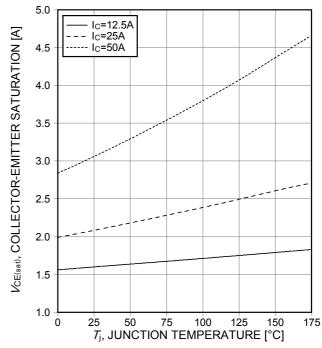


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature (  $V_{\rm GE}$ =15V)



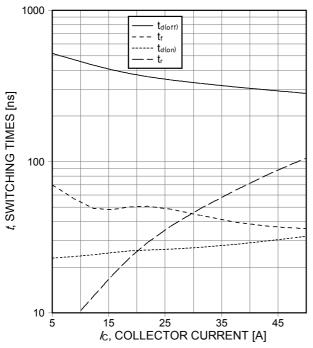


Figure 9. Typical switching times as a function of collector current (ind. load. T=175°C Mor=600V

(ind. load,  $T_j$ =175°C,  $V_{CE}$ =600V,  $V_{GE}$ =15/0V,  $R_{G}$ =23 $\Omega$ , test circuit in Fig. E)

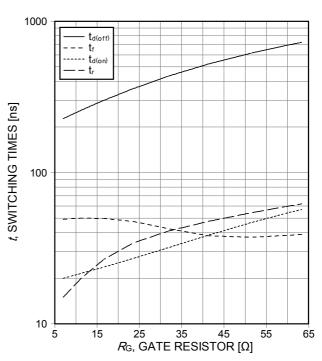


Figure 10. Typical switching times as a function of gate resistor
(ind. load,  $T_j$ =175°C,  $V_{CE}$ =600V,  $V_{GE}$ =15/0V,  $I_{CE}$ =25A, test circuit in Fig. E)

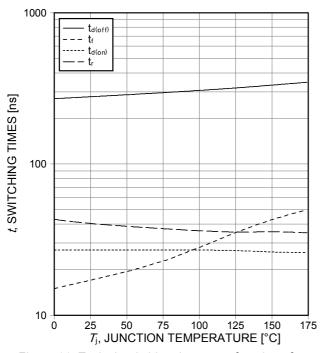


Figure 11. Typical switching times as a function of junction temperature (ind. load,  $V_{\text{CE}}$ =600V,  $V_{\text{GE}}$ =15/0V,  $V_{\text{CE}}$ =25A,  $V_{\text{CE}}$ =23 $\Omega$ , test circuit in Fig. E)

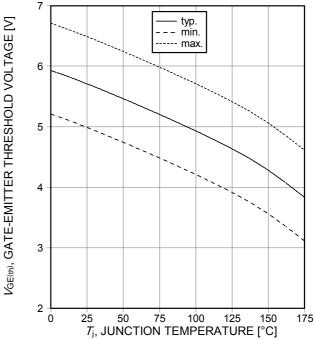


Figure 12. Gate-emitter threshold voltage as a function of junction temperature (/c=0.85mA)



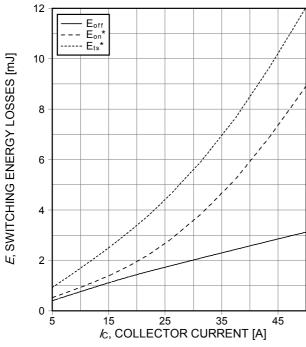


Figure 13. Typical switching energy losses as a function of collector current (ind. load,  $T_j$ =175°C,  $V_{CE}$ =600V,  $V_{GE}$ =15/0V,  $R_{G}$ =23 $\Omega$ , test circuit in Fig. E)

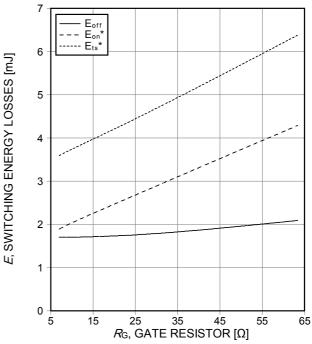


Figure 14. Typical switching energy losses as a function of gate resistor (ind. load, Tj=175°C, VcE=600V, VGE=15/0V, Ic=25A, test circuit in Fig. E)

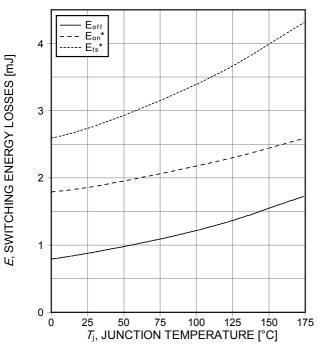


Figure 15. Typical switching energy losses as a function of junction temperature (ind load,  $V_{\text{CE}}$ =600V,  $V_{\text{GE}}$ =15/0V,  $I_{\text{CE}}$ =25A,  $I_{\text{CE}}$ =23 $\Omega$ , test circuit in Fig. E)

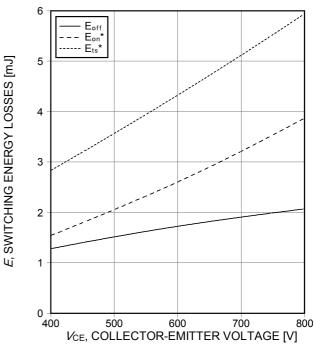


Figure 16. Typical switching energy losses as a function of collector emitter voltage (ind. load,  $T_j$ =175°C,  $V_{GE}$ =15/0V,  $I_{C}$ =25A,  $R_{G}$ =23 $\Omega$ , test circuit in Fig. E)



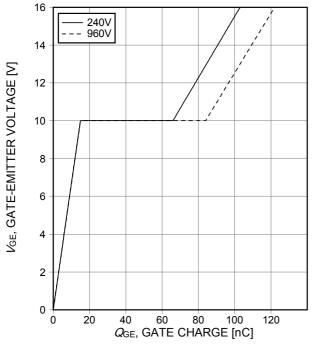


Figure 17. Typical gate charge (/c=25A)

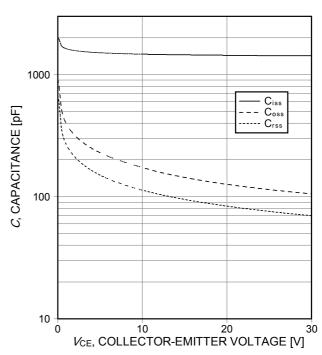


Figure 18. Typical capacitance as a function of collector-emitter voltage (V<sub>GE</sub>=0V, f=1MHz)

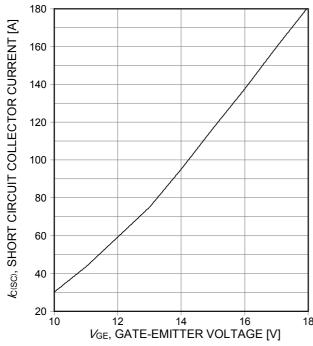


Figure 19. Typical short circuit collector current as a function of gate-emitter voltage (V<sub>CE</sub>≤600V, start at T<sub>j</sub>=25°C)

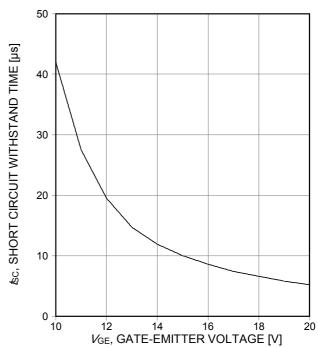


Figure 20. Short circuit withstand time as a function of gate-emitter voltage ( $V_{CE} \le 600 \text{V}$ , start at  $T_j \le 150 ^{\circ}\text{C}$ )



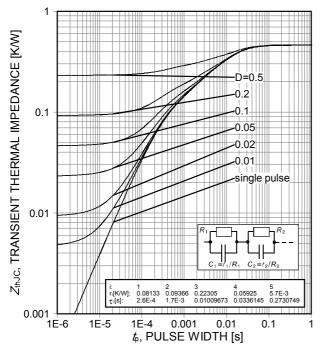


Figure 21. IGBT transient thermal impedance  $(D=t_0/T)$ 

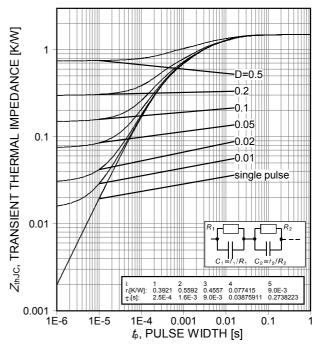


Figure 22. Diode transient thermal impedance as a function of pulse width  $(D=t_p/T)$ 

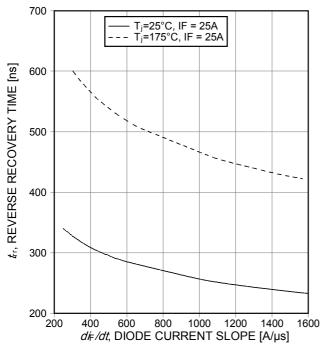


Figure 23. Typical reverse recovery time as a function of diode current slope ( $V_R$ =600V)

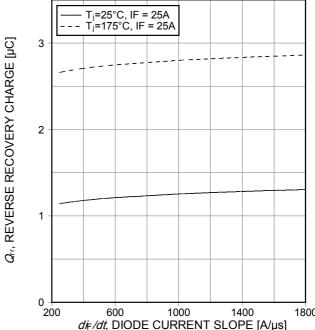


Figure 24. Typical reverse recovery charge as a function of diode current slope ( V<sub>R</sub>=600V)



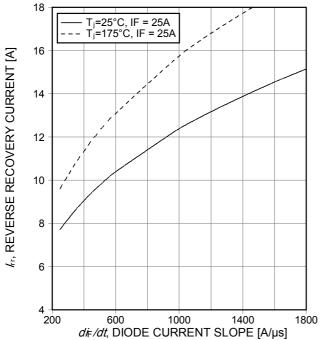


Figure 25. Typical reverse recovery current as a function of diode current slope ( $V_R$ =600V)

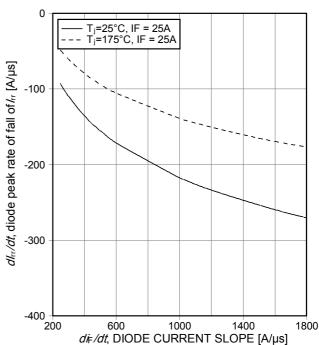


Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope (V<sub>R</sub>=600V)

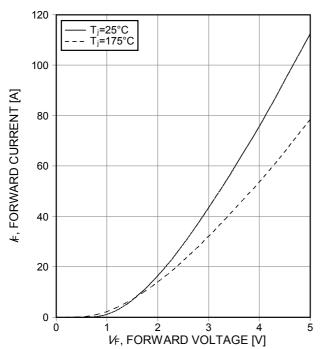


Figure 27. Typical diode forward current as a function of forward voltage

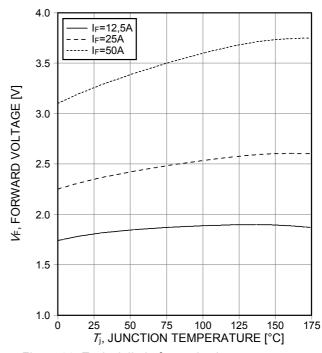
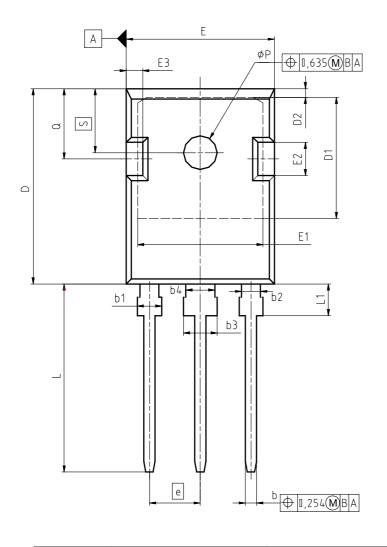
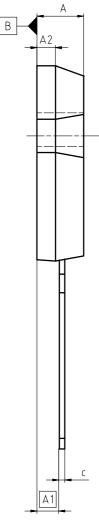


Figure 28. Typical diode forward voltage as a function of junction temperature

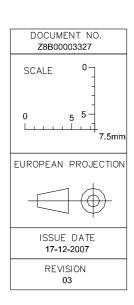


# PG-TO247-3





DIM	MILLIMI	ETERS	INCHES		
DIIVI	MIN	MAX	MIN	MAX	
А	4.90	5.16	0.193	0.203	
A1	2.27	2.53	0.089	0.099	
A2	1.85	2.11	0.073	0.083	
b	1.07	1.33	0.042	0.052	
b1	1.90	2.41	0.075	0.095	
b2	1.90	2.16	0.075	0.085	
b3	2.87	3.38	0.113	0.133	
b4	2.87	3.13	0.113	0.123	
С	0.55	0.68	0.022	0.027	
D	20.82	21.10	0.820	0.831	
D1	16.25	17.65	0.640	0.695	
D2	1.05	1.35	0.041	0.053	
E	15.70	16.03	0.618	0.631	
E1	13.10	14.15	0.516	0.557	
E2	3.68	5.10	0.145	0.201	
E3	1.68	2.60	0.066	0.102	
е	5	44	0.2	14	
N	;	3	(	3	
L	19.80	20.31	0.780	0.799	
L1	4.17	4.47	0.164	0.176	
øΡ	3.50	3.70	0.138	0.146	
Q	5.49	6.00	0.216	0.236	
S	6.04	6.30	0.238	0.248	





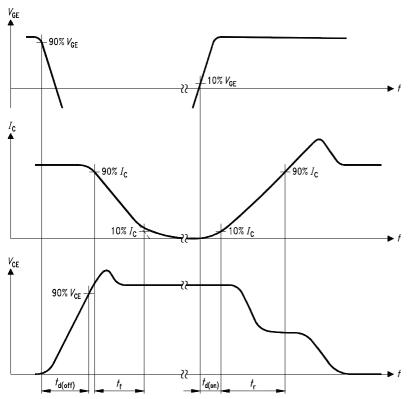


Figure A. Definition of switching times

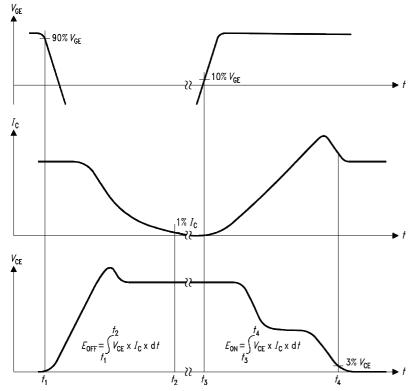


Figure B. Definition of switching losses

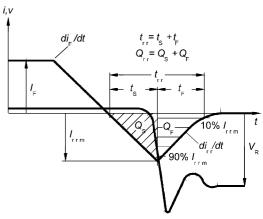


Figure C. Definition of diodes switching characteristics

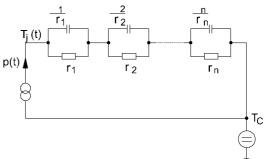


Figure D. Thermal equivalent circuit

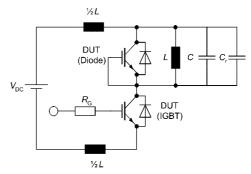


Figure E. Dynamic test circuit Leakage inductance L= 180nH, Stray capacitor C<sub>c</sub> = 40pF, Relief capacitor C<sub>r</sub> = 1nF (only for ZVT switching)





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