

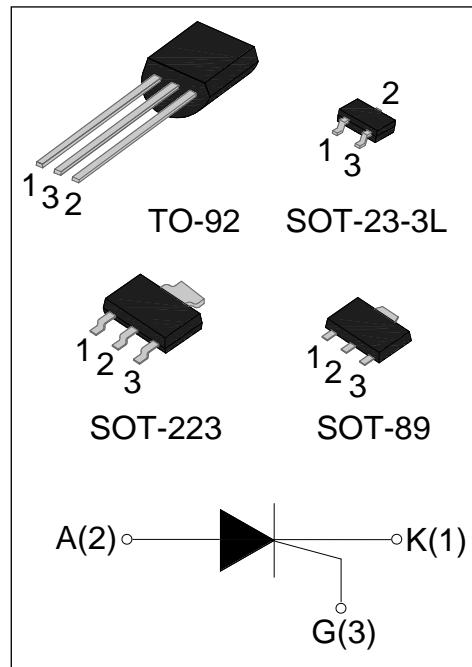


JX008 Series 0.8A Sensitive SCRs

Rev.4.0

DESCRIPTION:

The JX008 SCR series provide high dv/dt rate with strong resistance to electromagnetic interface. They are especially recommended for use on residual current circuit breaker, straight hair, igniter etc.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	0.8	A
I_{GT}	≤ 200	μA
V_{DRM} / V_{RRM}	600	V

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	°C
Operating junction temperature range	T_j	-40-110	°C
Repetitive peak off-state voltage	V_{DRM}	600	V
Repetitive peak reverse voltage	V_{RRM}	600	V
RMS on-state current	$I_{T(RMS)}$	0.8	A
Non repetitive surge peak on-state current (tp=10ms)	I_{TSM}	8	A
I^2t value for fusing (tp=10ms)	I^2t	0.32	A^2s
Critical rate of rise of on-state current	dI/dt	50	$A/\mu s$
Peak gate current (tp=20μs, $T_j=110^\circ C$)	I_{GM}	0.2	A
Peak gate power (tp=20μs, $T_j=110^\circ C$)	P_{GM}	0.5	W
Average gate power dissipation($T_j=110^\circ C$)	$P_{G(AV)}$	0.1	W

ELECTRICAL CHARACTERISTICS ($T_j=25^\circ\text{C}$ unless otherwise specified)

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I_{GT}	$V_D=12V$ $R_L=33\Omega$	-	40	200	μA
V_{GT}		-	0.6	0.8	V
V_{GD}	$V_D=V_{DRM}$ $T_j=110^\circ\text{C}$	0.2	-	-	V
I_L	$I_G=1.2 I_{GT}$	-	-	6	mA
I_H	$I_T=0.05A$	-	-	5	mA
dV/dt	$V_D=2/3V_{DRM}$ $T_j=110^\circ\text{C}$ $R_{GK}=1\text{K}\Omega$	10	-	-	V/ μs

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX)	Unit
V_{TM}	$I_T=1.1A$	$t_p=380\mu\text{s}$	$T_j=25^\circ\text{C}$	1.5 V
I_{DRM}	$V_D=V_{DRM}$	$V_R=V_{RRM}$	$T_j=25^\circ\text{C}$	5 μA
I_{RRM}			$T_j=110^\circ\text{C}$	100 μA

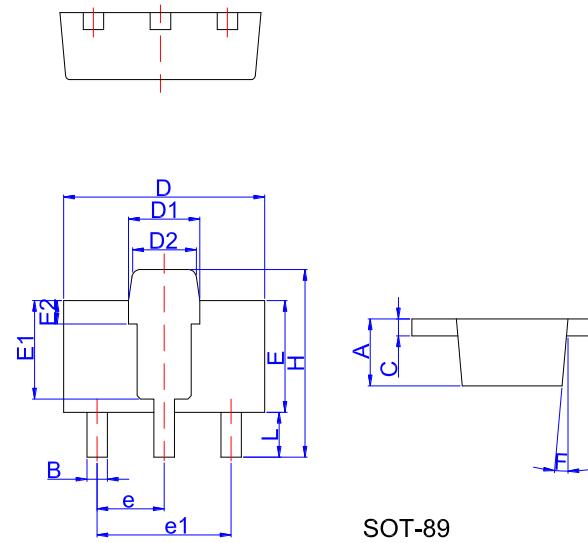
THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case	TO-92/ SOT-23-3L	75
		SOT-89	32
		SOT-223	20

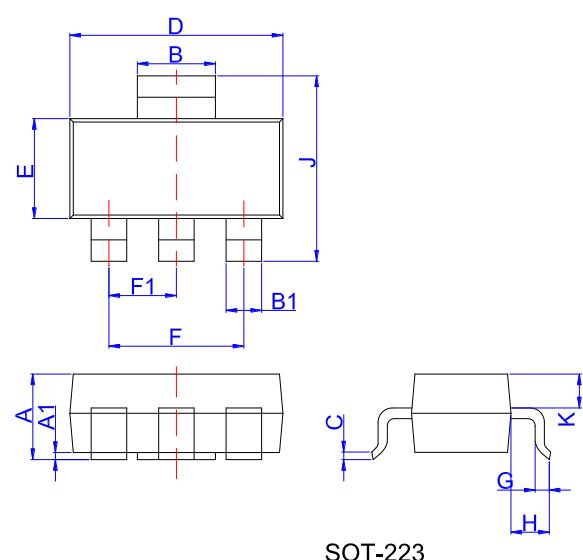
ORDERING INFORMATION

J	X	008	U
<u>JieJie Microelectronics Co.,Ltd</u>			U:TO-92 L:SOT-23-3L N:SOT-89 V:SOT-223
Sensitive gate SCRs			$I_T(\text{RMS}):0.8A$

PACKAGE MECHANICAL DATA

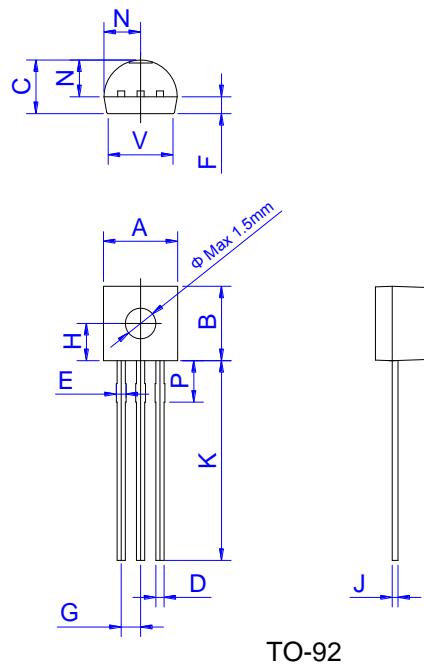


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.40		1.60	0.055		0.063
B	0.35		0.52	0.014		0.020
C	0.35		0.46	0.014		0.018
D	4.30		4.70	0.169		0.185
D1	1.50		1.70	0.059		0.067
D2	1.30		1.50	0.051		0.059
E	2.30		2.70	0.091		0.106
E1		2.20			0.087	
E2		0.52			0.020	
e		1.50			0.059	
e1		3.00			0.118	
F		5°			5°	
H	3.94		4.0	0.155		0.157
L	0.80		1.20	0.031		0.047

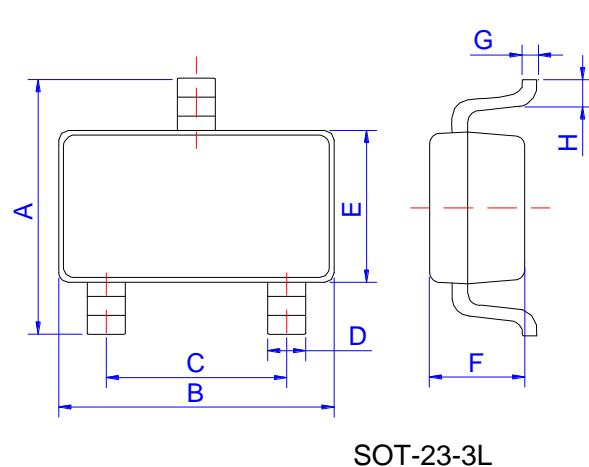


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	1.5	1.6	1.8	0.059	0.063	0.071
A1	0	0.06	0.10	0	0.002	0.004
B	2.9	3.0	3.1	0.114	0.118	0.122
B1	0.6	0.7	0.8	0.024	0.028	0.031
C	0.22	0.26	0.32	0.009	0.010	0.013
D	6.3	6.5	6.7	0.248	0.256	0.264
E	3.3	3.5	3.7	0.130	0.138	0.146
F		4.6			0.181	
F1		2.3			0.091	
G	0.7	0.9	1.1	0.028	0.035	0.043
H	1.5	1.75	2.0	0.059	0.069	0.079
J	6.7	7.0	7.3	0.264	0.276	0.287
K	0.8	0.9	1.0	0.031	0.035	0.039

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.45		5.20	0.175		0.205
B	4.32		5.33	0.170		0.210
C	3.18		4.19	0.125		0.165
D	0.407		0.533	0.016		0.021
E	0.60		0.80	0.024		0.031
F	-	1.1	-	-	0.043	-
G	-	1.27	-	-	0.050	-
H	-	2.30	-	-	0.091	-
J	0.36		0.50	0.014		0.020
K	12.70		15.0	0.500		0.591
N	2.04		2.66	0.080		0.105
P	1.86		2.06	0.073		0.081
V	-		4.3	-		0.169



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.65		2.95	0.104		0.116
B		2.92			0.115	
C		1.90			0.075	
D	0.34		0.36	0.013		0.014
E		1.60			0.063	
F		1.17			0.046	
G		0.15			0.006	
H	0.25		0.55	0.010		0.022

FIG.1 Maximum power dissipation versus RMS on-state current

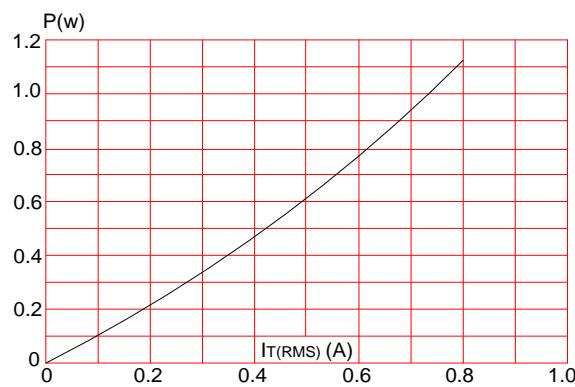


FIG.2: RMS on-state current versus case temperature

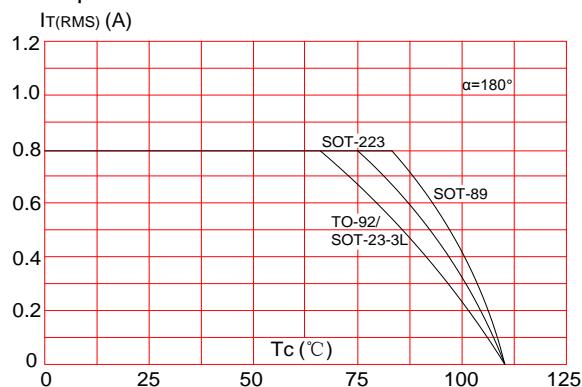


FIG.3: Surge peak on-state current versus number of cycles

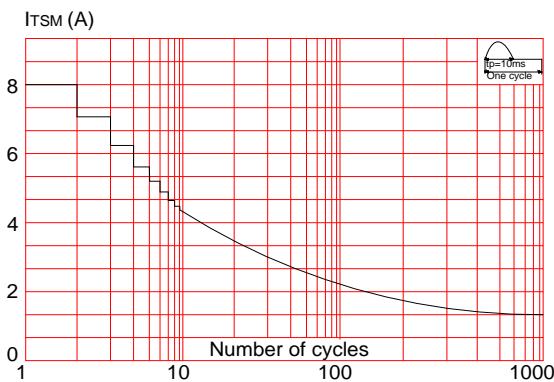


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $tp < 10\text{ms}$, and corresponding value of I^2t

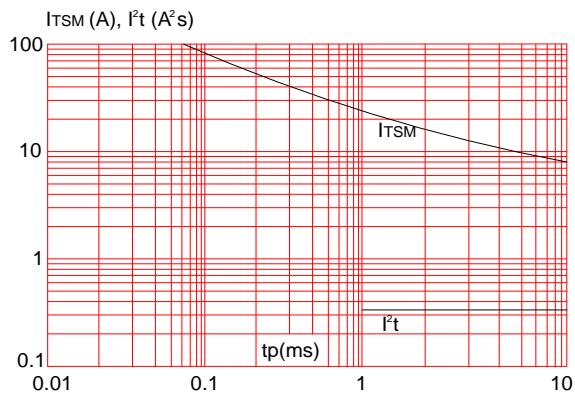


FIG.4: On-state characteristics (maximum values)

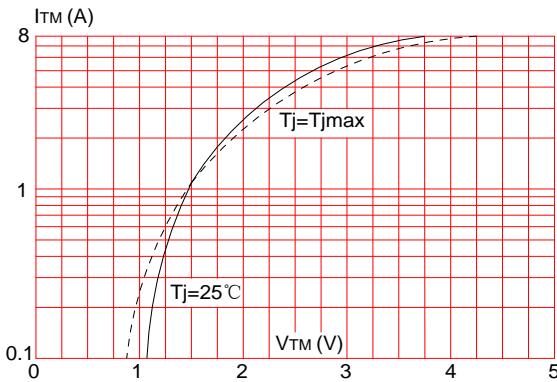
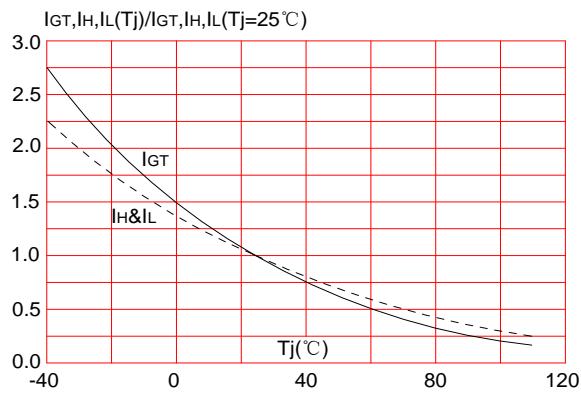


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature



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