

Preliminary Specifications Subject to Change without Notice

DESCRIPTION

The JW1536NC is a ultra-low standby power and fast dynamic response off-line constant voltage regulator for Buck and Buck- Boost topology with 500V MOSFET integrated, and with only one external component around IC for operation, which is specialized for IoT module power supply. JW1536NC can output 3.3V default voltage, which decreases the system cost. In light load condition, JW1536NC operates in green mode, in which the inductor peak current and the switching frequency is lower than that of full load to improve the system efficiency and it's also suppress the audible noise problem and guarantee the fast dynamic state response.

JW1536NC has multi-protection functions which largely enhance the safety and reliability of the system, including VDD under-voltage Lockout (UVLO), short circuit protection (SCP), over load protection(OLP), pulse-by- pulse current limit and over-temperature protection (OTP).

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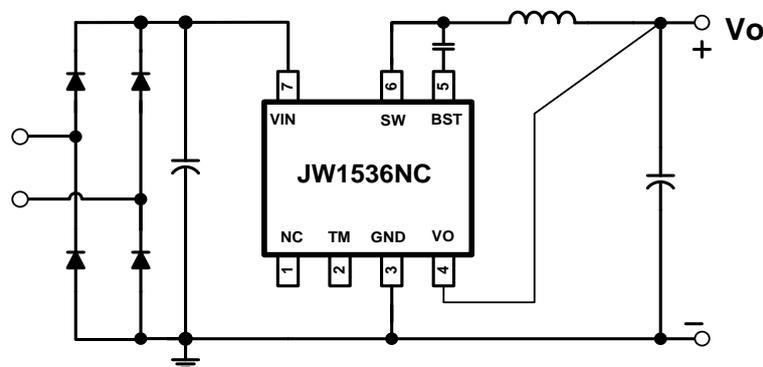
FEATURES

- Ultra-low Standby Power Consumption
- Ultra-low System BOM Cost
- 500V MOS and Freewheel Diode Integrated
- 3.3V Default Output Voltage with $\pm 3\%$
- Fast Dynamic State Response
- Peak Current Mode Control
- Built-in Frequency Jittering
- High Efficiency Over Wide Operating Range
- VDD UVLO
- Short Circuit Protection and Over Load Protection
- Pulse-by-pulse Current Limit
- Over Temperature Protection
- SOP7 Package

APPLICATIONS

- IoT Module
- Smart Lighting

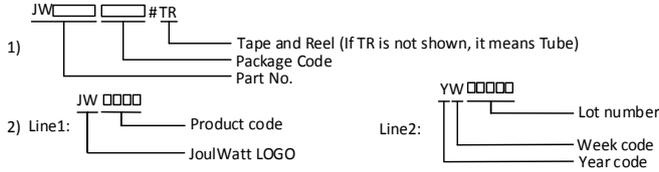
TYPICAL APPLICATION



ORDER INFORMATION

DEVICE ¹⁾	PACKAGE	TOP MARKING ²⁾	ENVIRONMENTAL ³⁾
JW1536NCSOPA#TR	SOP7	JW1536NC YW□□□□□	Green

Notes:

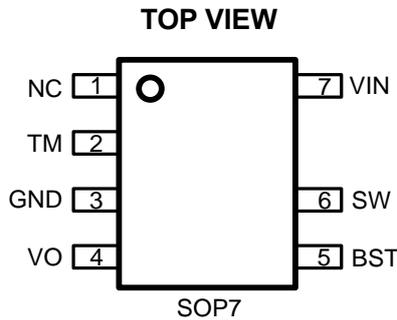


3) All JoulWatt products are packaged with Pb-free and Halogen-free materials and compliant to RoHS standards.

DEVICE INFORMATION

DEVICE	OPERATION MODE AT LIGHT LOAD	PACKAGE	MSL	STATUS
JW1536NCSOPA#TR	PFM	SOP7	MSL3	Available

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATING¹⁾

VIN Voltage to SW.....	500V
VIN Voltage to GND.....	700V
BST Voltage to SW.....	9.5V
VO Voltage to GND.....	6V
Junction Temperature ^{2) 3)}	150°C
Lead Temperature.....	260°C
Storage Temperature.....	-65°C to +150°C
ESD Susceptibility (Human Body Model)	2.5kV

RECOMMENDED OPERATING CONDITIONS

VIN Voltage.....400V
Operating Junction Temp (T_J)-40°C to 125°C

THERMAL PERFORMANCE⁴⁾

θ_{JA} $\theta_{JC(top)}$

SOP7.....96.....45°C/W

Note:

- 1) Exceeding these ratings may damage the device. These stress ratings do not imply function operation of the device at any other conditions beyond those indicated under RECOMMENDED OPERATING CONDITIONS.
- 2) The JW1536NC includes thermal protection that is intended to protect the device in overload conditions. Continuous operation over the specified absolute maximum operating junction temperature may damage the device.
- 3) The device is not guaranteed to function outside of its operating conditions.
- 4) Measured on JESD51-7, 4-layer PCB.

ELECTRICAL CHARACTERISTICS

T_A = 25°C, unless otherwise stated.

Advance Information, not production data, subject to change without notice.

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNITS
BST Charge ON Voltage	V _{BST_ON}		7.5	8.0	8.5	V
BST Charge OFF Voltage	V _{BST_OFF}		7.9	8.4	8.9	V
HVJFET Charge Current	I _{HV}			1		mA
Quiescent Current	I _Q	no switch, sleep mode	2		4	uA
Operation Current	I _{OP}	Fs=30kHz			120	uA
Output Voltage	V _O		3.201	3.3	3.399	V
Peak Current	I _{PK}	JW1536NC	0.23	0.25	0.27	A
Maximum On Time	T _{ONMAX}			12		µs
Leading Edge Blanking Time	T _{LEB}			250	400	ns
Oscillator Frequency	f _{osc}			30		kHz
Frequency Jittering Range	± Δf/f _{OSC}			8		%
Frequency Jittering Period	T _{Jit}			15		ms
Power MOS Breakdown Voltage	BV		500			V
Power MOS Rdson	Rdson	Vgs=10V		26	30	Ω
Over Thermal Protection Threshold ⁵⁾	T _{OTP}			150		°C
Thermal Protection Hysteresis ⁵⁾	T _{OTPHYS}			30		°C

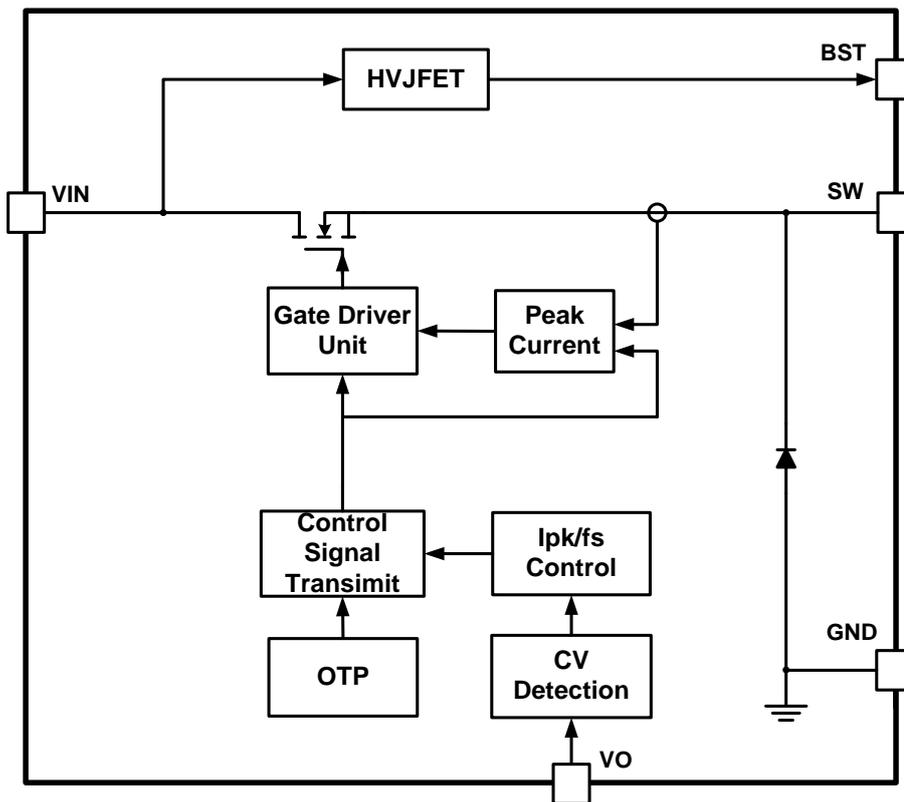
Note:

6) Guaranteed by design.

PIN DESCRIPTION

PIN SOP8	NAME	DESCRIPTION
1	NC	
2	TM	Test mode pin, TM pin connect to GND in application.
3	GND	Chip ground power supply pin
4	VO	Output voltage detection
5	BST	Power supply for internal driver, 0.1uF~0.47uF value ceramic capacitor is ok for normal operation.
6	SW	Switching node
7	VIN	The drain of MOSFET and power supply for BST

BLOCK DIAGRAM



FUNCTIONAL DESCRIPTION

JW1536NC is a high efficiency and high performance off-line constant voltage regulator for Buck and Buck- Boost topology.

Start Up

JW1536NC can be supplied from VIN directly. When the internal high voltage(HV) power souse charges BST cap up to the V_{BST_ST} , the gate driver starts to switch. IC enters into open loop operation until the output voltage is higher than certain voltage . Once the output voltage is lower than reference voltage lasting for certain time, JW1536NC stops switching and enters into protection.

Peak Current Control

JW1536NC detects the inductor peak current when internal MOS turn-on. MOS will be turned off immediately as the inductor peak reach the peak current reference. JW1536NC will spread frequency when the peak current is higher than 1.3 reference peak current especially at startup or short circuit condition, which is to avoid the inductor saturation and very high peak current for more reliability.

Constant Voltage Control

The output voltage is detected by VO pin. Internal close loop controller will regulate the peak current and work frequency to keep the output voltage stable and response the dynamic load state very quickly.

Green Mode

In light or no load condition, JW1536NC

operates in DCM which means the OFF time is very long. JW1536NC will reduce the peak current of the inductor to minimize the power loss. The longer Toff time, the lower Ipeak, which optimize the efficiency and also decrease the power consumption especially when no-load condition.

Short Circuit Protection(SCP)/ Over Load Protection(OLP)

In short circuit or over load condition, output voltage can't be charged to reference voltage. JW1536NC will operate in auto-restart mode which is represented in the following description.

Auto-restart Mode

JW1536NC will enter auto-restart mode if SCP/ OLP is triggered when the output voltage can't reach the reference for certain time. The chip stops switching for very long time and enter into restart mode repeatedly.

Over Temperature Protection

When internal temperature of the chip exceeds T_{OTP} , JW1536NC operates in auto-restart mode to help the chip cooling.

PCB Design

1. The BST ceramic capacitor must be located nearly to IC between the BST pin and SW.
2. Make the area of the power loop as small as possible in order to reduce the EMI radiation.

REFERENCE DESIGN

Note: Information in the following reference design sections is not part of JoulWatt component specification. Customers are responsible for determining suitability of components chosen for their purposes and should validate their design implementation to make sure the proper system functionality.

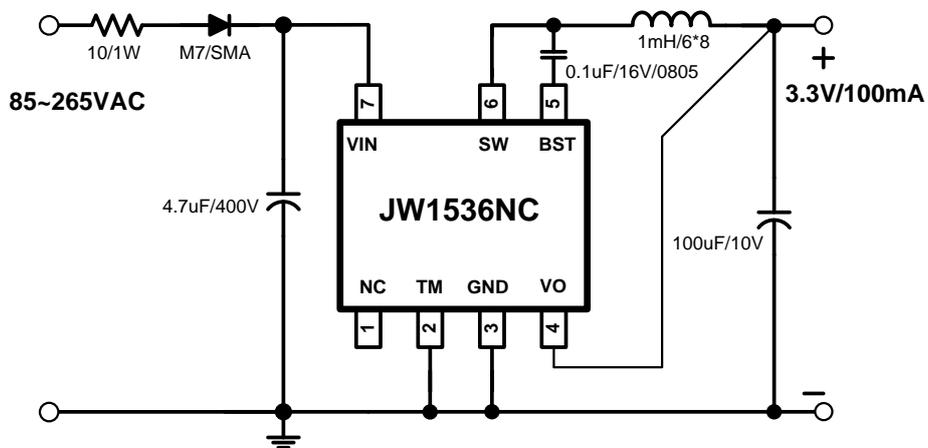
Reference 1:

The reference design is suitable for non-isolated buck power supply default 3.3V output, using JW1536NC.

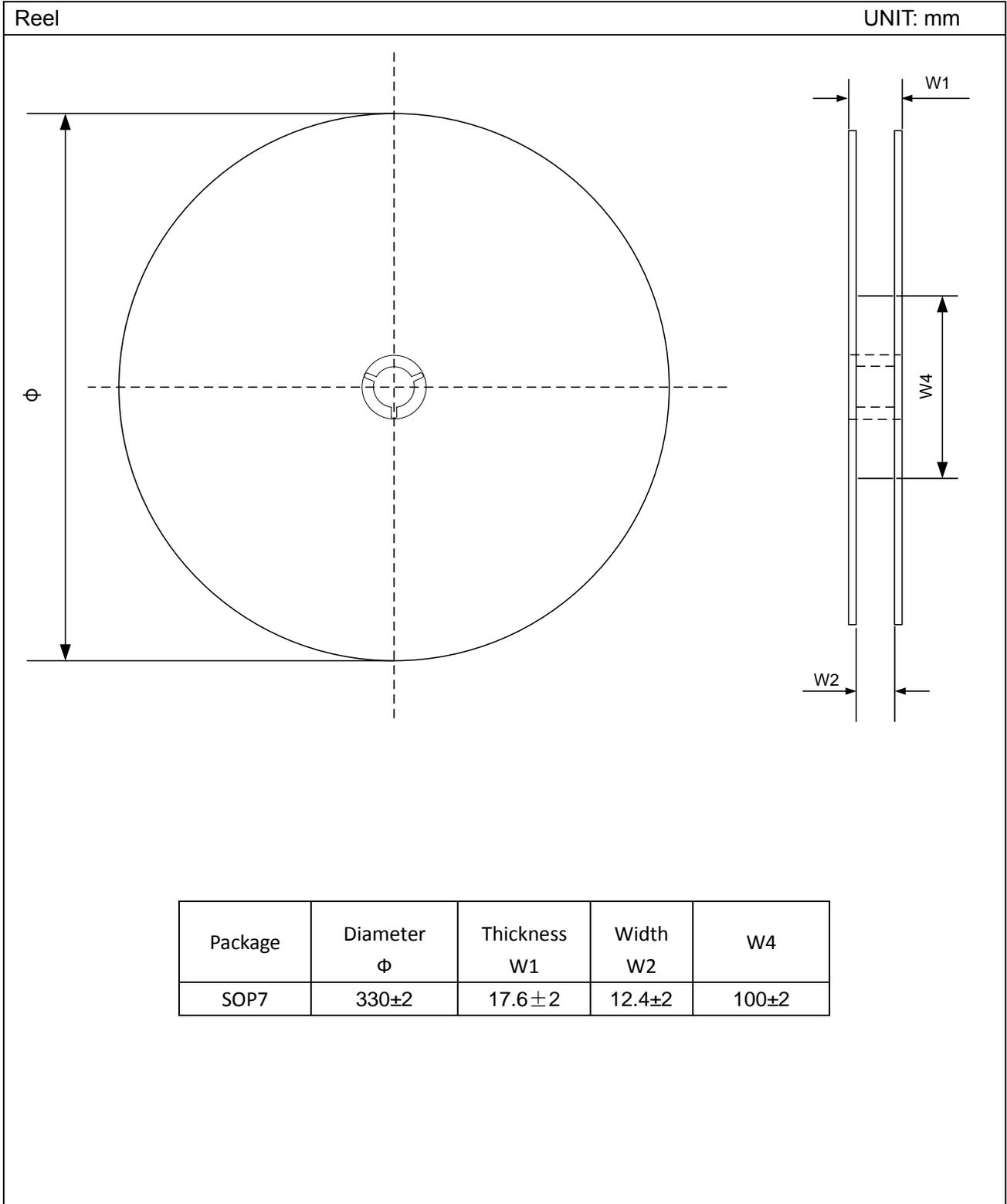
V_{IN} : 85~265VAC

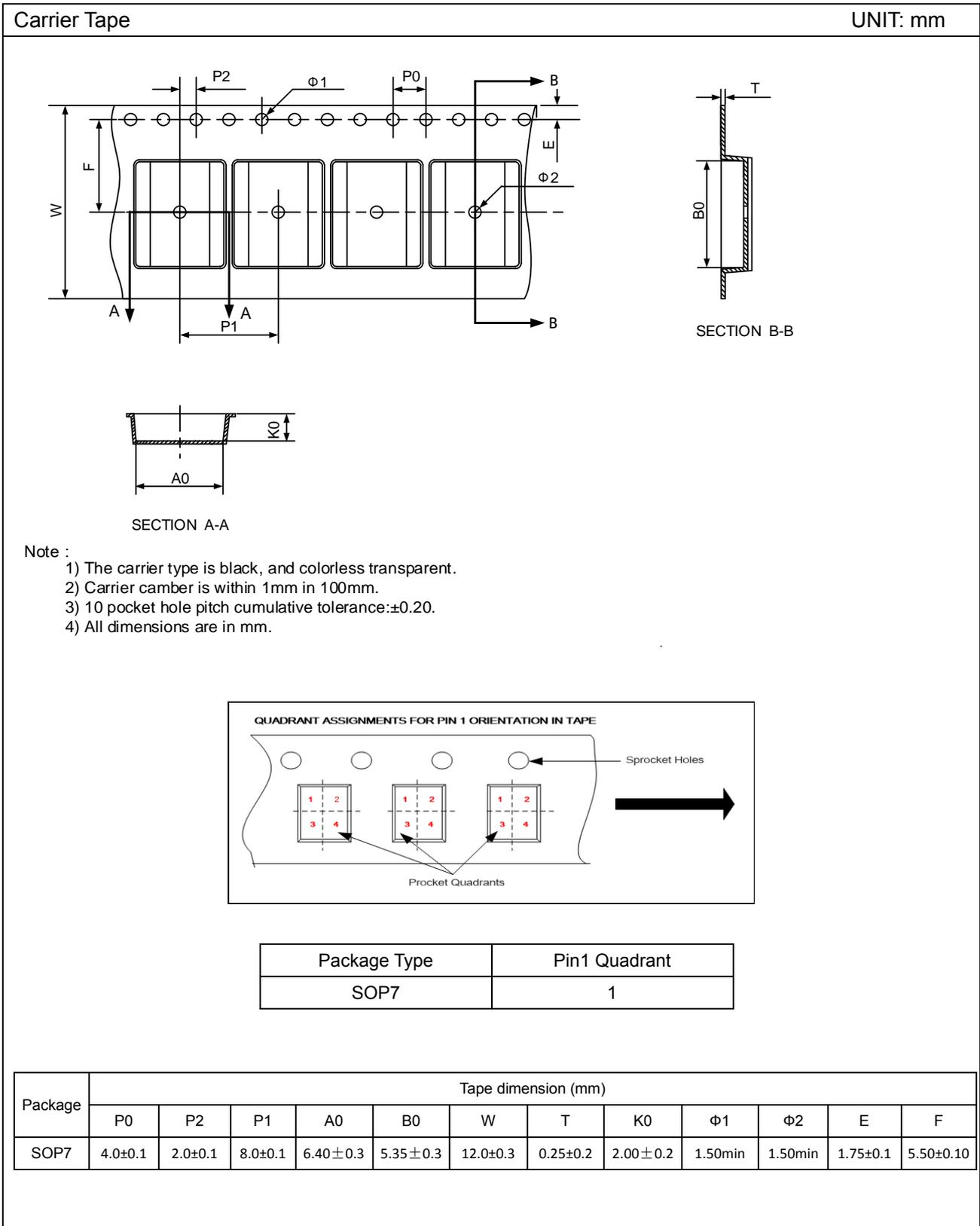
V_{OUT} : 3.3V

I_{OUT} : 100mA

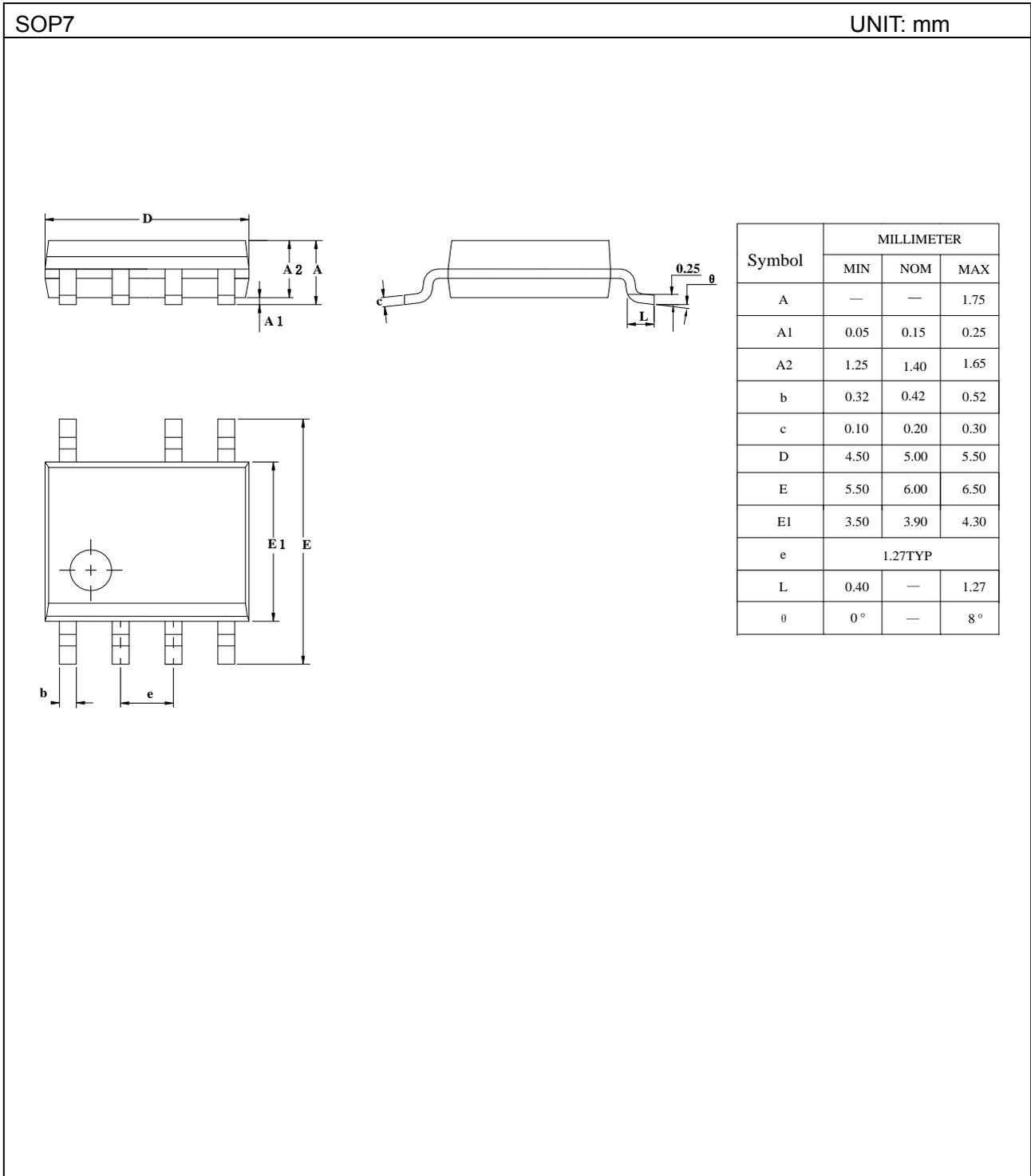


TAPE AND REEL INFORMATION





PACKAGE OUTLINE



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