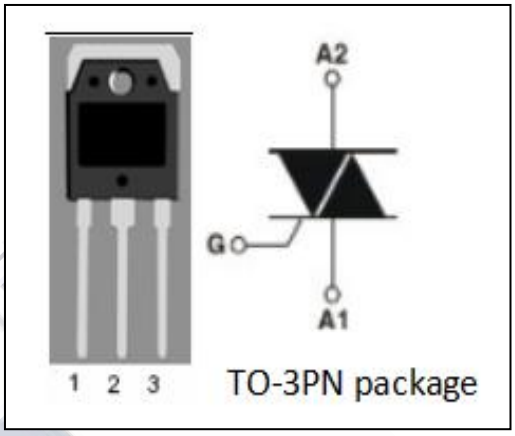
JST41Z-1200BW

DESCRIPTION

- With TO-3PN packaging
- Insulated package
- Can be operated in 3 quadrants
- Advanced technology to provide customers with high commutation performances
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



APPLICATIONS

- Switching applications
- Phase control
- Static switching on inductive or resistive load

ABSOLUTE MAXIMUM RATINGS(T_a=25℃)

SYMBOL	PARAMETER		MAX	UNIT
V_{DRM}	Repetitive peak off-state voltage		1200	V
V_{RRM}	Repetitive peak reverse voltage		1200	V
$I_{T(RSM)}$	Average on-state current	Tc=75℃	40	A
I_{TSM}	Surge non-repetitive on-state current	50HZ	400	A
$P_{G(AV)}$	Average gate power dissipation (over any 20 ms period)		1	W
T_j	Operating junction temperature		-40~125	℃
T_{stg}	Storage temperature		-40~150	℃

ELECTRICAL CHARACTERISTICS (T_C=25℃ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS		MIN	MAX	UNIT
I_{RRM}	Repetitive peak reverse current	$V_R=V_{RRM}$ Rated;	Tj=25℃		0.01	mA
I_{DRM}	Repetitive peak off-state current	$V_D=V_{DRM}$ Rated;	Tj=125℃		5	
V_{TM}	On-state voltage	I_T=60A； t_P=380 μ s			1.55	V
I_{GT}	Gate-trigger current	V_D =12V;R_L=33Ω;	I		50	mA
			II		50	
			III		50	
V_{GT}	Gate-trigger voltage	V_D =12V;R_L=33Ω;			1.3	V
Rth (j-c)	Junction to case				0.9	℃/W

isc Thyristors **JST41Z-1200BW**

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