LCD MOUDULE SPECIFICATION FOR APPROVAL	DATE	21/10/06
	VER.	1.0
JHD202C	PAGE	1

# CONTENTS

1.	FEATURES	4
2.	MECHANICAL SPEC	4
3.	ABSOLUTE MAXIMUM RATING	5
4.	ELECTRICAL CHARACTERISTICS	5
5.	ELECTRO-OPTICAL CHARACTERISTICS	6
6.	QC/QA PROCEDURE	7
7.	RELIABILITY	8
8.	BLOCK DIAGRAM	9
9.	POWER SUPPLY	9
10.	TIMIING DIAGRAM	10
11.	AC CHARACTERISTICS	11
12.	INSTRUCTION SET	12
13.	INITIALIZATION SEQUENCE	13
14.	FONT TABLE	14
15.	HANDLING PRECAUTION	15
16.	EXTERNAL DIMENSION	18
17.	INTERFACE	19

LCD MOUDULE SPECIFICATION FOR APPROVAL	DATE	21/10/06
	VER.	1.0
JHD202C	PAGE	2

## 1. FEATURES

•Display construction ······ 20 Characters \* 2 Lines

•Display mode····· TN/STN

•Display type ···· Positive Reflective

•Backlight······ LED/4. 2VDC
•Viewing direction····· 6 o' clock
•Operating temperature···· Indoor

•Driving voltage ..... Single power

•Driving method··········· 1/16 duty, 1/4bias •Type······· COB (Chip On Board)

•Number of data line ······ 8-bit parallel

•Connector····· Pin

## 2. MECHANICAL DATA

	ITEM	WIDTH	HEIGHT	THICKNESS	UNIT
Modi	Module size		37. 0 116. 0 14. 0 (MAX)		mm
Viewing area		18.6	83. 0	ı	mm
	Construction		5*7		dots
character	Size	3. 2	5. 55	-	mm
	Pitch	3.8	6. 15	ı	mm
D. /	Size	0.6	0.65	_	mm
Dot	Pitch	0.65	0.70	_	mm
Diameter of mounting hole			3. 5		mm
W	eight	About 50		g	

Leb moebell billen ferifier	DATE	21/10/06
FOR APPROVAL	VER.	1.0
JHD202C	PAGE	3

# 3. ABSOLUTE MAXIMUM RATINGS

Characteristics	Symbol	Ratings
Operating Voltage	VDD	-0.3V to +7.0V
Driver Supply Voltage	Vico	VDD-12V to VDD+0.3V
Input Voltage Range	Vin	-0.3V to VDD + 0.3V
Operating Temperature	TA	0 ℃ to +60 ℃
Storage Temperature	Tsto	-55 ℃ to +125 ℃

# 4. ELECTRICAL CHARACTERISTICS

(TA = 25, VDD = 2.7 to 4.5V)

01	C		Limit		1114	Total Constitions		
Characteristics	Symbol	Min.	Тур.	Max.	Unit	Test Condition		
Operating Current	loo	-	0.2	0.4	mΑ	External clock (Note)		
Input High Voltage	Vih1	0.7VDD	-	VDD	V	Direct DO DAY DDZ ()		
Input Low Voltage	VIL1	-0.3	-	0.55	V	Pins:(E, RS, R/W, DB7 - 0)		
Input High Voltage	VIH2	0.7VDD	-	VDD	V	Pi- 0004		
Input Low Voltage	VIL2	-0.2	-	0.2VDD	V	Pin OSC1		
Input High Current	Іін	-1.0	-	1.0	μА	Pins: (RS, R/W, DB7 - 0)		
Input Low Current	lıL	-5.0	-15	-30	μА	VDD = 3.0V		
Output High	.,	0.75/55			v	Iон = - 0.1mA		
Voltage (TTL)	Voh1	0.75VDD	-	-	٧	Pins: DB7 - 0		
Output Low	Vol.1	_	_	0.2VDD	V	lot = 0.1mA		
Voltage (TTL)	VOL1	-	_	0.2000	٧	Pins: DB7 - 0		
Output High	Voh2	0.8VDD			V	Іон = - 40µА,		
Voltage (CMOS)	V OH2	0.6000	0.8000	-			v	Pins: CL1, CL2, M, D
Output Low	Valz		_	0.2VDD	V	Ιοι. = 40μΑ, Pins:		
Voltage (CMOS)	VOL2	-		0.2000	v	CL1, CL2, M, D		
Driver ON Resistance	B			20	ΚΩ	Io = ±50μA, VLCD = 4V		
(COM)	Rcom	-	-	20	K22	Pins: COM16 - 1		
Driver ON Resistance				30	ΚΩ	Io = $\pm$ 50μA, VLcn = 4V		
(SEG)	Rseg	-	-	30	1/77	Pins: SEG40 - 1		
LCD Voltage	VLCD	3.0	-	11.0	V	VDD-V5, 1/4 bias or 1/5 bias		

Leb moebell billen ferifier	DATE	21/10/06
FOR APPROVAL	VER.	1.0
JHD202C	PAGE	4

## 5. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Contrast ratio	K	ф=0	1.4	4	-	_	1
Response time	Tr	ф=1	-	130	-	ms	2
(rise) Response time	Tf	ф=2		130	-	ms	2
(fall)	ф	Φ K ≥1.4 10 +30 -30 +30		)	dom	2	
Viewing angle	θ			0 +3	0	deg.	3

Note 1: Definition of Contrast Ratio "K"

В

Vop

10%

K= Brightness of non-selected segment(A)
Brightness of selected segment(B)

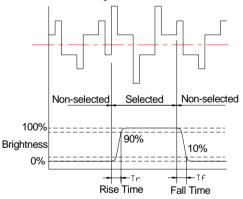
Brightness Curve of selected area

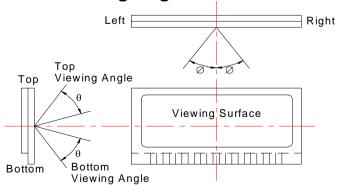
Brightness Curve of non-selected area

**Note 3: Definition of Viewing Angle** 

**Driving Voltage** 

Note 2: Definition of Optical Response Time

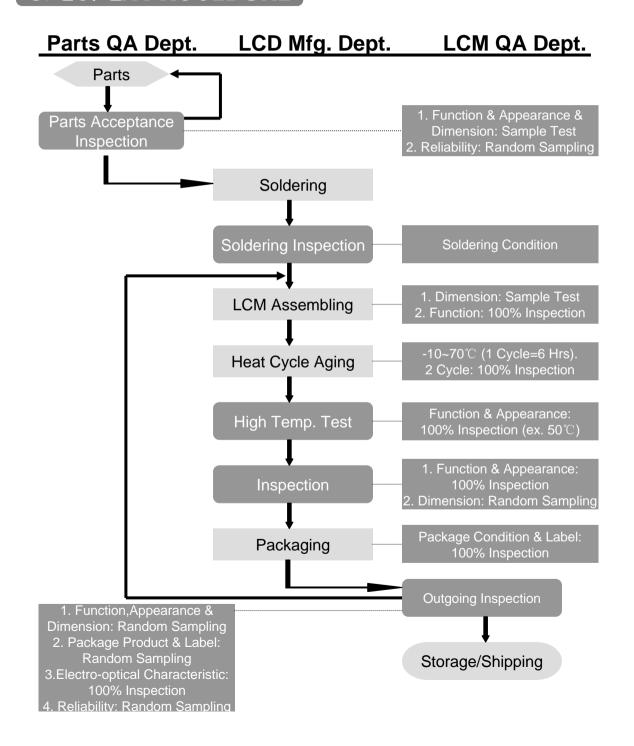




Please select either top or bottom viewing angle

LCD MOUDULE SPECIFICATION FOR APPROVAL	DATE	21/10/06
	VER.	1.0
JHD202C	PAGE	5

## 6. QC/QA PROCEDURE



LCD MOUDULE SPECIFICATION FOR APPROVAL	DATE	21/10/06
	VER.	1.0
JHD202C	PAGE	6

## 7. RELIABILITY

## •Operating life time:

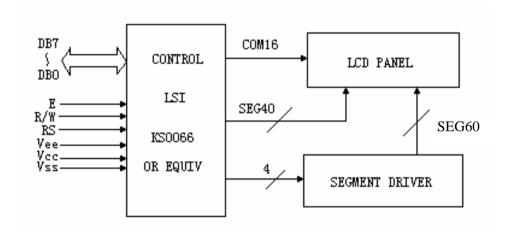
Longer than 50000 hours (at room temperature without direct irradiation of sunlight)

## •Reliability Characteristics:

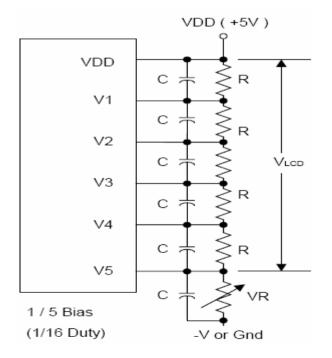
Item	Test	Criterion
High temp	70℃ / 200 Hrs	■Total current
Low temp.	-20℃ / 200 Hrs	consumption should be below double of
High humidity	40°C * 90%RH / 200 Hrs	initial value ■Contrast ratio
Thermal shock	-20°C→25°C→70°C→25°C /5 Cycles (30min) (5min) (30min) (5min)	should be within initial value ± 50%
Vibration	1. Operating time: Thirty minutes exposure in each direction (x, y, z) 2. Sweep Frequency (1min):10Hz→ 55Hz →10Hz 3. Amplitude: 0.75mm double amplitude	■No defect in cosmetic and operational function is allowable

LCD MOUDULE SPECIFICATION	DATE	21/10/06	
FOR APPROVAL	VER.	1.0	
JHD202C	PAGE	7	

# 8. BLOCK DIAGRAM



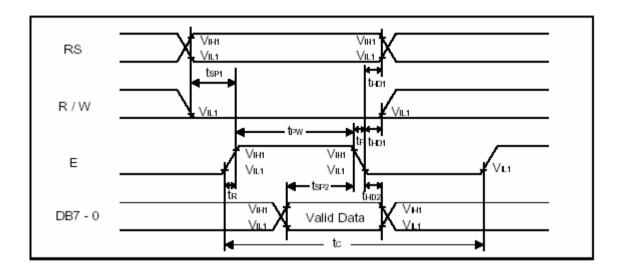
## 9. POWER SUPPLY



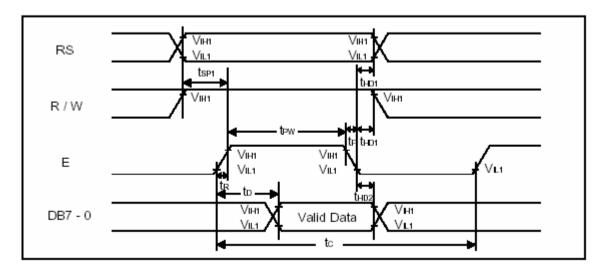
LCD MOUDULE SPECIFICATION	DATE	21/10/06	
FOR APPROVAL	VER.	1.0	
JHD202C	PAGE	8	

# 10. TIMING DIAGRAM

## • WRITE OPERATION



## READ OPERATION



	DATE	21/10/06	
FOR APPROVAL	VER.	1.0	
JHD202C	PAGE	9	

# 11. AC CHARACTERISTICS

#### • WRITE MODE

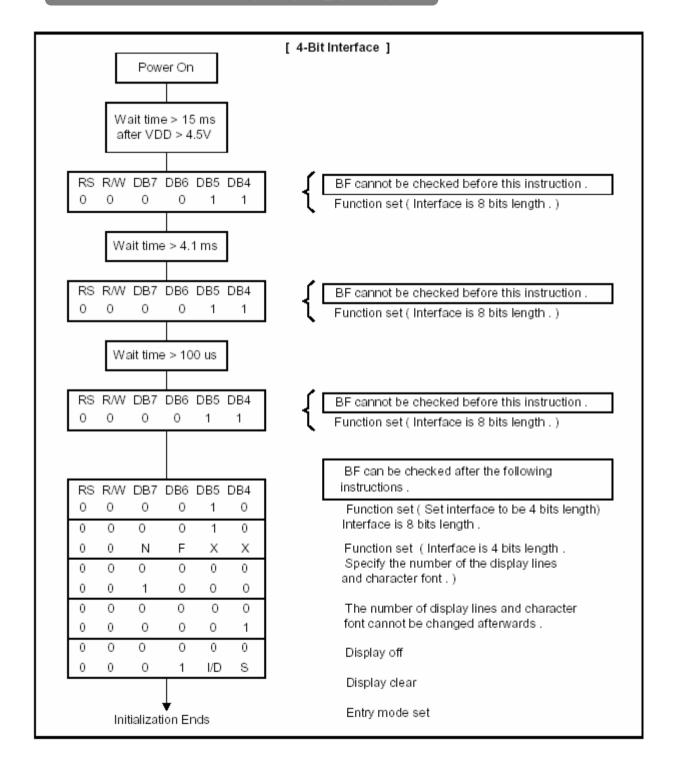
			Limit		Unit		
Characteristics	Symbol	Min.	Тур.	Тур. Мах.		Test Condition	
E Cycle Time	tc	1000	-	-	ns	Pin E	
E Pulse Width	tew	450	,	-	ns	Pin E	
E Rise/Fall Time	tr, tr	-	-	25	ns	Pin E	
Address Setup Time	tsp1	60	1	-	ns	Pins: RS, R/W, E	
Address Hold Time	tho1	20	-	-	ns	Pins: RS, R/W, E	
Data Setup Time	tsp2	195	-	-	ns	Pins: DB7 - 0	
Data Hold Time	<b>t</b> HD2	10	-	-	ns	Pins: DB7 - 0	

## • READ MODE

			Limit				
Characteristics	Symbol	Min.	Min. Typ. Max.		Unit	Test Condition	
E Cycle Time	tc	1000	1	-	ns	Pin E	
E Pulse Width	tw	450	1	-	ns	Pin E	
E Rise/Fall Time	tr, tr	ı	1	25	ns	Pin E	
Address Setup Time	tsp1	60	1	-	ns	Pins: RS, R/W,E	
Address Hold Time	thor	20	1	-	ns	Pins: RS, R/W,E	
Data Output Delay Time	tɒ	·	ı	360	ns	Pins: DB7 - 0	
Data hold time	t <sub>HD2</sub>	5.0	-	-	ns	Pin DB7 - 0	

202 110020222220110111011	DATE	21/10/06
FOR APPROVAL	VER.	1.0
JHD202C	PAGE	10

## 12. INITIALIZATION SEQUENCE



	DATE	21/10/06	
FOR APPROVAL	VER.	1.0	
JHD202C	PAGE	11	

# 13. INSTRUCTION SET

COMMAND	COMMAND CODE						ODE		COMMAND CODE	E-CYCLE		
COMMAND	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	COMMAND CODE	f <sub>osc</sub> =250KHz
SCREEN CLEAR	0	0	0	0	0	0	0	0	0	1	Screen Clear, Set AC to 0 Cursor Reposition	1.64ms
CURSOR RETURN	0	0	0	0	0	0	0	0	1	*	DDRAM AD=0, Return, Content Changeless	1.64ms
INPUT SET	0	0	0	0	0	0	0	1	I/D	S	Set moving direction of cursor, Appoint if move	40us
DISPLAY SWITCH	0	0	0	0	0	0	1	D	С	В	Set display on/off,cursor on/off, blink on/off	40us
SHIFT	0	0	0	0	0	1	S/C	R/L	*	*	Remove cursor and whole display,DDRAM changeless	40us
FUNCTION SET	0	0	0	0	1	DL	Ν	F	*	*	Set DL,display line,font	40us
CGRAM AD SET	0	0	0	1 ACG				CG			Set CGRAM AD, send receive data	40us
DDRAM AD SET	0	0	1		ADD						Set DDRAM AD, send receive data	40us
BUSY/AD READ CT	0	1	BF				AC				Executing internal function, reading AD of CT	40us
CGRAM/ DDRAM DATA WRITE	1	0			D	ATA'	WRIT	Έ			Write data from CGRAM or DDRAM	40us
CGRAM/ DDRAM DATA READ	1	1				DATA	REAI	)			Read data from CGRAM or DDRAM	40us
	I/D=1: Increment Mode; I/D=0: Decrement Mode S=1: Shift S/C=1: Display Shift; S/C=0: Cursor Shift R/L=1: Right Shift; R/L=0: Left Shift DL=1: 8D DL=0: 4D N=1: 2R N=0: 1R F=1: 5x10 Style; F=0: 5x7 Style BF=1: Execute Internal Function; BF=0: Command Received								DDRAM: Display data RAM CGRAM: Character Generator RAM ACG: CGRAM AD ADD: DDRAM AD & Cursor AD AC: Address counter for DDRAM & CGRAM	E-cycle changing with main frequency. Example: If fcp or fosc=270KHz  40us x 250/270 =37us		

LCD MOUDULE SPECIFICATION	DATE	21/10/06	
FOR APPROVAL	VER.	1.0	
JHD202C	PAGE	12	

# 14. FONT TABLE

√ b7-													
b3 b4 -b0	0000	0010	0011	0100	0101	0110	0111	1010	1011	1100	1101	1110	1111
0000	CG/ RAM (1)			3	<b></b> -	*-	<b>F=</b> •			-::	<b>=</b> _	CC	
0001	(2)		<u>i</u>	<b>  </b>		-≣:	-::4	<u>Ci</u>	<u>.</u> F	<b>;</b>	i		
0010	(3)	11	2				<b>}</b>	<b>=</b>	4	ij	×	<b>#</b>	
0011	(4)	#	.5		===	i	<b>Ξ</b> .	_i	-	<b>#</b>	==	:::-	000
0100	(5)	\$	#				<b>†</b> .	٠.		<b>!</b> -	•••	<b> </b> 4	<b>:</b>
0101	(6)	<b>:</b> :::				<b>:</b>	<b>L.</b> .	==	<b>-</b>	<b>;</b>		· · ·	ü
0110	(7)	8.	6	<b></b>	Ų	+"	Ų	<b>=</b>	ij		===	p	Σ
0111	CG/ RAM (8)		7		ij,	===	ijij	<u>.</u> F	#	[X	<b>-</b>		Л
1000	CG/ RAM	ĺ.	8	<b>i-i</b>	×	<b>i</b> 1	×	I		<b>;</b>	ij	.j-	$\overline{\times}$
1001	(2)	>	9	I	¥	i.	-==	:	7	<u>.</u> !	ıĿ	≣	<b>-</b> 4
1010	(3)	:#::	# #		2	<b>i</b>	<b>3</b> 2.			iì	<u> </u>		<b>∓</b>
1011	(4)		# #	K		k	₹	<b>;</b>	<b>#</b>			: <b>"</b> :::	Fi
1100	(5)	<b>.</b>	<	<u></u>	#	1		1:	<b>=</b> .:		ŗŢ	4	24
1101	(6)		===	H		m	<b>}</b>		<u></u>	^,	 =	#_	
1110	(7)	==	>	ŀŀ		l'"i	-3-	==	13			ŀ̈̈	
1111	CG/ RAM/(8)	.*	?				<del>+</del> -	• • • •	<u>'.</u> '	7	<b>III</b>	Ö	

	DATE	21/10/06	
FOR APPROVAL	VER.	1.0	
JHD202C	PAGE	13	

## 15. Handling Precautions

#### 1. Limitation of Application:

Optrex products are designed for use in ordinary electronic devices such as business machines, telecommunications equipment, measurement devices and etc. Please handle the products with care. (see below)

Optrex products are not designed,intended ,or authorized for use in any application which the failure of the product could result in a situation where personal injury or death may occur . these applications include, but are not limited to . life-sustaining equipment,nuclear control devices , aerospace equipment , devices related to hazardous or flammable materials , etc.[If Buyer intends to purchase or use the Optrex Products for such unintended or unauthorized applications , Buyer must secure prior written consent to such use by a responsible officer of Optrex Corporation.]Should Buyer purchase or use Optrex Products for any such unintended or unauthorized application [ without such consent ].Buyer shall indemnify and hold Optrex and its officers. employees. subsidiaries, affiliates and distributors harmless against all claims, costs, damages and expenses , and reasonable attorney's fees, arising out of , directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Optrex was negligent regarding the design or manufacture of the part. 2.Industrial Rights and Patents

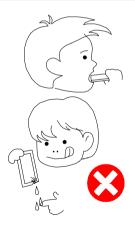
Optrex shall not be responsible for any infringement of industrial property rights of third parties in any country arising out of the application or use of Optrex products, except which directly concern the structure or production of such products.

#### No Press and Shock!

# If pressure to LCD, orientation may be disturbed. LCD will broken by shock!

#### Don't Swallow or Touch Liquid Crystal!

Liquid Crystal may be leaked when display is broked. If it accidentally gets your hands, wash then with water!



#### Don't not Scratch!

## No DC Voltage to LCD!



DC volrage or driveing higher than the specified voltage will reduce the lifetime of the LCD





LCD MOUDULE SPECIFICATION	DATE	21/10/06	
FOR APPROVAL	VER.	1.0	
JHD202C	PAGE	14	

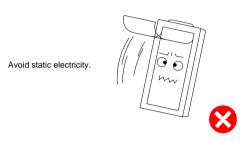
## Don't Press the Metallic Frame and Disassemble the LCM

### Slowly Peel Off Protective Film!

Pressure on the metallic frame and PCB may deform the conductive rubber or break the liquid crystal cell and back light, which will cause defects.

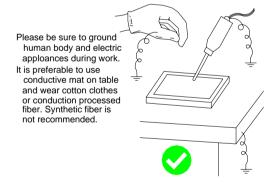
LCD may be shifted or conductive rubber may be reshaped, which will cause defects.

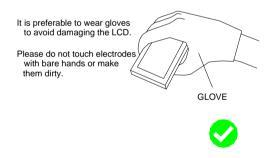




#### **Avoid Static Electricity!**

## Wear Gloves While Handing!



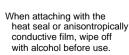


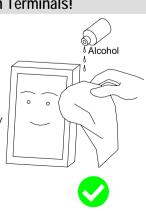
## Keep Away From Extreme Heat and Humidity!

## **Use Alcohol to Clean Terminals!**









	DATE	21/10/06
FOR APPROVAL	VER.	1.0
JHD202C	PAGE	15

#### Don't Drop Water on LCD!

Note that the presence of waterdrops or dew in the LCD panel may deteriorate the polarizer or corrade electrode.



#### Precaution in Soldering LCD Module

Basic instructions: Solder I/O terminals only.

Use soldering iron without leakage.

(1)Soldering condition to I/O terminals

Temperature at tip of the iron:  $280\pm10^{\circ}$ C

Soldering time: 3~4 sec.

Type of solder: Eutectic solder (containing colophony-flux)

- \*Please do not use flux because it may soak into LCD Module or contaminate it.
- \*It is preferable to peel off protective film on display surface after soldering I/O terminals is finished.
- (2)Remove connector or cable
  - \*When you remove connector or cable soldered to I/O terminals, please confirm that solder is fully melted. If you remove by force, electrodes at I/O terminals may be damaged(or stripped off).
  - \*It is recommended to use solder suction machine.

#### Long-term Storage

If it is necessary to store LCD modules for a long time, please comply with the following procedures.

If storage condition is not satisfactory, display(especially polarizer) may be deteriorated or soldering I/O terminals may become difficult(some oxide is generated at I/O terminals plating).

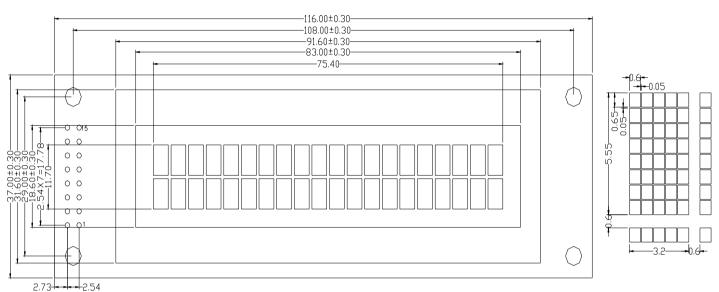
- 1.Store as delivered by Optrex
- 2.If you store as unpacked,put in anti-static bag,seal its opening and store where it is not subjected to direct sunshine nor fluorescent lamp.
- 3. Store at temperature 0 to +35°C and at low humidity. Please refer to our specification sheets for storage temperature range and humidity condition.

#### Long-term Storage

Please use power supply with built-in surge protection circuit.

ECD WOOD CEE STECK TOTALION	DATE	21/10/06
FOR APPROVAL	VER.	1.0
JHD202C	PAGE	16

# 16. EXTERNAL DIMENSION



202 110 02 022 51 2011 10111101	DATE	21/10/06
FOR APPROVAL	VER.	1.0
JHD202C	PAGE	17

# 17. INTERFACE

1	VSS	GROUND	0V (GND)	
2	VCC	POWER SUPPLY FOR LOGIC CIRCUIT	+5V	
3	VEE	LCD CONTRAST ADJUSTMENT		
4	RS	INSTRUCTION/DATA REGISTER SELECTION	RS = 0 : INSTRUCTION REGISTER RS = 1 : DATA REGISTER	
5	R/W	READ/WRITE SELECTION	R/W = 0 : REGISTER WRITE R/W = 1 : REGISTER READ	
6	Е	ENABLE SIGNAL		
7	DB0			
8	DB1	]		
9	DB2	]		
10	DB3	DATA BUS	8 BIT: DB0-DB7	
11	DB4	DATA BUS	8 BH. DBU-DB/	
12	DB5			
13	DB6			
14	DB7	]		
15	LED-	SUPPLY VOLTAGE FOR LED-	0V	
16	LED+	SUPPLY VOLTAGE FOR LED+	+5V	