

IV1Q12160D7Z– 1200V 160mΩ Automotive SiC MOSFET

Features

- Low impedance package with driver source pin
- High blocking voltage with low on-resistance
- High speed switching with low capacitance
- High operating junction temperature capability
- Very fast and robust intrinsic body diode

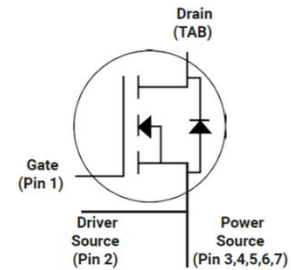
Applications

- Solar inverters
- EV battery chargers
- High voltage DC/DC converters
- Switch mode power supplies

Outline:



TO263-7



Marking Diagram:

YYWWZ
XXXX
1Q12160D7Z

1Q12160D7Z = Specific Device Code
 YY = Year
 WW = Work Week
 Z = Assembly Location
 XXXX = Lot Traceability

Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
V_{DS}	Drain-Source voltage	1200	V	$V_{GS}=0V$, $I_D=100\mu A$	
$V_{GSmax}(DC)$	Maximum DC voltage	-5 to 22	V	Static (DC)	
$V_{GSmax}(Spike)$	Maximum spike voltage	-10 to 25	V	<1% duty cycle, and pulse width<200ns	
V_{GSon}	Recommended turn-on voltage	20 ± 0.5	V		
V_{GSoff}	Recommended turn-off voltage	-3.5 to -2	V		
I_D	Drain current (continuous)	19	A	$V_{GS}=20V$, $T_c=25^\circ\text{C}$	Fig. 21
		14	A	$V_{GS}=20V$, $T_c=100^\circ\text{C}$	
I_{DM}	Drain current (pulsed)	47.5	A	Pulse width limited by SOA	Fig. 24
P_{TOT}	Total power dissipation	108	W	$T_c=25^\circ\text{C}$	Fig. 22
T_{stg}	Storage temperature range	-55 to 175	$^\circ\text{C}$		
T_J	Operating junction temperature	-55 to 175	$^\circ\text{C}$		

Thermal Data

Symbol	Parameter	Value	Unit	Note
$R_{\theta(J-C)}$	Thermal Resistance from Junction to Case	1.380	$^\circ\text{C/W}$	Fig. 23

Electrical Characteristics (T_c=25°C unless otherwise specified)

Symbol	Parameter	Value			Unit	Test Conditions	Note
		Min.	Typ.	Max.			
I _{DSS}	Zero gate voltage drain current		5	100	μA	V _{DS} =1200V, V _{GS} =0V	
I _{GSS}	Gate leakage current			±100	nA	V _{DS} =0V, V _{GS} =-5~20V	
V _{TH}	Gate threshold voltage	1.8	2.9	5	V	V _{GS} =V _{DS} , I _D =1.9mA	Fig. 8, 9
			1.9			V _{GS} =V _{DS} , I _D =1.9mA @ T _C =175°C	
R _{ON}	Static drain-source on-resistance		160	195	mΩ	V _{GS} =20V, I _D =10A @T _J =25°C	Fig. 4, 5, 6, 7
			250		mΩ	V _{GS} =20V, I _D =10A @T _J =175°C	
C _{iss}	Input capacitance		885		pF	V _{DS} =800V, V _{GS} =0V, f=1MHz, V _{AC} =25mV	Fig. 16
C _{Oss}	Output capacitance		38		pF		
C _{rss}	Reverse transfer capacitance		2		pF		
E _{Oss}	C _{Oss} stored energy		16		μJ		Fig. 17
Q _g	Total gate charge		43		nC	V _{DS} =800V, I _D =10A, V _{GS} =-5 to 20V	Fig. 18
Q _{gs}	Gate-source charge		9		nC		
Q _{gd}	Gate-drain charge		19		nC		
R _g	Gate input resistance		9.5		Ω	f=1MHz	
E _{ON}	Turn-on switching energy		115.6		μJ	V _{DS} =800V, I _D =10A, V _{GS} =-3.5 to 20V R _{G(ext)} =5Ω L=270μH	Fig. 19, 20
E _{OFF}	Turn-off switching energy		25.9		μJ		
t _{d(on)}	Turn-on delay time		4.8		ns		
t _r	Rise time		9.8				
t _{d(off)}	Turn-off delay time		12.8				
t _f	Fall time		12.1				

Reverse Diode Characteristics (T_c=25°C unless otherwise specified)

Symbol	Parameter	Value			Unit	Test Conditions	Note
		Min.	Typ.	Max.			
V _{SD}	Diode forward voltage		4.1		V	I _{SD} =5A, V _{GS} =0V	Fig. 10, 11, 12
			3.7		V	I _{SD} =5A, V _{GS} =0V, T _J =175°C	
t _{rr}	Reverse recovery time		32.7		ns	V _{DS} =800V, I _D =10A, V _{GS} =-3.5 to 20V R _{G(ext)} =24Ω di/dt=2A/ns L=270μH	
Q _{rr}	Reverse recovery charge		86.0		nC		
I _{RRM}	Peak reverse recovery current		7.6		A		

Typical Performance (curves)

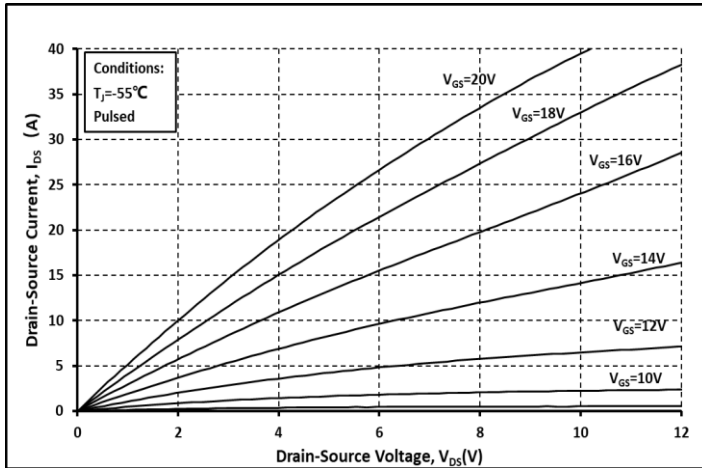


Fig. 1 Output Curve @ $T_j = -55^\circ\text{C}$

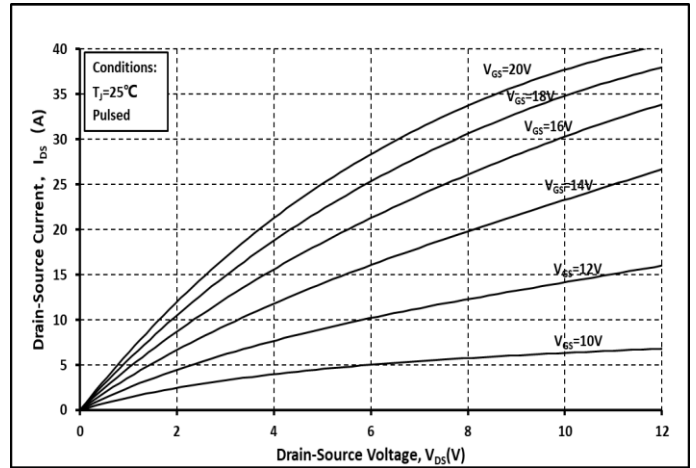


Fig. 2 Output Curve @ $T_j = 25^\circ\text{C}$

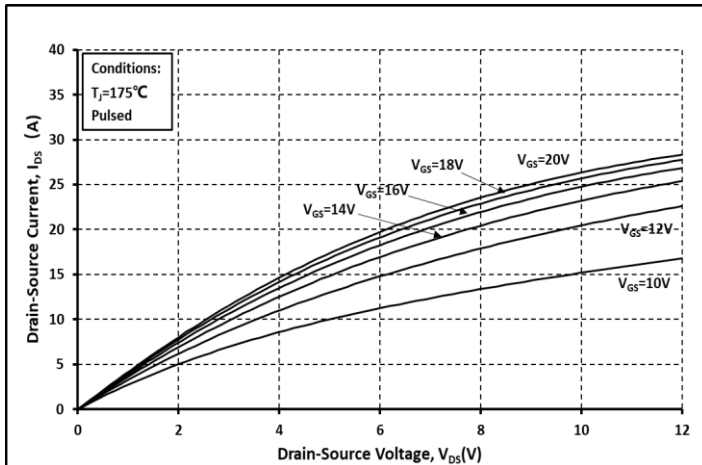


Fig. 3 Output Curve @ $T_j = 175^\circ\text{C}$

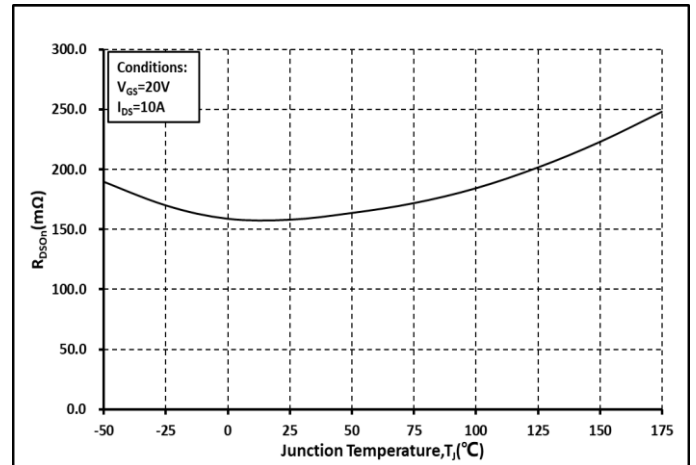


Fig. 4 R_{on} vs. Temperature

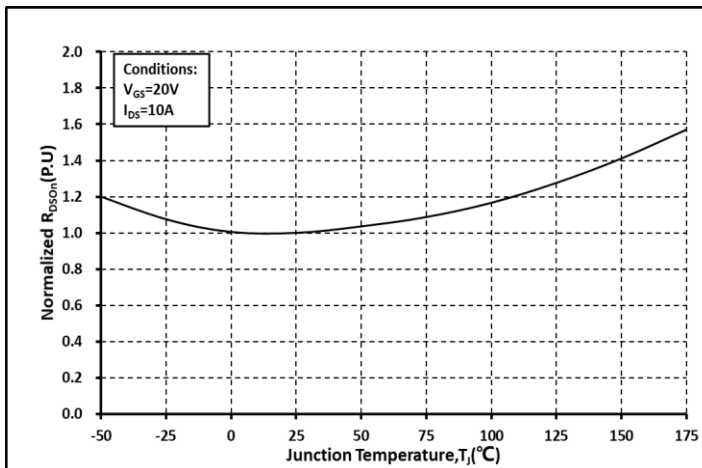


Fig. 5 Normalized R_{on} vs. Temperature

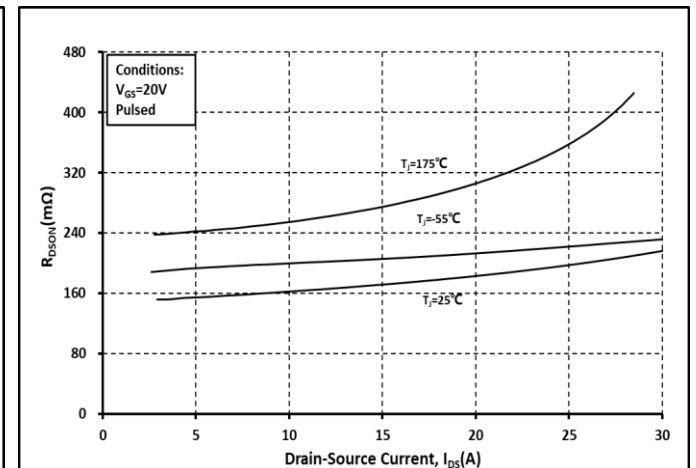


Fig. 6 R_{on} vs. I_{DS} @ Various Temperature

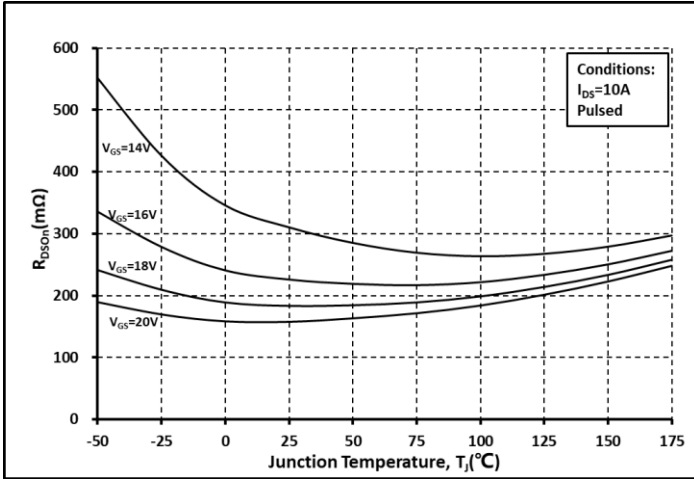


Fig. 7 Ron vs. Temperature @ Various V_{GS}

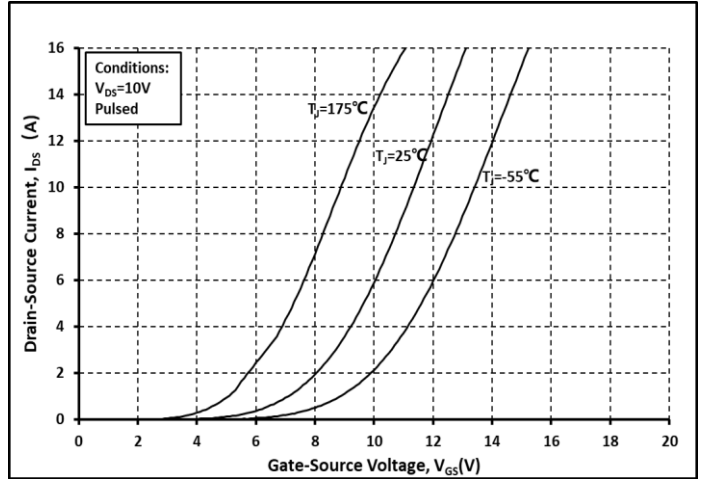


Fig. 8 Transfer Curves @ Various Temperature

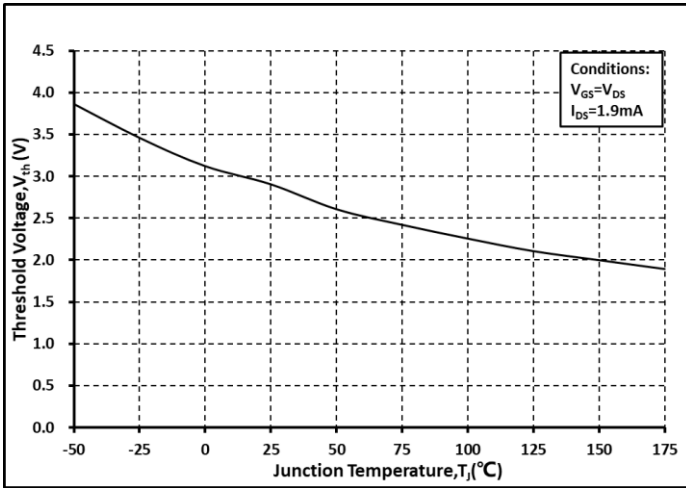


Fig. 9 Threshold Voltage vs. Temperature

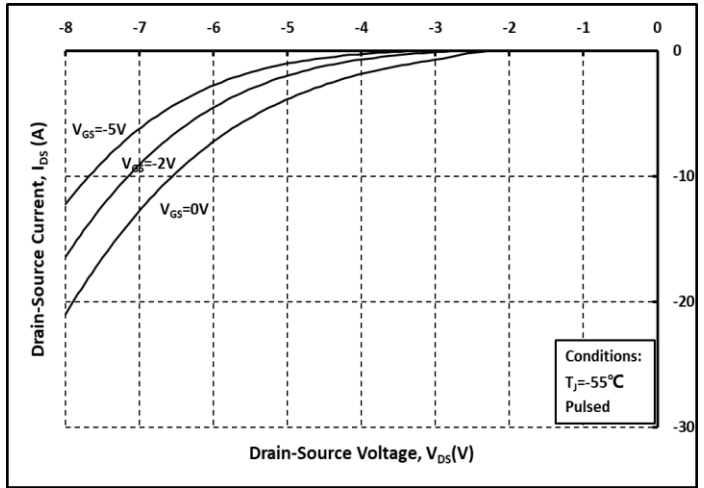


Fig. 10 Body Diode Curves @ $T_J=-55^{\circ}C$

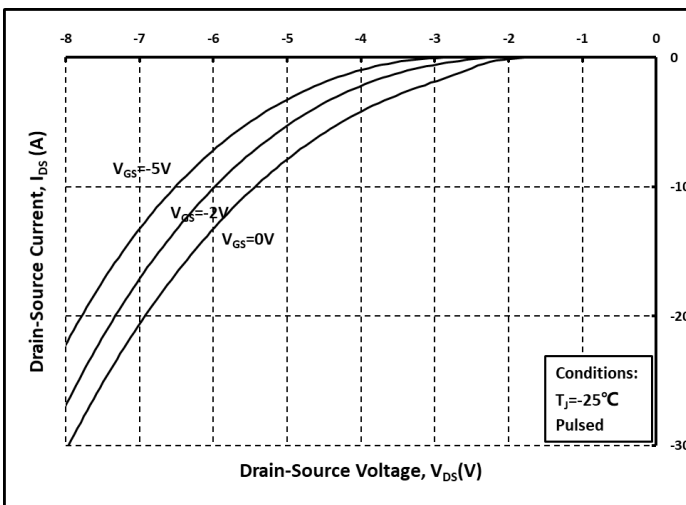


Fig. 11 Body Diode Curves @ $T_J=25^{\circ}C$

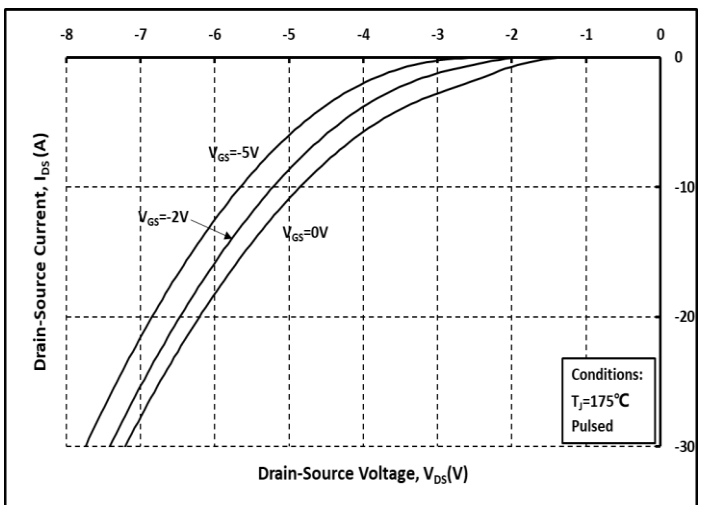


Fig. 12 Body Diode Curves @ $T_J=175^{\circ}C$

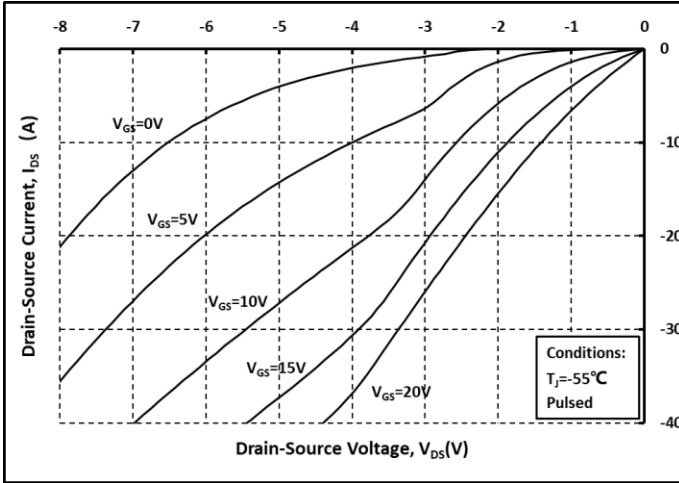


Fig. 13 3rd Quadrant Curves @ $T_j = -55^\circ\text{C}$

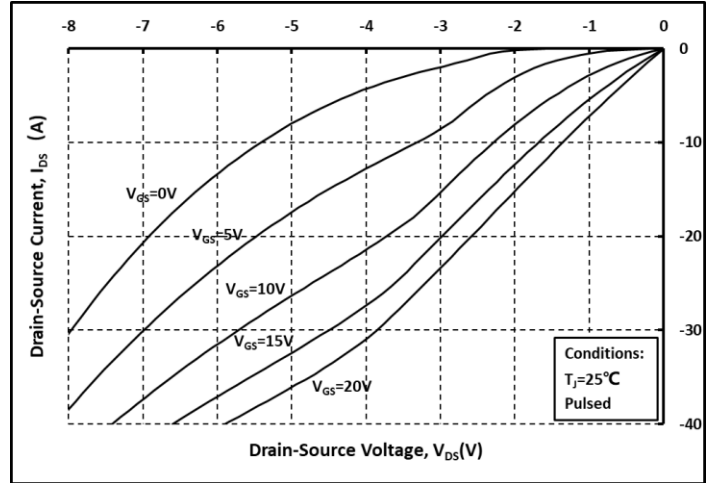


Fig. 14 3rd Quadrant Curves @ $T_j = 25^\circ\text{C}$

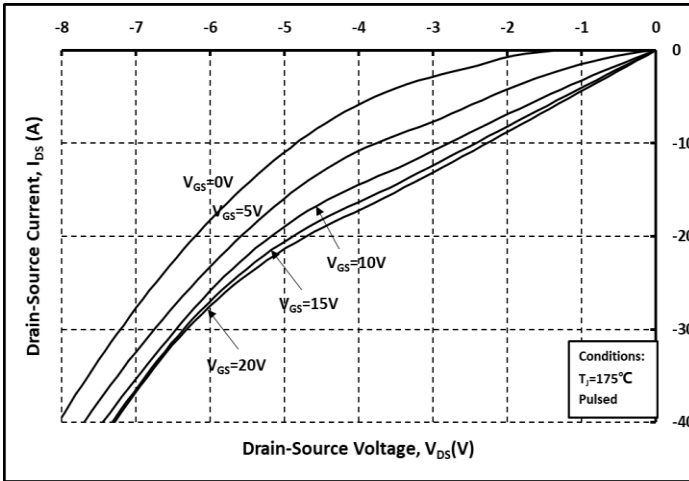


Fig. 15 3rd Quadrant Curves @ $T_j = 175^\circ\text{C}$

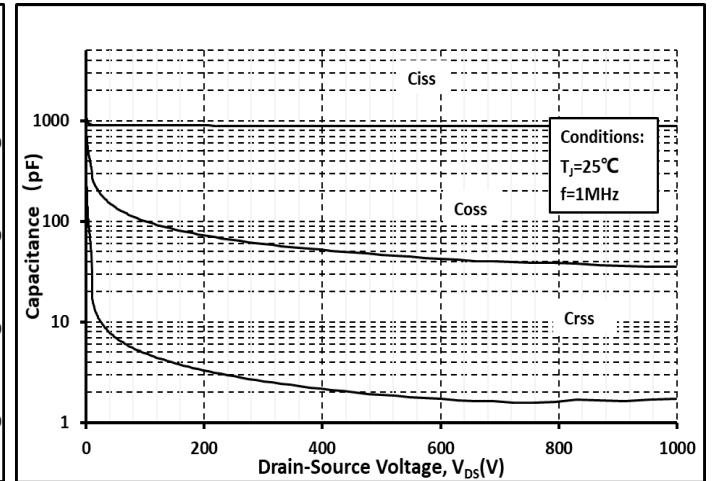


Fig. 16 Capacitance vs. V_{ds}

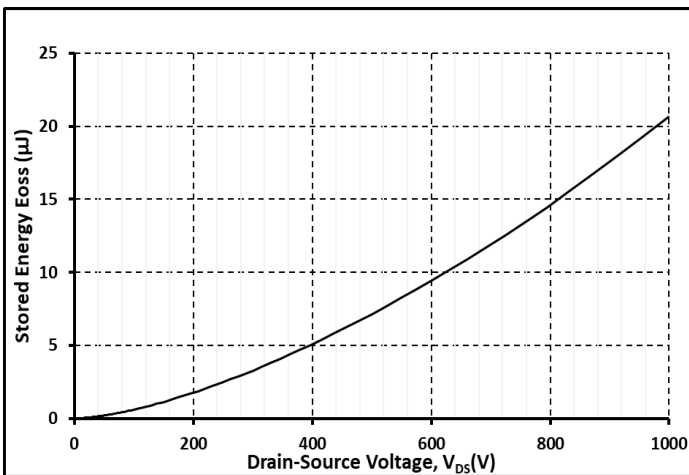


Fig. 17 Output Capacitor Stored Energy

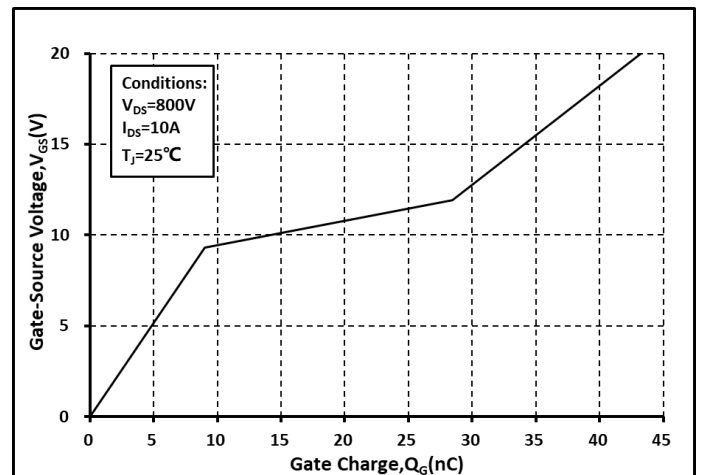


Fig. 18 Gate Charge Characteristics

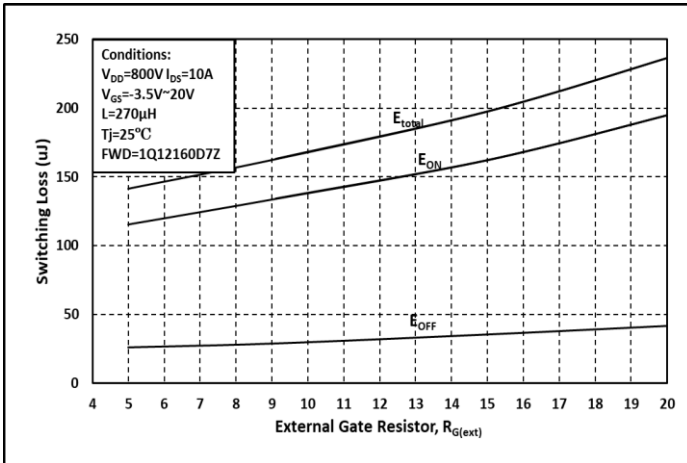


Fig. 19 Switching Energy vs. $R_{G(ext)}$

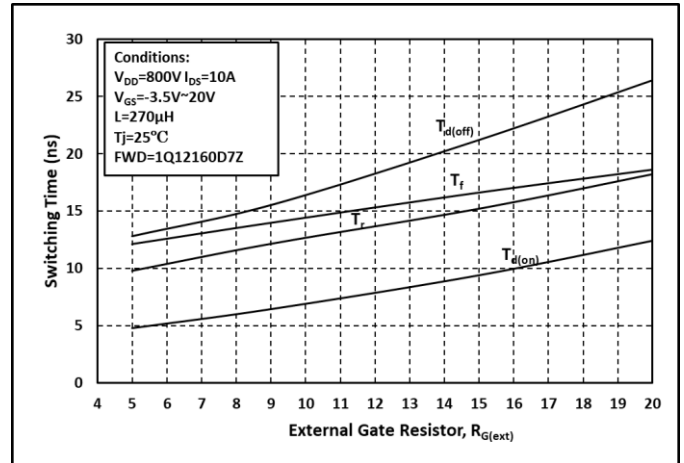


Fig. 20 Switching Times vs. $R_{G(ext)}$

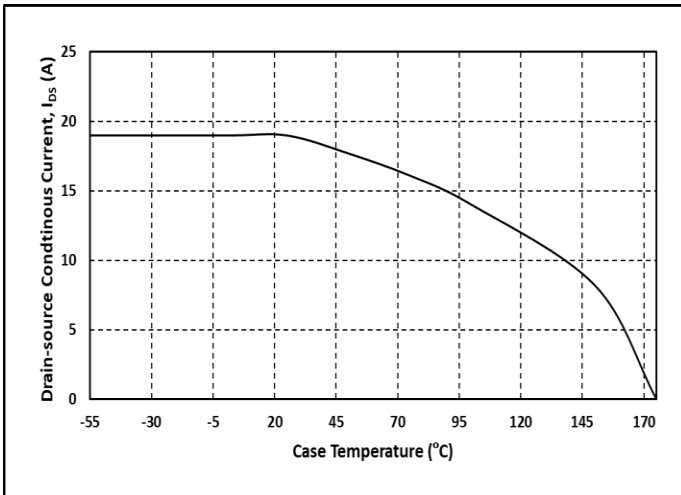


Fig. 21 Continuous Drain Current vs. Case Temperature

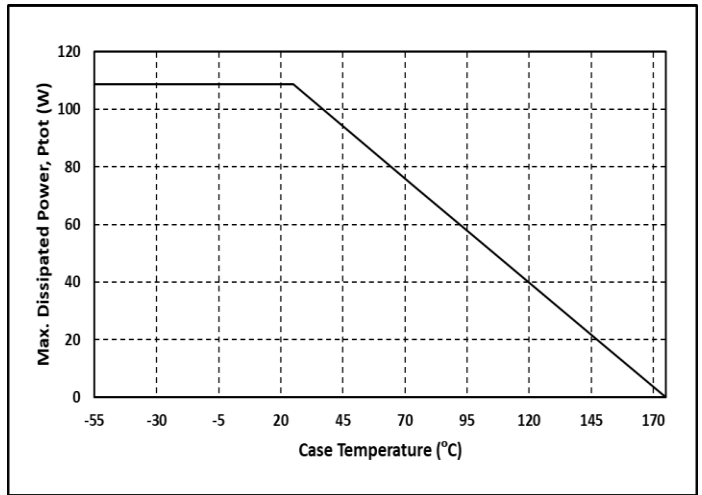


Fig. 22 Max. Power Dissipation Derating vs. Case Temperature

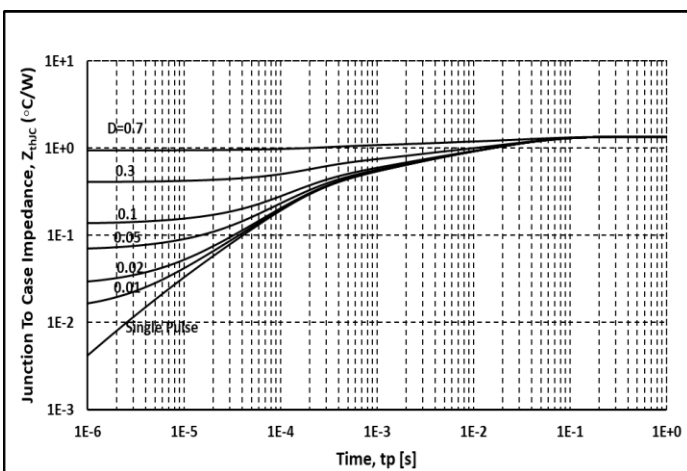


Fig. 23 Thermal Impedance

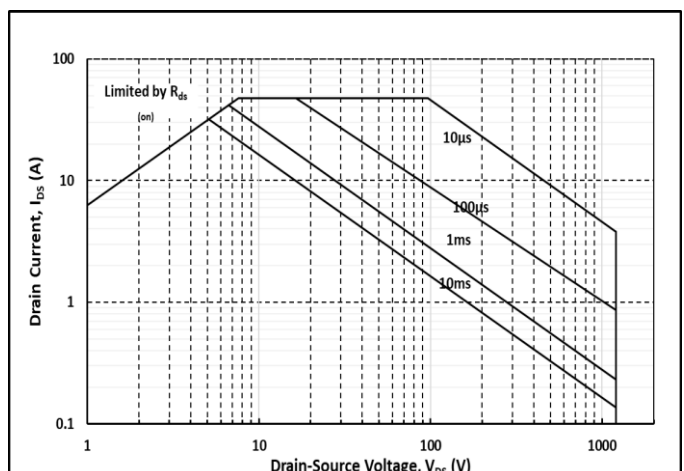
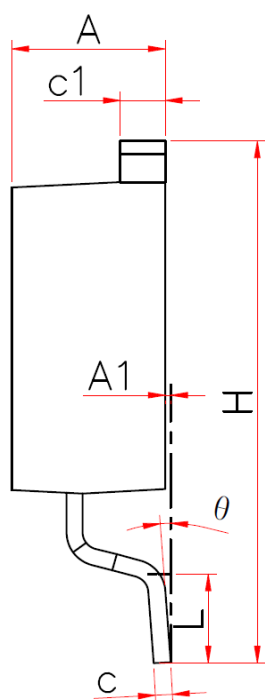
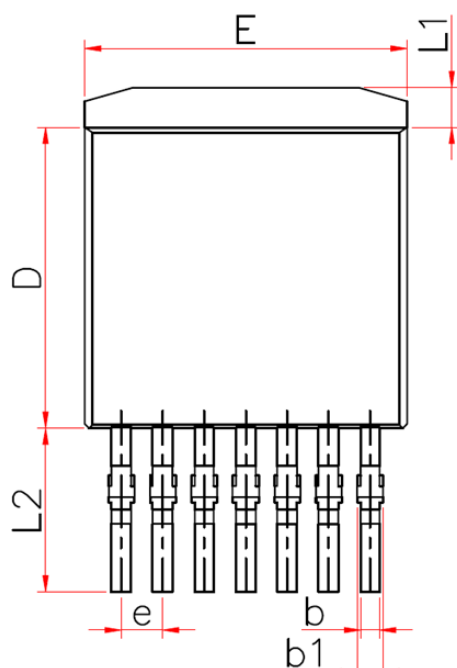
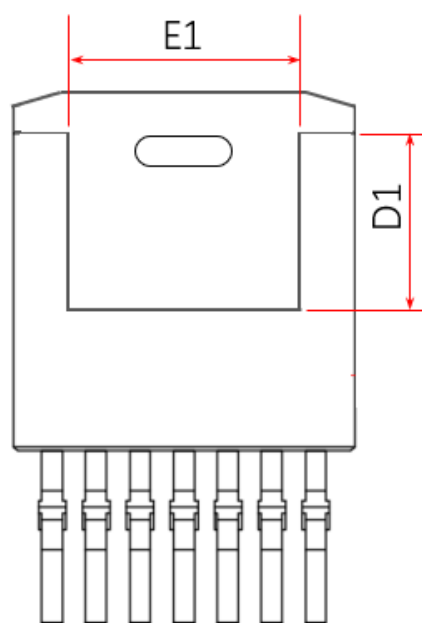


Fig. 24 Safe Operating Area

Package Dimensions



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.300	4.560
A1	—	0.250
b	0.500	0.700
b1	0.600	0.900
c	0.450	0.600
c1	1.200	1.400
D	8.930	9.230
D1	4.650	4.950
E	10.08 0	10.28 0
E1	6.820	7.620
e	1.27 REF.	
H	15.00 0	16.00 0
L	1.900	2.500
L1	0.980	1.420
L2	4.350	5.890
θ	0°	7°



Note:

1. Package Reference: JEDEC TO263, Variation AD
2. All Dimensions are in mm
3. Subject to Change Without Notice

Notes

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