ECOSPARK® Ignition IGBT

250 mJ, 400 V, N-Channel Ignition IGBT

ISL9V2540S3ST-F085C

Features

- SCIS Energy = 250 mJ at $T_J = 25^{\circ}C$
- Logic Level Gate Drive
- AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

Applications

- Automotive Ignition Coil Driver Circuits
- High Current Ignition System
- Coil on Plug Applications

MAXIMUM RATINGS (T_J = 25°C unless otherwise stated)

Parameter	Symbol	Value	Unit
Collector to Emitter Breakdown Voltage (I _C = 1 mA)	BV _{CER}	430	V
Emitter to Collector Voltage – Reverse Battery Condition (I _C = 10 mA)	BV _{ECS}	24	V
I_{SCIS} = 12.9 A, L = 3.0 mHy, R _{GE} = 1 k Ω , T _C = 25°C (Note 1)	E _{SCIS25}	250	mJ
I_{SCIS} = 10 A, L = 3.0 mHy, R _{GE} = 1 k Ω , T _C = 150°C (Note 2)	E _{SCIS150}	150	mJ
Collector Current Continuous, at V _{GE} = 4.0 V, T _C = 25°C	IC25	15.5	Α
Collector Current Continuous, at V _{GE} = 4.0 V, T _C = 110°C	IC110	15.3	Α
Gate to Emitter Voltage Continuous	V_{GEM}	±10	V
Power Dissipation Total, T _C = 25°C	PD	166.7	W
Power Dissipation Derating, T _C > 25°C	PD	1.11	W/°C
Operating Junction and Storage Temperature	T _J , T _{STG}	–40 to 175	°C
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)	TL	300	°C
Reflow soldering according to JESD020C	T _{PKG}	260	°C
HBM–Electrostatic Discharge Voltage at 100 pF, 1500 Ω	ESD	4	kV
CDM-Electrostatic Discharge Voltage at 1 Ω	ESD	2	kV

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- Self Clamped inductive Switching Energy (ESCIS25) of 250 mJ is based on the test conditions that is starting T_J = 25°C, L = 3 mHy, I_{SCIS} = 12.9 A, V_{CC} = 100 V during inductor charging and V_{CC} = 0 V during time in clamp.
 Self Clamped inductive Switching Energy (ESCIS150) of 150 mJ is based on
- Self Clamped inductive Switching Energy (ESCIS150) of 150 mJ is based on the test conditions that is starting T_J = 150°C, L = 3 mHy, I_{SCIS} = 10 A, V_{CC} = 100 V during inductor charging and V_{CC}=0 V during time in clamp.



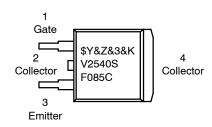
ON Semiconductor®

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D²PAK-3 CASE 418AJ

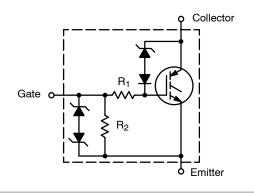
MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Date Code (Week & Year)

&K = Lot Code

V2540SF085C = Specific Device Code



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Case - Steady State (Drain)	$R_{ heta JC}$	0.9	°C/W

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS					•	•	•
Collector to Emitter Breakdown Voltage	BV _{CER}	I_{CE} = 2 mA, V_{GE} = 0 V, R_{GE} = 1 k Ω , T_{J} = -40 to 150°C		370	400	430	V
Collector to Emitter Breakdown Voltage	BV _{CES}	I_{CE} = 10 mA, V_{GE} = 0 V, R_{GE} = 0 Ω , T_{J} = -40 to 150°C		390	420	450	V
Emitter to Collector Breakdown Voltage	BV _{ECS}	$I_{CE} = -75 \text{ mA}, V_{GE} = 0 \text{ V}, T_{J} = 25^{\circ}\text{C}$		30	-	-	V
Gate to Emitter Breakdown Voltage	BV _{GES}	$I_{GES} = \pm 2 \text{ mA}$		±12	±14	-	V
Collector to Emitter Leakage	I _{CER}	V_{CE} = 175 V, R_{GE} = 1 k Ω	T _J = 25°C	1 -	_	25	μΑ
Current			T _J = 150°C	1 -	_	1	mA
Emitter to Collector Leakage	I _{ECS}	V _{EC} = 24 V	T _J = 25°C	1 -	_	1	mA
Current			T _J = 150°C	1 -	_	40	
Series Gate Resistance	R ₁		•	1 -	70	_	Ω
Gate to Emitter Resistance	R ₂			10	_	26	kΩ
ON CHARACTERISTICS				-			
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _{CE} = 6 A, V _{GE} = 4 V, T _J = 25°C		-	1.37	1.8	٧
Collector to Emitter Saturation Voltage	V _{CE(SAT)}	I _{CE} = 10 A, V _{GE} = 4.5 V, T _J = 150°C		-	1.77	2.2	V
DYNAMIC CHARACTERISTICS							
Gate Charge	$Q_{G(ON)}$	I _{CE} = 10 A, V _{CE} = 12 V, V _{GE} = 5 V		_	15.1	_	nC
Gate to Emitter Threshold	V _{GE(TH)}	I _{CE} = 1 mA, V _{CE} = V _{GE}	T _J = 25°C	1.3	-	2.2	V
Voltage			T _J = 150°C	0.75	-	1.8	
Gate to Emitter Plateau Voltage	V_{GEP}	V _{CE} = 12 V, I _{CE} = 12 A		-	3.1	-	V
SWITCHING CHARACTERISTIC	s			-			
Current Turn-On Delay	1.1	V_{CE} = 14 V, R_L = 1 Ω , V_{GE} = 5 V, R_G = 1 k Ω , T_J = 25°C		_	0.61	_	μS
Time-Resistive	td _{(ON)R}						
Time-Resistive Current Rise Time-Resistive	ta _{(ON)R}			-	2.17	-	
	. ,		$E = 5 \text{ V}, R_G = 1 \text{ k}\Omega,$	-	2.17 3.64	-	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

ORDERING INFORMATION

Device	Package	Shipping [†]
ISL9V2540S3ST-F085C	D ² PAK-3 (Pb-Free)	800 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

TYPICAL CHARACTERISTICS

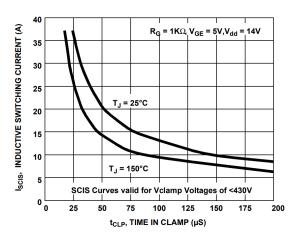


Figure 1. Self Clamped Inductive Switching Current vs. Time in Clamp

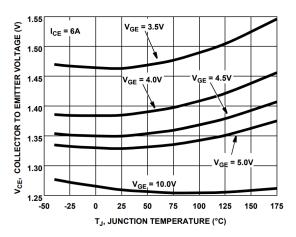


Figure 3. Collector to Emitter On–State Voltage vs. Junction Temperature

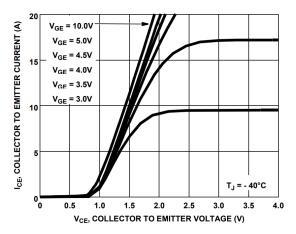


Figure 5. Collector to Emitter On-State Voltage vs. Collector Current

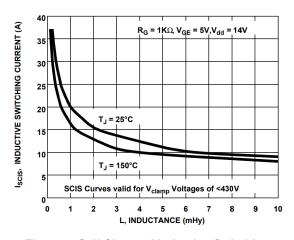


Figure 2. Self Clamped Inductive Switching Current vs. Inductance

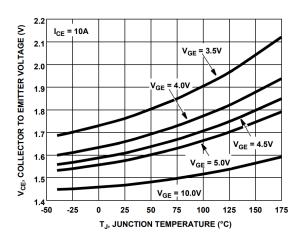


Figure 4. Collector to Emitter On-State Voltage vs. Junction Temperature

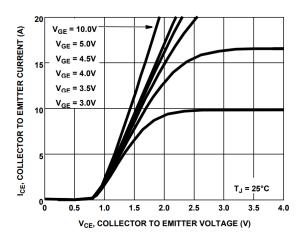


Figure 6. Collector to Emitter On- State Voltage vs. Collector Current

TYPICAL CHARACTERISTICS (Continued)

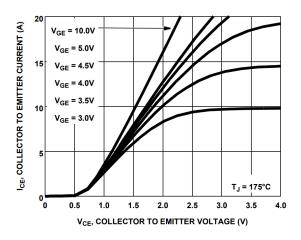


Figure 7. Collector to Emitter On-State Voltage vs. Collector Current

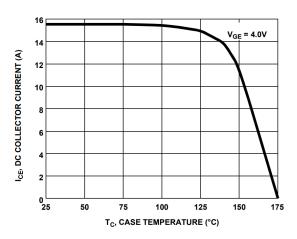


Figure 9. DC Collector Current vs. Case Temperature

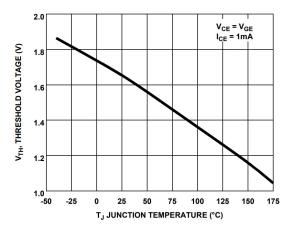


Figure 11. Threshold Voltage vs. Junction Temperature

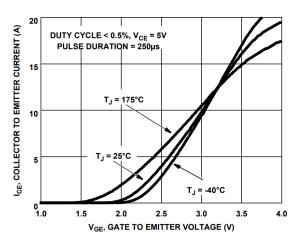


Figure 8. Transfer Characteristics

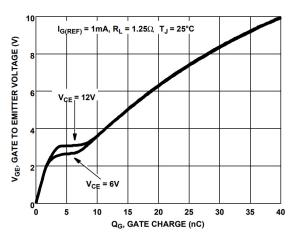


Figure 10. Gate Charge

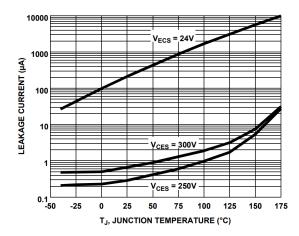
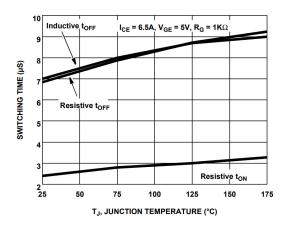


Figure 12. Leakage Current vs. Junction Temperature

TYPICAL CHARACTERISTICS (Continued)



1500
FREQUENCY = 1 MHz

1250
CIES

COES

0 5 10 15 20 25

VCE, COLLECTOR TO EMITTER VOLTAGE (V)

Figure 13. Switching Time vs. Junction Temperature

Figure 14. Capacitance vs. Collector to Emitter Voltage

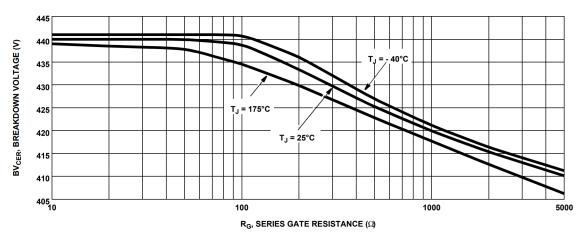


Figure 15. Break down Voltage vs. Series Resistance

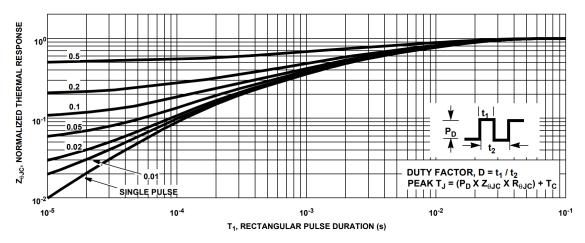
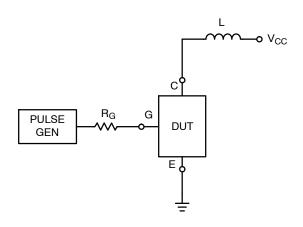


Figure 16. IGBT Normalized Transient Thermal Impedance, Junction to Case

TEST CIRCUIT AND WAVEFORMS



 $R_{G} = 1 \text{ k}\Omega$ $R_{G} = 1 \text{ k}\Omega$

Figure 17. Inductive Switching Test Circuit

Figure 18. t_{ON} and t_{OFF} Switching Test Circuit

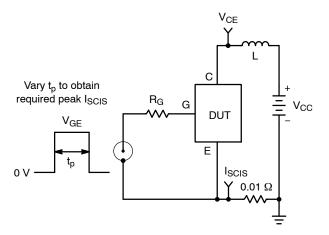


Figure 19. Energy Test Circuit

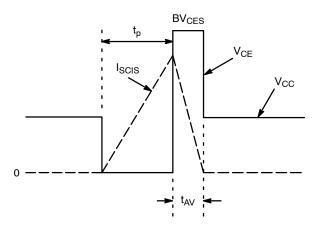


Figure 20. Energy Waveforms

PACKAGE DIMENSIONS

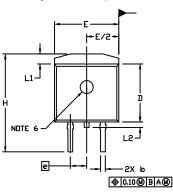
D²PAK-3 (TO-263, 3-LEAD) CASE 418AJ

ISSUE E

NOTES

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: INCHES
- 3. CHAMFER OPTIONAL.
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.005 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE DUTERMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
- 5. THERMAL PAD CONTOUR IS OPTIONAL WITHIN DIMENSIONS E, L1, D1, AND E1.
- 6. OPTIONAL MOLD FEATURE.
- 7. ①,② ... OPTIONAL CONSTRUCTION FEATURE CALL DUTS.

	INCHES		MILLIN	ETERS
DIM	MIN.	MAX.	MIN.	MAX.
Α	0.160	0.190	4.06	4.83
A1	0.000	0.010	0.00	0.25
b	0.020	0.039	0.51	0.99
c	0.012	0.029	0.30	0.74
c2	0.045	0.065	1.14	1.65
D	0.330	0.380	8.38	9.65
D1	0.260		6.60	i
E	0.380	0.420	9.65	10.67
E1	0.245		6.22	-
e	0.100 BSC		2.54 BSC	
Н	0.575	0.625	14.60	15.88
L	0.070	0.110	1.78	2.79
L1		0.066	-	1.68
L2		0.070		1.78
L3	0.010 BSC		0.25 BSC	
М	-8*	8*	-8*	8•



RECOMMENDED MOUNTING FOOTPRINT

0.436

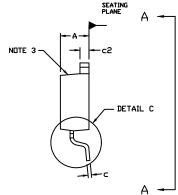
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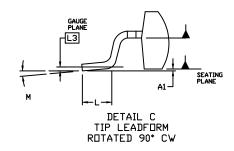
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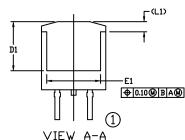
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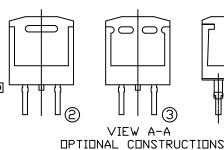
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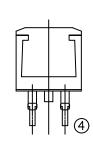
0.100 PITCH











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